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NTE7029 Integrated Circuit Module – AF Power Amp, 2–Channel, 100W Min

Features:

- Built–In Muting Circuit Reduces Pop On Noises

Absolute Maximum Ratings: ($T_A = +25^\circ\text{C}$ unless otherwise specified)

Maximum Supply Voltage, V_{CCmax} $\pm 75\text{V}$
 Thermal Resistance, Junction–to–Case, R_{thJC} 1.1°C/W
 Junction Temperature, T_J $+150^\circ\text{C}$
 Operating Case Temperature, T_C $+125^\circ\text{C}$
 Storage Temperature, T_{stg} -30° to $+125^\circ\text{C}$
 Available Time for Shorted Load ($V_{CC} = \pm 51.0\text{V}$, $R_L = 8\Omega$, $f = 50\text{Hz}$, $P_O = 100\text{W}$), t_s 1sec

Recommended Operating Conditions: ($T_A = +25^\circ\text{C}$ unless otherwise specified)

Operating Voltage, V_{CC} $\pm 51.0\text{V}$
 Load Resistance, R_L 8Ω

Operating Characteristics: ($T_A = +25^\circ\text{C}$, $V_{CC} = \pm 51.0\text{V}$, $R_L = 8\Omega$, $R_g = 600\Omega$, $V_G = 40\text{dB}$,
 R_L : Non–Inductive Load unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Quiescent Current	I_{CCO}	$V_{CC} = \pm 60\text{V}$	20	40	100	mA
Output Power	P_O	THD = 0.4%, $f = 20\text{Hz}$ to 20kHz	100	–	—	W
Total Harmonic Distortion	THD	$P_O = 1.0\text{W}$, $f = 1\text{kHz}$	–	–	0.3	%
Frequency Response	f	$P_O = 1.0\text{W}$, +0dB, –3dB	20 to 50k			Hz
Input Resistance	r_i	$P_O = 1.0\text{W}$, $f = 1\text{kHz}$	–	55	–	k Ω
Output Noise Voltage	V_{NO}	$V_{CC} = \pm 60\text{V}$, $R_g = 10\text{k}\Omega$	–	–	1.2	mVrms
Midpoint Voltage	V_N	$V_{CC} = \pm 60\text{V}$	–70	0	+70	mV
Muting Voltage	V_M		–2	–5	–10	V

Pin Connection Diagram (Front View)

22	N.C.
21	N.C.
20	(-) Input Rt Ch
19	(+) Input Rt Ch
18	GND
17	Compensation
16	(-) V _{CC}
15	Output Rt Ch
14	Bypass
13	(+) V _{CC}
12	Output Lt Ch
11	(-) V _{CC}
10	Compensation
9	Compensation
8	Muting
7	Compensation
6	Compensation
5	Compensation
4	(-) Input Lt Ch
3	(+) Input Lt Ch
2	N.C.
1	N.C.

