ELECTRONICS, INC.
44 FARRAND STREET
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NTE1818
Integrated Circuit
Module, AF PO, 25W/Ch,
Dual Power Supply

Features:
- Muting circuit to cut off pop noise
- Greatly reduced heat sink due to case temperature +125°C guaranteed

Absolute Maximum Ratings: (T_A = +25°C unless otherwise specified)

Maximum Supply Voltage, $V_{CC\ max}$ . . . . . ±39V
Thermal Resistance, Junction–to–Case, R_{thJC} . . . . . 2.6°C/W
Junction Temperature, T_J . . . . . 150°C
Operating Case Temperature, T_C . . . . . 125°C
Storage Temperature Range, T_{stg} . . . . . –30° to +125°C
Available Time for Load Shorted (V_{CC} = ±26V, R_L = 8Ω, f = 50Hz, P_O = 25W), t_s 2sec

Recommended Operating Conditions: (T_A = +25°C unless otherwise specified)

Recommended Operating Voltage, V_{CC} . . . . . ±26V
Load Resistance, R_L 8Ω

Operating Characteristics: (T_A = +25°C, V_{CC} = ±26V, R_L = 8Ω, R_g = 600Ω, VG = 40dB, R_L: non–inductive load, unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Quiescent Current	I_{CCO}	V_{CC} = ±31V	20	40	100	mA
Output Power	$P_{O(1)}$	THD – 0.4%, f = 20Hz ~ 20kHz	25	–	–	W
	$P_{O(2)}$	V_{CC} = ±22V, THD = 1.0%	25	–	–	W
Total Harmonic Distortion	THD	P_O = 1.0W, f = 1kHz	–	–	0.3	%
Frequency Response	f_L, f_H	P_O = 1.0W, –3dB	20 to 50k			Hz
Input Resistance	ri	P_O = 1.0W, f = 1kHz	–	55	–	kΩ
Output Noise Voltage	V_{NO}	V_{CC} = ±31V. R_g = 10kΩ	–	–	1.2	mV_{rms}
Midpoint Voltage	V_N	V_{CC} = ±31V	–70	0	70	mV
Muting Voltage	V_M		–2	–5	–10	V

Pin Connection Diagram