

Signal Transistors

MPS-L01, MPS-L51

T-29-27

Silicon Transistors

TO-92

The GE/RCA MPS-L01 NPN type and the MPS-L51 PNP type are planar epitaxial silicon transistors designed for general-purpose, high-voltage amplifier applications. PNP values are negative; observe proper polarity. These types are supplied in JEDEC TO-92 package.

MAXIMUM RATINGS, Absolute-Maximum Values:

	MPS-L01	MPS-L51	
COLLECTOR TO EMITTER VOLTAGE (V_{CEO})	120	100	V
EMITTER TO BASE VOLTAGE (V_{EB0})	5	4	V
COLLECTOR TO BASE VOLTAGE (V_{CBO})	140	100	V
CONTINUOUS COLLECTOR CURRENT (I_C)	150	600	mA
TOTAL POWER DISSIPATION $T_C \leq 25^\circ\text{C}$ (P_T)	1500	1500	mW
TOTAL POWER DISSIPATION $T_A \leq 25^\circ\text{C}$ (P_T)	625	625	mW
DERATE FACTOR, $T_C > 25^\circ\text{C}$	12	12	$\text{mW}/^\circ\text{C}$
DERATE FACTOR, $T_A > 25^\circ\text{C}$	5	5	$\text{mW}/^\circ\text{C}$
OPERATING TEMPERATURE (T_J)	-55°C to $+150^\circ\text{C}$		$^\circ\text{C}$
STORAGE TEMPERATURE (T_{STO})	-65°C to $+150^\circ\text{C}$		$^\circ\text{C}$
LEAD TEMPERATURE $1/16'' \pm 1/32''$ ($1.58\text{mm} \pm 0.8\text{mm}$) from case at 10s max. (T_L)	$+260^\circ\text{C}$		$^\circ\text{C}$

File Number **2071**

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MPS-L01, MPS-L51**T-29-27**ELECTRICAL CHARACTERISTICS, At Ambient Temperature ($T_A = 25^\circ\text{C}$ Unless Otherwise Specified)

CHARACTERISTICS	SYMBOL	LIMITS				UNITS	
		MPS-L01		MPS-L51			
		MIN.	MAX.	MIN.	MAX.		
Collector-Emitter Breakdown Voltage ($I_C = 1\text{mA}, I_B = 0$)*	BV_{CEO}	120	—	100	—	V	
Collector-Base Breakdown Voltage ($I_C = 100\mu\text{A}, I_E = 0$)	BV_{CBO}	140	—	100	—		
Emitter-Base Breakdown Voltage ($I_E = 10\mu\text{A}, I_C = 0$)	BV_{EBO}	5	—	4	—		
DC Forward Current Transfer Ratio ($I_C = 50\text{mA}, V_{CE} = 5\text{V}$) ($I_C = 10\text{mA}, V_{CE} = 5\text{V}$)	h_{FE}	—	—	40	250	—	
		50	300	—	—		
		—	—	0.2	0.25		
Collector Saturation Voltage ($I_C = 10\text{mA}, I_B = 1\text{mA}$) ($I_C = 50\text{mA}, I_B = 5\text{mA}$)	$V_{CE(\text{SAT})}$	—	0.3	—	0.3	V	
Base-Emitter Saturation Voltage ($I_C = 10\text{mA}, I_B = 1\text{mA}$) ($I_C = 50\text{mA}, I_B = 5\text{mA}$)	$V_{BE(\text{SAT})}$	—	1.2	—	1.2		
		—	1.4	—	1.2		
Gain-Bandwidth Product ($I_C = 10\text{mA}, V_{CE} = 10\text{V}, f = 100\text{MHz}$)	f_T	60	—	60	—	MHz	
Output Capacitance ($V_{CB} = 10\text{V}, I_E = 0, f = 1\text{MHz}$)	C_{ob}	—	8	—	8	pF	

*Pulse condition: 300μs pulse width, 2% duty cycle.

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TERMINAL CONNECTIONS

- Lead 1 - Emitter
- Lead 2 - Base
- Lead 3 - Collector