## ML5954 4-channel BTL driver for CD-ROM Driver & DVD Player

#### \* Application

- ◆ CD-ROM Driver
- ♦ DVD player

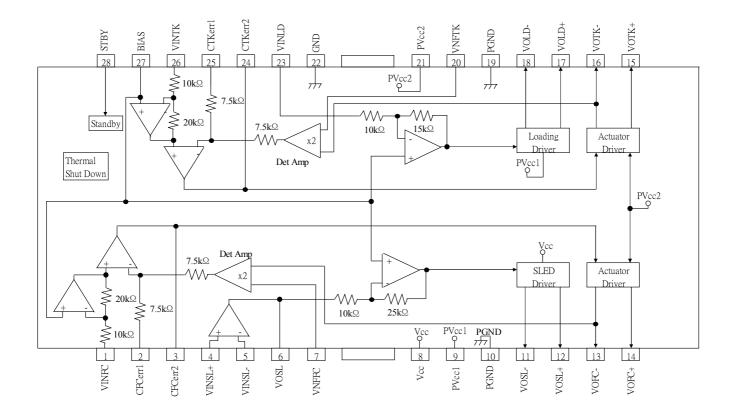
#### \* Features

- Wide Dynamic Range (4.0V(Typ.) When  $V_{CC}=12V$ ,  $PV_{CC}=5V$  and  $R_L=8\Omega$ )
- Level Shift Circuit on-chip
- Thermal Shutdown Circuit on-chip
- Internal Standby Mode.
- HSOP-28 package

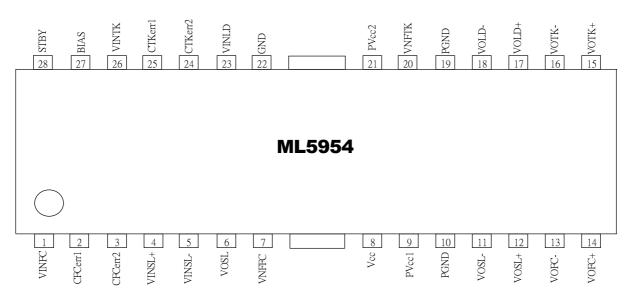
## \* General Description

The ML5954 is a four-channel BTL driver IC for driving the motors and actuators in products such as CD-ROM and DVD. Two channels use current feedback to minimize the current phase shift caused by the influence of load inductance. The IC is used in CD-ROM drivers and DVD players.

## \* Block Diagram



## \* Pin Configuration



# \* Pin Description

Pin	Pin Name	Function	Pin	Pin Name	Function
1	VINFC	Focus driver input	15	VOTK+	Tracking driver output (+)
2	CFCerr1	For connection of capacitor for the error amp filter	16	VOTK-	Tracking driver output (-)
3	CFCerr2	For connection of capacitor for the error amp filter	17	VOLD+	Loading driver output (+)
4	VINSL+	Op Amp input (+) for the sled driver	18	VOLD-	Loading driver output (-)
5	VINSL-	Op Amp input (-) for the sled driver	19	PGND	Ground for actuator driver block
6	VOSL	Op Amp output for the sled driver	20	VNFTK	Tracking driver feedback pin
7	VNFFC	Focus driver feedback pin	21	PV <sub>CC2</sub>	Power supply voltage for actuator driver block
8	V <sub>CC</sub>	Supply voltage	22	GND	Ground
9	PV <sub>CC1</sub>	Power supply voltage for sled driver block	23	VINLD	Loading driver input
10	PGND	Ground for sled driver block	24	CTKerr2	For connection of capacitor for the error amp filter
11	VOSL-	Sled driver output (-)	25	CTKerr1	For connection of capacitor for the error amp filter
12	VOSL+	Sled driver output (+)	26	VINTK	Tracking driver input
13	VOFC-	Focus driver output (-)	27	BIAS	Bias input
14	VOFC+	Focus driver output (+)	28	STBY	Standby control

Note 1: The indicated polarity for the output pins is defined under the inputs are (+) polarity. The output H bridge power supply pins are  $PV_{CC2}$  for the focus & tracking channel,  $PV_{CC1}$  for the loading channel, and  $V_{CC}$  for the pre-block and sled channel. Always ensure that  $V_{CC} \leq PV_{CC1/2}$ .

## \* Absolute Maximum Ratings

Parameter	Symbol	Ratings	Units		
Supply Voltage	VCC, PV <sub>CC1/2</sub>	13.5	V		
Power Dissipation	Pd	$1.7 *^{1}$	W		
Storage Temperature	Tstg	$-55 \sim +150$	°C		

*Note 1 : Power dissipation is reduced by* 5mW *for each increase in*  $T_A$  *of 1*  $^{\circ}$ *C over 25*  $^{\circ}$ *C.* 

Note 2 : Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

## \* Recommended operating conditions

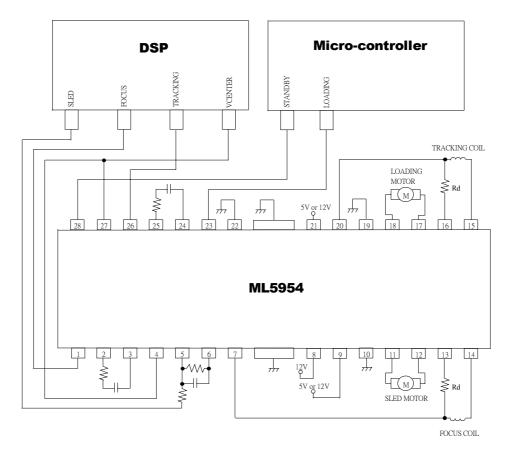
Parameter	Symbol	Ratings	Units
Operating Temperature	Topr	$0 \sim +70$	°C
	V <sub>CC</sub>	4.3 ~ 13.2	V
Supply Voltage	PV <sub>CC1</sub>	$4.3 \sim V_{CC}$	V
	PV <sub>CC2</sub>	$4.3 \sim V_{CC}$	V

# \* Electrical Characteristics

$(T - 25 \circ C U - 12 U D U - 12 U D U)$	-DV = -5VV = -25VD = -	-D -D -D -0  umlage	( athomysica analified )
$(T_A=25^{\circ}C, V_{CC}=12V, PV_{CC1}=$	$\mathbf{V} \cap \mathbf{V} \cap \mathbf{V} = \mathbf{J} \mathbf{V}$ , $\mathbf{V} \in \mathbf{V} \cap \mathbf{V} = \mathbf{Z}$ , $\mathbf{J} \mathbf{V}$ , $\mathbf{K} \in \mathbf{I}$	$-K_{12}-K_{12}-K_{14}-\delta_{22}$ . unless	s otherwise specified.)
(-A	$- \cdot CC_2 - \cdot $	L2L3L4,	· • • • • • • • • • • • • • • • • • • •

Parameter	Symbol	Conditions	Min	Тур	Max	Units			
Quiescent Current	I <sub>CC</sub>			18	27	mA			
Standby Current	I <sub>ST</sub>				0.5	mA			
Standby on Voltage	V <sub>STON</sub>	All channel on			0.5	V			
Standby off Voltage	V <sub>STOFF</sub>	All channel off	2.0			V			
Actuator driver									
Output Offset Current	I <sub>OO</sub>		-6		6	mA			
Maximum Output Amplitude	V <sub>OM</sub>	$V_{IN} = V_{BIAS} \pm 1.5 V$	3.6	4.0		V			
Transmission Gain	gm	$V_{IN} = V_{BIAS} \pm 0.2 V$	1.3	1.5	1.7	A/V			
Sled Motor Driver / Pre Op-Amp									
Common Input Voltage Range	V <sub>ICM</sub>		-0.3		11.0	V			
Input Bias Current	I <sub>IB</sub>			30	300	nA			
Output Low Level Voltage	V <sub>OL</sub>	No Load		0.8	1.1	V			
Output Source Current	I <sub>SO</sub>		0.3	0.5		mA			
Output sink Current I <sub>SI</sub>	I <sub>SI</sub>		1			mA			
Sled Motor Driver									
Output Offset voltage	V <sub>OOFSL</sub>		-100	0	100	mV			
Maximum Output Voltage	V <sub>OMSL</sub>		7.5	9.0		V			
Closed-Loop Voltage Gain	G <sub>VSL</sub>	$V_{IN} = V_{BIAS} \pm 0.2 V$	18.0	20.0	22.0	dB			
Loading Motor Driver									
Output Offset Voltage	V <sub>OOFLD</sub>		-50	0	50	mV			
Maximum Output Voltage	V <sub>OMLD</sub>		3.6	4.0		V			
Closed-Loop	G <sub>VLD</sub>	$V_{IN} = V_{BIAS} \pm 0.2 V$	13.5	15.5	17.5	dB			
Gain Error by Polarity	$\Delta G_{VLD}$	$V_{IN} = V_{BIAS} \pm 0.2 V$	0	1	2	dB			

## \* Application Circuit



#### **Precautions for use**

- (1) This IC has a built-in thermal shutdown circuit that mutes the output current when the chip temperature reached 175°C (Typ). The hysteresis is set to 25 °C (Typ.), so the driver circuits start up again when the chip temperature falls to 150 °C (Typ.).
- (2) The driver buffer is switched off when the supply voltage falls below 3.5V (Typ.), and is switched back on when the voltage reaches 3.7V (Typ.) again.
- (3) In case standby pin voltage under 1.4V or opened, quiescent current is muted. Standby pin voltage should be over 1.8V for normal application.
- (4) Insert the by-pass capacitor between  $V_{CC}$ -pin and GND-pin of the IC as near as possible (approximately 0.1uF)
- (5) Heat dissipation fins are attached to the GND on the inside of the package. Make sure to connect these to the external GND.

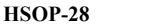
Supplement: (Current-feedback driver)

The transmission gain (output current/input voltage) is given by :

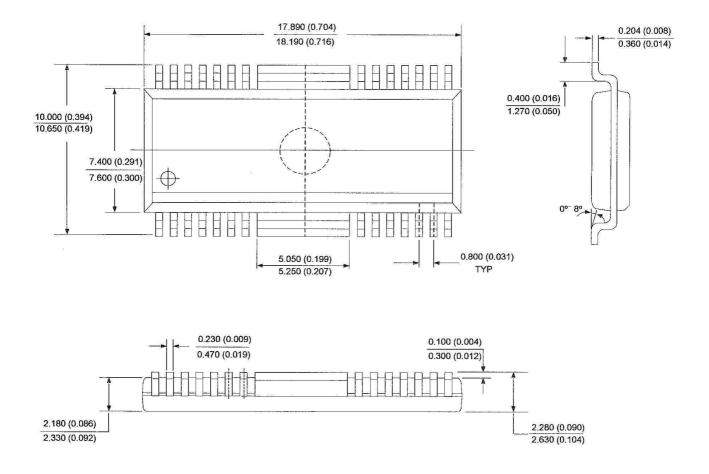
$$gm = \frac{1}{(Rd + R_{WIRE})} (A/V)$$

,where  $R_{WIRE}$  is the total gold wire resistance inside the package (0.15 $\Omega \pm 0.05\Omega$ ) (Typ.).

## \* Mechanical Dimensions



Unit: mm (inch)



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