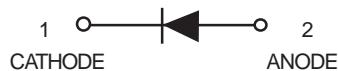
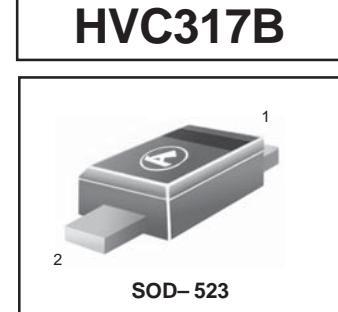


Variable Capacitance Diode for Tuner

HVC317B

FEATURES

- High capacitance ratio. ($n = 13.0 \text{ min}$)
- Ultra small Flat Package (UFP) is suitable for surface mount design.



DEVICE MARKING

HVC317B = A5

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$)

| Item | Symbol | Value | Unit |
|----------------------|-----------|--------------|------------------|
| Reverse voltage | V_R | 35 | V |
| Junction temperature | T_j | 125 | $^\circ\text{C}$ |
| Storage temperature | T_{stg} | - 55 to +125 | $^\circ\text{C}$ |

 Notes 1. $R_L=10\text{k}\Omega$

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$)

| Item | Symbol | Min | Typ | Max | Unit | Test Condition |
|-------------------|-------------------|------|-----|------|----------|---|
| Reverse current | I_{R1} | — | — | 10 | nA | $V_R = 30\text{V}$ |
| | I_{R2} | — | — | 100 | | $V_R = 30\text{V}, T_A = 60^\circ\text{C}$ |
| Capacitance | C_1 | 9.00 | — | 11.5 | pF | $V_R = 1\text{V}, f = 1 \text{ MHz}$ |
| | C_{25} | 0.60 | — | 0.80 | | $V_R = 25\text{V}, f = 1 \text{ MHz}$ |
| Capacitance ratio | n | 13.0 | — | — | — | C_1 / C_{25} |
| Series resistance | r_s | — | — | 1.6 | Ω | $V_R = 5\text{V}, f = 470 \text{ MHz}$ |
| Matching error | $\Delta C/C^{*1}$ | — | — | 6.0 | % | $V_R = 1 \text{ to } 25\text{V}, f = 1 \text{ MHz}$ |

Notes 1. C.C system (Continuous Connected taping system) enable to make any 10 pcs of $\Delta C/C$ continuous in a reel , expect extention to another group.

Calculate Matching Error,

$$\Delta C/C = \frac{(C_{\max} - C_{\min})}{C_{\min}} \times 100 (\%)$$

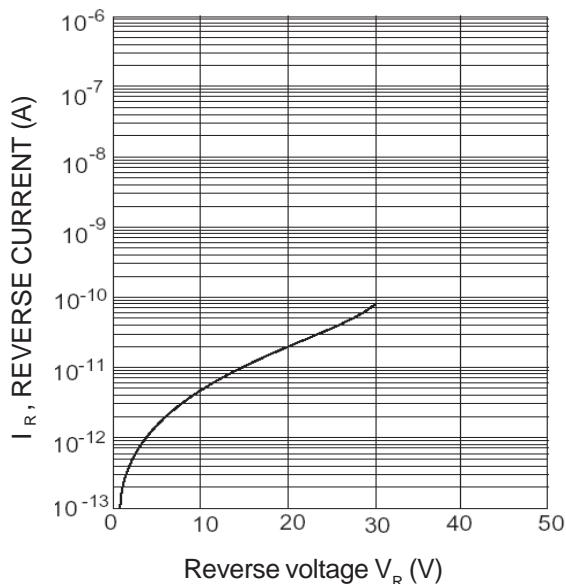
HVC317B


Fig.1 Reverse current Vs. Reverse voltage

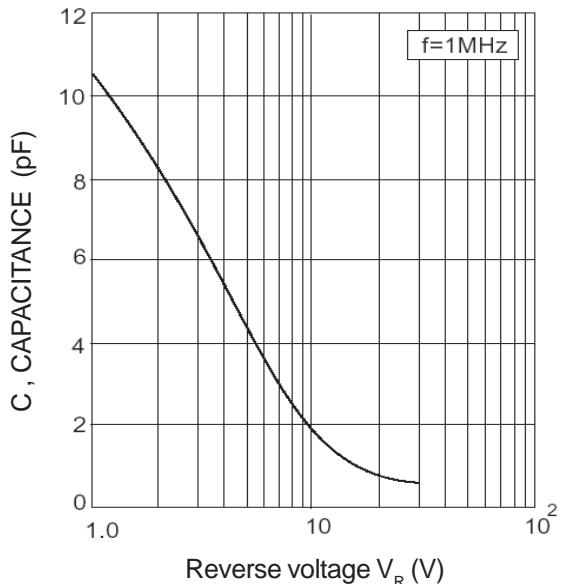


Fig.2 Capacitance Vs. Reverse voltage

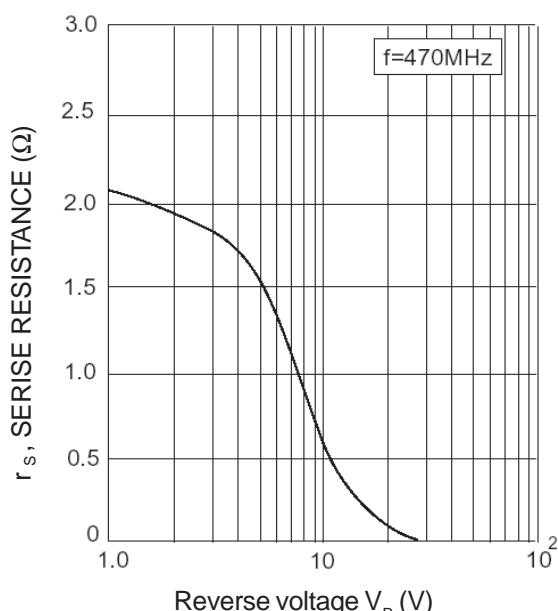


Fig.3 Series resistance Vs. Reverse voltage

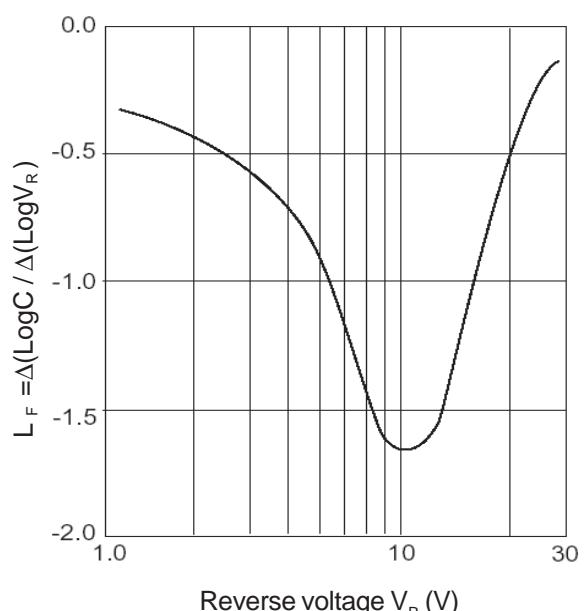


Fig.4 Linearity factor Vs. Reverse voltage