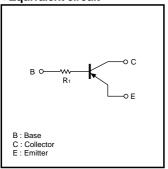
# Digital transistors (built in resistor)

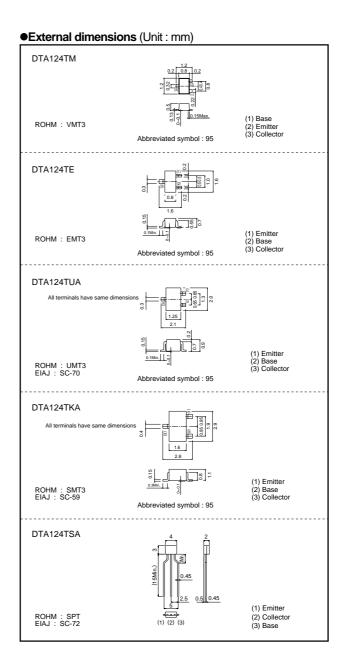
# DTA124TM / DTA124TE / DTA124TUA / DTA124TKA / DTA124TSA

### Features

- Built-in circuit enables the configuration of an inverter circuit without connecting external input resistors. (see equivalent circuit).
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on / off conditions need to be set for operation, making device design easy.

## ●Equivalent circuit





# ●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol		Unit				
		М	Е	UA	KA	SA	Offic
Collector-base voltage	Vсво	-50					V
Collector-emitter voltage	VCEO		V				
Emitter-base voltage	VEBO		V				
Collector current	lc	-100					mA
Collector power dissipation	Pc	1	50	2	00	300	mW
Junction temperature	Tj	150					°C
Storage temperature	Tstg		°C				

# ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	-50	-	-	V	Ic=-50μA
Collector-emitter breakdown voltage	BVceo	-50	-	-	V	Ic=-1mA
Emitter-base breakdown voltage	ВVево	-5	-	-	V	Iε=-50μA
Collector cutoff current	Ісво	-	-	-0.5	μΑ	Vcb=-50V
Emitter cutoff current	ІЕВО	-	-	-0.5	μΑ	V <sub>EB</sub> =-4V
Collector-emitter saturation voltage	VCE(sat)	-	-	-0.3	V	Ic/Iв=-10mA/-1mA
DC current transfer ratio	hfe	100	250	600	-	Vce=-5V, Ic=-1mA
Input resistance	R <sub>1</sub>	15.4	22	28.6	kΩ	_
Transition frequency	f⊤	-	250	-	MHz	Vce=-10V, Ie=5mA, f=100MHz *

<sup>\*</sup> Transition frequency of the device

# Packaging specifications

	Package	VMT3	EMT3	UMT3	SMT3	SPT
	Package type	Taping	Taping	Taping	Taping	Taping
	Code	T2L	TL	T106	T146	TP
Part No.	Basic ordering unit (pieces)	8000	3000	3000	3000	5000
DTA124TM		0	-	-	-	-
DTA124TE		-	0	-	-	-
DTA124TU	4	-	-	0	-	-
DTA124TKA	4	-	_	_	0	-
DTA124TSA	4	-	-	-	-	0

# •Electrical characteristic curves

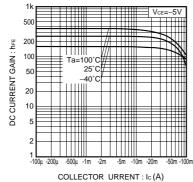


Fig.1 DC current gain vs. collector current

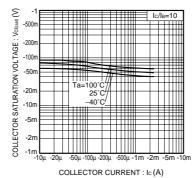


Fig.2 Collector-emitter saturation voltage vs. collector current



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