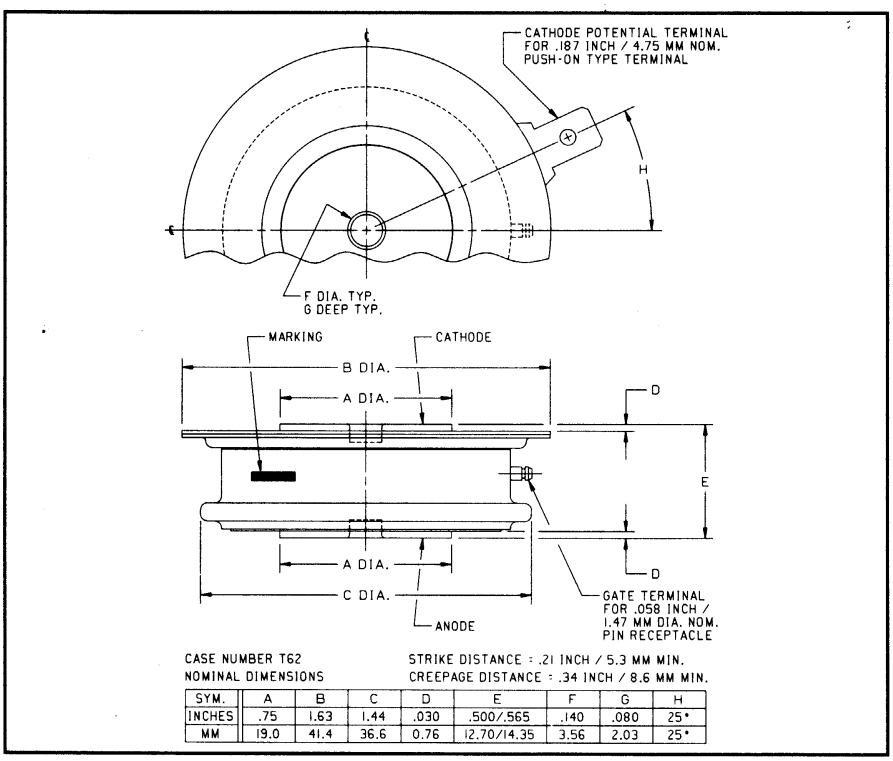
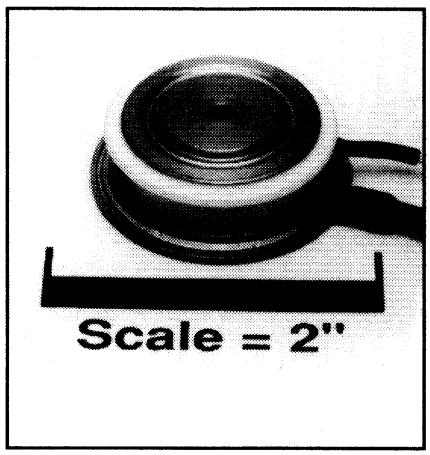
Powerex, Inc., 200 Hillis Street, Youngwood, Pennsylvania 15697-1800 (412) 925-7272
Powerex, Europe, S.A. 428 Avenue G. Durand, BP107, 72003 Le Mans, France (43) 41.14.14

Phase Control SCR
115 Amperes Average
1600 Volts



SYM.	A	B	C	D	E	F	G	H
INCHES	.75	1.63	1.44	.030	.500/.565	.140	.080	25°
MM	19.0	41.4	36.6	0.76	12.70/14.35	3.56	2.03	25°

C350 (Outline Drawing)



C350 Phase Control SCR
115 Amperes Average, 1600 Volts

Description:
Powerex Silicon Controlled Rectifiers (SCR) are designed for phase control applications. These are all-diffused, Press-Pak (Pow-R-Disc) devices employing the field-proven amplifying (di/namic) gate.

Features:
- ☐ Low On-State Voltage
- ☐ High di/dt
- ☐ High dv/dt
- ☐ Hermetic Packaging
- ☐ Excellent Surge and I^2t Ratings

Applications:
- ☐ Power Supplies
- ☐ Battery Chargers
- ☐ Motor Control

Ordering Information:
Select the complete five or six digit part number you desire from the table, i.e. C350PM is a 1600 Volt, 115 Ampere Phase Control SCR.

Type	Voltage V_{DRM} V_{RRM}	Code	Current $I_{T(av)}$
C350	600	M	115
	800	N	
	1000	P	
	1200	PB	
	1400	PD	
	1600	PM	

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Absolute Maximum Ratings

	Symbol	C350	Units
RMS On-State Current @ T_C = 89°C	$I_{T(RMS)}$	180	Amperes
Average On-State Current @ T_C = 89°C	$I_{T(av)}$	115	Amperes
Peak One-Cycle Surge (Non-Repetitive) On-State Current (60Hz)	I_{TSM}	1600	Amperes
Peak One-Cycle Surge (Non-Repetitive) On-State Current (50Hz)	I_{TSM}	1480	Amperes
Critical Rate-of-Rise of On-State Current (Non-Repetitive)	di/dt	800	Amperes/μs
Critical Rate-of-Rise of On-State Current (Repetitive)	di/dt	500	Amperes/μs
I^2t (for Fusing), 8.3 milliseconds	I^2t	10,600	A^2sec
Peak Gate Power Dissipation	P_{GM}	10	Watts
Average Gate Power Dissipation	$P_{G(av)}$	2	Watts
Storage Temperature	T_{STG}	−40 to 150	°C
Operating Temperature	T_J	−40 to 125	°C
Mounting Force		720 to 880	lb.
Mounting Force		3.20 to 3.92	kN

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Electrical and Thermal Characteristics

Characteristics	Symbol	Test Conditions	C350	Units
Voltage—Blocking State Maximums				
Forward Leakage, Peak	I_{DRM}	T_J = 125°C, V = V_{DRM}	20	mA
Reverse Leakage, Peak	I_{RRM}	T_J = 125°C, V = V_{RRM}	20	mA
Current—Conducting State Maximums				
Peak On-State Voltage	V_{TM}	I_{TM} = 500A Peak, T_C = 25°C, Duty Cycle ≤0.01%	2.6	Volts
Switching				
Typical Turn-Off Time	t_q	T_J = 125°C; I_{TM} = 50 Amps Peak; V_R = 50 Volts Min.; V_{DRM} = Rated (Reapplied); Rate-of-Rise of Reapplied Off-State Voltage = 20V/µsec (Linear); Gate Bias = 0 Volts, 100Ω during Turn-Off Interval; Duty Cycle ≤0.01%	200	µsec
Typical Delay Time	t_d	T_C = 25°C, I_{TM} = 50 Adc, V_{DRM} = Rated, Gate Supply: 10 Volt Open Circuit, 20 Ohm, 0.1 µsec max. rise time	1.0	µsec
Min. Critical dv/dt exponential to V_{DRM}	dv/dt	T_J = 125°C, Gate Open	200	V/µsec
Thermal				
Maximum Thermal Resistance, double sided cooling				
Junction to Case	$R_{\theta JC}$		0.135	°C/Watt
Case to Sink, Lubricated	$R_{\theta CS}$		0.04	°C/Watt
Gate—Maximum Parameters				
Gate Current to Trigger	I_{GT}	V_D = 6V, T_C = 25°C, R_L = 3Ω	150	mA
Gate Voltage to Trigger	V_{GT}	V_D = 6V, R_L = 3Ω, T_J = −40°C to +120°C	3.0	Volts
Non-Triggering Gate Voltage	V_{GDM}	T_C = 120°C, Rated V_{DRM}, R_L = 1000Ω	0.15	Volts
Peak Forward Gate Current	I_{GTM}		10	Amperes
Peak Reverse Gate Voltage	V_{GRM}		5	Volts

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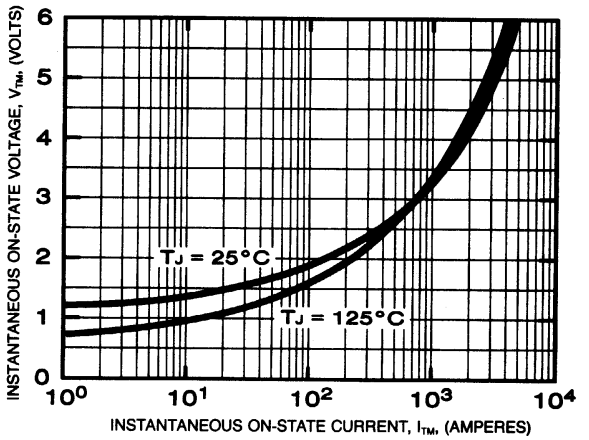
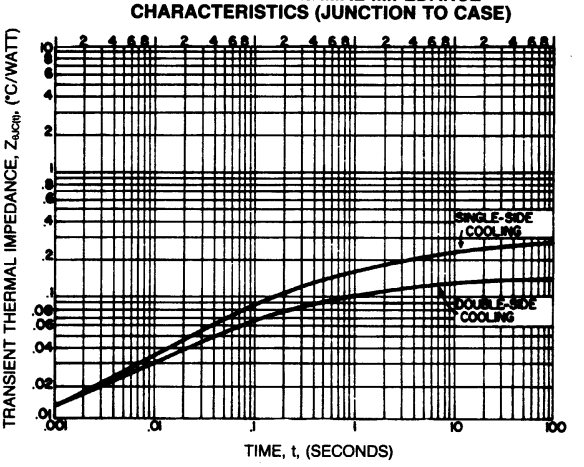
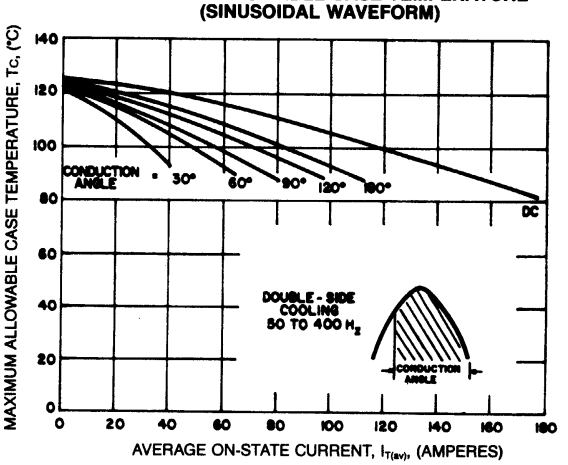
MAXIMUM ON-STATE CHARACTERISTICS



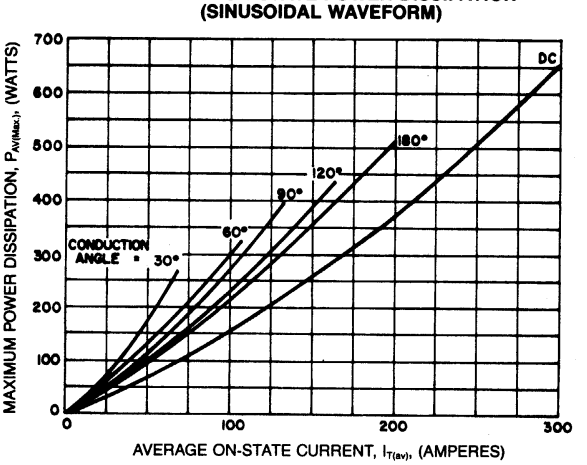
TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (JUNCTION TO CASE)



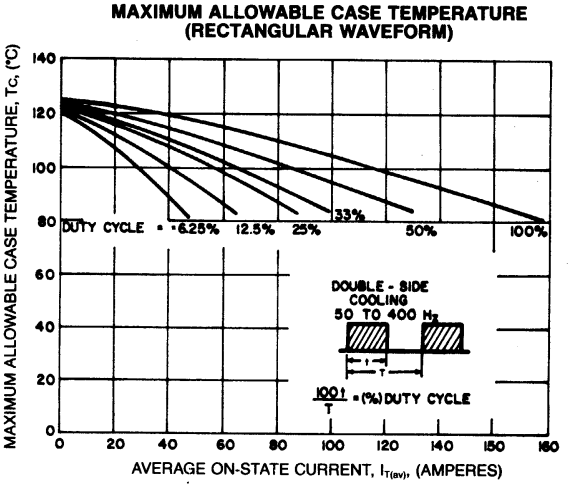
MAXIMUM ALLOWABLE CASE TEMPERATURE (SINUSOIDAL WAVEFORM)



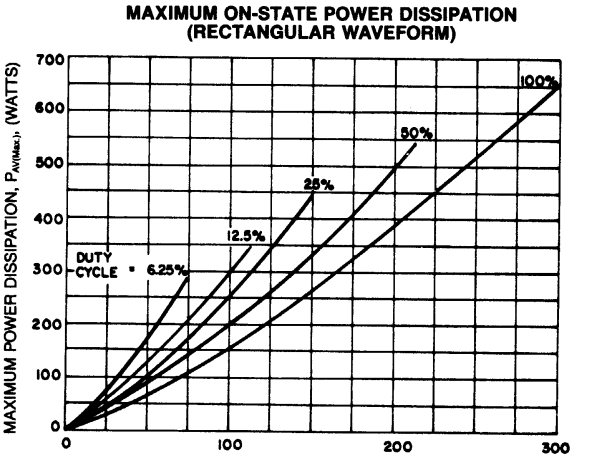
MAXIMUM ON-STATE POWER DISSIPATION (SINUSOIDAL WAVEFORM)



MAXIMUM ALLOWABLE CASE TEMPERATURE (RECTANGULAR WAVEFORM)



MAXIMUM ON-STATE POWER DISSIPATION (RECTANGULAR WAVEFORM)



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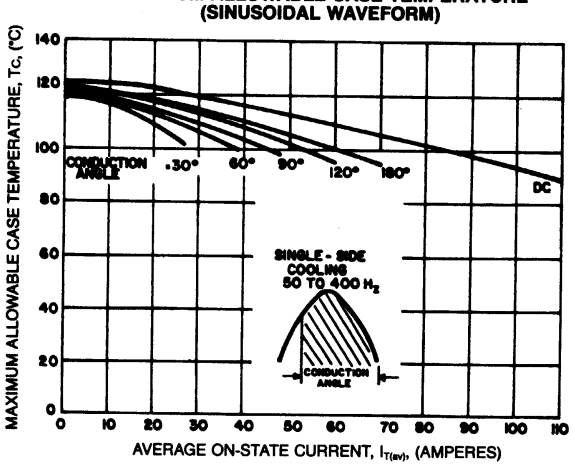
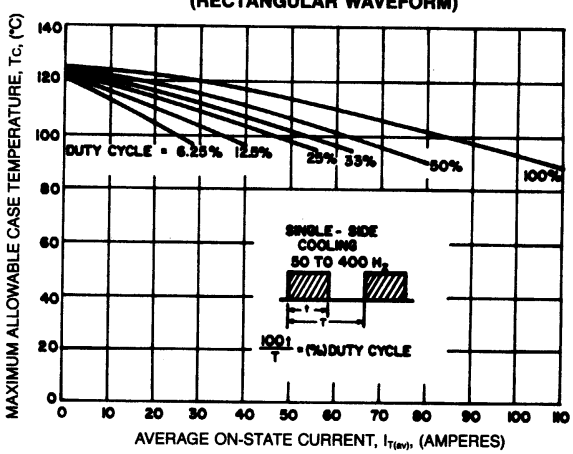
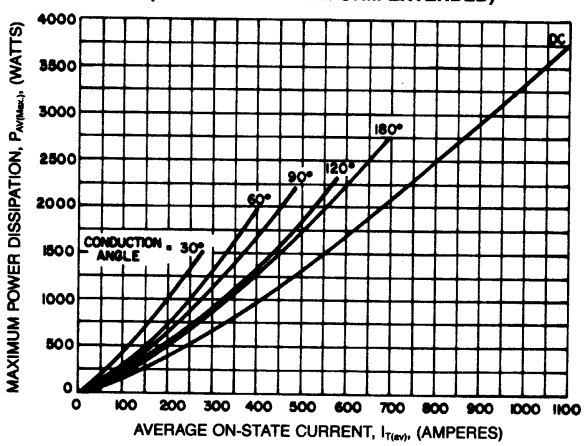
MAXIMUM ALLOWABLE CASE TEMPERATURE (SINUSOIDAL WAVEFORM)



MAXIMUM ALLOWABLE CASE TEMPERATURE (RECTANGULAR WAVEFORM)



MAXIMUM ON-STATE POWER DISSIPATION (SINUSOIDAL WAVEFORM EXTENDED)



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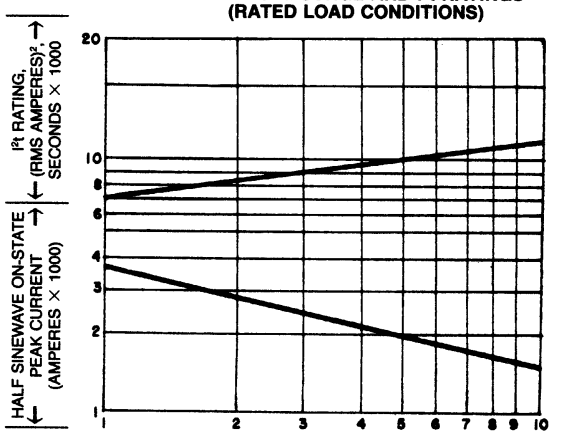
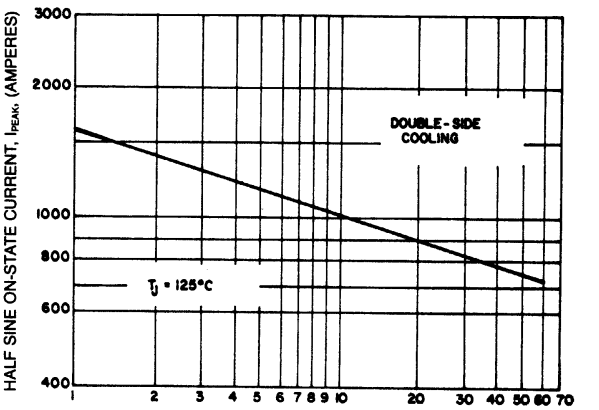
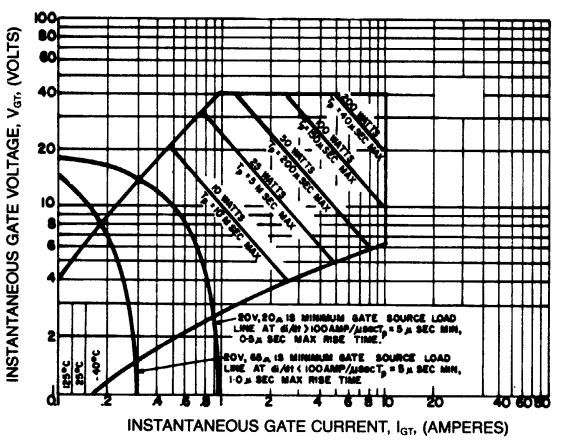
SUB-CYCLE SURGE AND I^2t RATINGS (RATED LOAD CONDITIONS)



MAXIMUM ALLOWABLE SURGE ON-STATE CURRENT (NON-REPETITIVE)



GATE CHARACTERISTICS



NOTES:
1. Maximum allowable gate power dissipation = 2 watts.
2. The locus of possible DC trigger points lie outside the boundaries shown at various case temperatures.
3. Tp = Rectangular Gate Current Pulse Width.