

Data Sheet B4069





SAW Components	B4069
Low-Loss Filter	770,0 MHz

Data Sheet

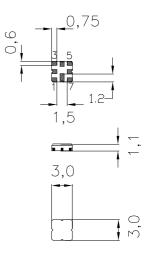
SMD ceramic package QCC8D

Features

- Low loss IF filter for HiperLAN
- Balanced to balanced operation
- Package for Surface Mounted Technology (SMT)

Terminals

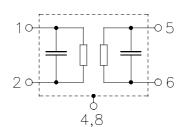
Ni, gold-plated



Dimensions in mm, approx. weight 0,037 g

Pin configuration

- 1 Input
- 2 Input or grounded input
- 5 Output
- 6 Output or grounded output
- 3, 7 To be grounded
- 4, 8 Case ground



Туре	Ordering code	Marking and Package	Packing		
		according to	according to		
B4069	B39771-B4069-U810	C61157-A7-A72	F61074-V8101-Z000		

Electrostatic Sensitive Device (ESD)

Maximum ratings

Operable temperature range	T	-20 /+ 80	°C	
Storage temperature range	$T_{\rm stg}$	- 40/+ 85	°C	
DC voltage	$V_{\rm DC}$	0	V	
Source power	P_{s}	0	dBm	source impedance 250 Ω



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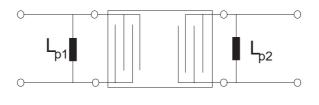
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Characteristics

Operating temperature range: $T_{\rm A} = -20 \ldots +80 \,^{\circ}{\rm C}$ Terminating source impedance: $Z_{\rm S} = 250 \, \Omega \, \| 125 \, {\rm nH}$ Terminating load impedance: $Z_{\rm L} = 250 \, \Omega \, \| 125 \, {\rm nH}$

			min.	typ.	max.	
Nominal frequency		f_{N}	_	770,0	_	MHz
Minimum insertion attenuation		α_{min}	_	1,7	3,5	dB
Amplitude ripple in passband (p-p)		Δα				
	$f_{\rm N} \pm 7.0~{\rm MHz}$		_	0,8	1,1	dB
	$f_{\rm N}$ ± 8,5 MHz		_	0,9	2,0	dB
Group delay ripple (p-p)		Δτ				
	$f_{\rm N}$ ± 8,5 MHz		_	25	50	ns
Relative Attenuation (relative to α_{min})		α_{rel}				
	f _N - 20,0 MHz	101	20	30	_	dB
	$f_{\rm N}$ + 20,0 MHz		15	23	_	dB
	<i>f</i> _N - 30,0 MHz		35	40	_	dB
	$f_{\rm N}$ + 30,0 MHz		25	32	_	dB
	$f_{N} \pm 40,0 \; MHz$		40	60	_	dB
	$f_{\rm N} \pm 60,0~{\rm MHz}$		45	63	_	dB
	$f_{\rm N}$ ± 80,0 MHz		45	68	_	dB
	<i>f</i> _N ±100,0 MHz		45	73	_	dB
	f _N ±120,0 MHz		50	70	_	dB

Matching network (Simulated)



 $L_{p1} = 125 nH$

 $L_{p2} = 125 nH$



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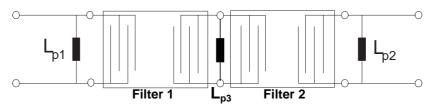
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Characteristics (2 Cascaded filters with || 125nH between filters)

Operating temperature range: $T_{\rm A} = -20 \ldots +80 \,^{\circ}{\rm C}$ Terminating source impedance: $Z_{\rm S} = 250 \, \Omega \, \| 125 \, {\rm nH}$ Terminating load impedance: $Z_{\rm L} = 250 \, \Omega \, \| 125 \, {\rm nH}$

			min.	typ.	max.	
Nominal frequency		f_{N}	_	770,0	_	MHz
Minimum insertion attenuation		α_{min}	_	3,5	7,0	dB
Amplitude ripple in passband (p-p)		Δα				
	$f_{\rm N} \pm 7,0~{\rm MHz}$		_	1,5	2,2	dB
	$f_{\rm N} \pm 8.5~{\rm MHz}$		_	1,8	4,0	dB
Group delay ripple (p-p)		Δτ				
	$f_{\rm N}$ ± 8,5 MHz			50	100	ns
Relative Attenuation (relative to α_{max})		α_{rel}				
	<i>f</i> _N - 20,0 MHz		45	54	<u> </u>	dB
	$f_{\rm N}$ + 20,0 MHz		30	48	<u> </u>	dB
	<i>f</i> _N - 30,0 MHz		70	78	_	dB
	$f_{\rm N}$ + 30,0 MHz		50	66	_	dB
	$f_{\rm N} \pm 40,0~{\rm MHz}$		80	116	_	dB
	$f_{\rm N}$ ± 60,0 MHz		90	125	_	dB
	$f_{\rm N}$ ± 80,0 MHz		90	136	_	dB
	<i>f</i> _N ±100,0 MHz		90	140	_	dB
	f _N ±120,0 MHz		100	135	_	dB

Matching network (Simulated)



 $L_{p1} = 125 nH$

 $L_{p2} = 125 nH$

 $L_{p3} = 125 nH$

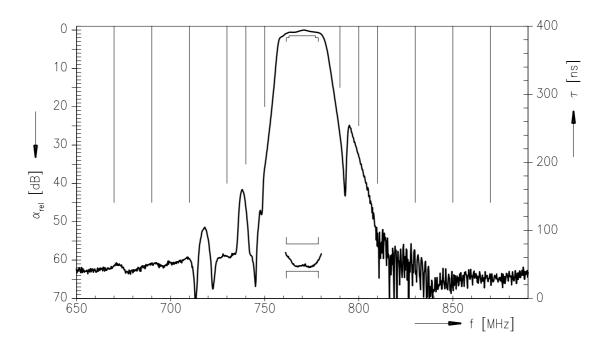


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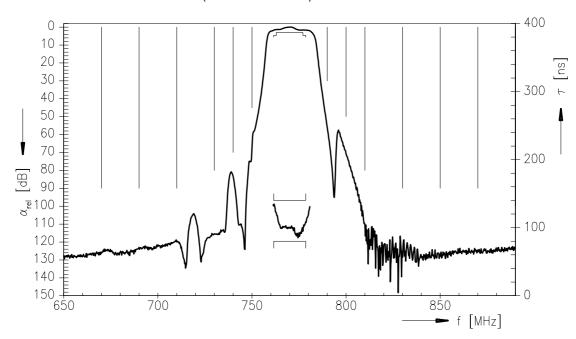
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Normalised Transfer Function (Single filter)



Normalised Transfer Function (2 Cascaded filters)



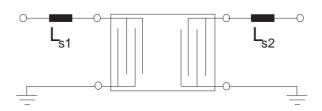


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Matching network (element values may depend on pcb layout)

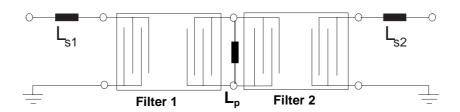
50 Ω unbalanced for single filter (test circuit for unbalanced input / output enviroment):



$$L_{s1} = 22nH$$

 $L_{s2} = 18nH$

50 Ω unbalanced for cascaded filters (test circuit for unbalance input/ output enviroment):



$$\begin{array}{l} L_{s1} = 22 nH \\ L_{s2} = 18 nH \\ L_{p} = 22 nH \end{array}$$



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