

Data Sheet B4955





B4955

Low-Loss Filter for Mobile Communication

183,6 MHz

Data Sheet



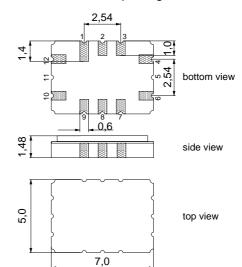
Features

- IF filter for mobile telephone
- Channel selection in CDMA systems
- Balanced or unbalanced
- High rejection, very small size
- Low amplitude ripple
- Package for Surface Mounted Technology (SMT)
- Filter surface passivated

Terminals

■ Ni, gold plated

SMD ceramic package QCC12C



Dimensions in mm, approx. weight 0,25 g

Pin configuration

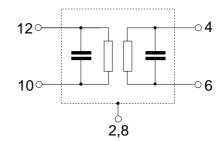
12 Input

10 Balanced input or ground

6 Output

Balanced output or ground

1, 2, 3, 7, 8, 9 To be grounded



Туре	Ordering code	Marking and Package	Packing		
		according to	according to		
B4955	B39181-B4955-H310	C61157-A7-A95	F61074-V8170-Z000		

Electrostatic Sensitive Device (ESD)

Maximum ratings

Operable temperature range	T	- 40/+ 85	°C
	_	40/ 05	• •
Storage temperature range	I _{stg}	– 40/+ 85	C
DC voltage	$V_{\rm DC}$	0	V
Source power	$P_{\rm s}$	10	dBm



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Characteristics

Specified temperature range: $T = -30^{\circ} \text{C} ... +85^{\circ} \text{C}$ Terminating source impedance: $Z_{\text{S}} = 300\Omega \parallel 44 \text{nH}$ Terminating load impedance: $Z_{\text{L}} = 675\Omega \parallel 54 \text{nH}$

	min.	typ.	max.	
Nominal frequency f _N	_	183,6	_	MHz
	min —	8,1	9,5	dB
Amplitude ripple $f_N - 0.3$ MHz $f_N + 0.3$ MHz	x	0,6	1,2	dB
Phase linearity (rms deviation) $f_{\rm N} - 0.615 {\rm MHz} \qquad f_{\rm N} + 0.615 {\rm MHz}$	_	1,3	2,8	۰
Relative attenuation (relative to α_{min}) α_{r} $f_{N} \pm 0,615 MHz$	el	4,0	5,5	dB
$f_{\rm N} - 0.9$ MHz $f_{\rm N} + 0.9$ MHz $f_{\rm N} - 1.25$ MHz $f_{\rm N} + 1.25$ MHz $f_{\rm N} - 1.7$ MHz $f_{\rm N} + 1.7$ MHz $f_{\rm N} \pm 2.05$ MHz $f_{\rm N} - 9.0$ MHz $f_{\rm N} - 1.25$ MHz $f_{\rm N} + 1.25$ MHz $f_{\rm N} + 9.0$ MHz	33 33 33 33 33 33 33 33	39 45 44 39 37 43 42 35 37	— — — — — — —	dB dB dB dB dB dB dB dB



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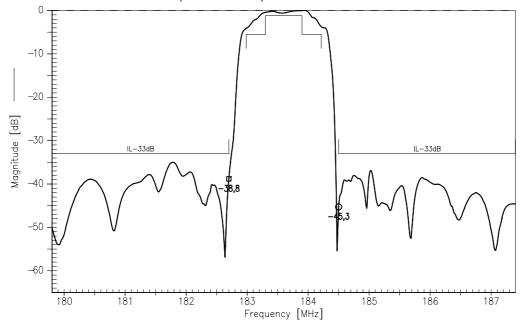
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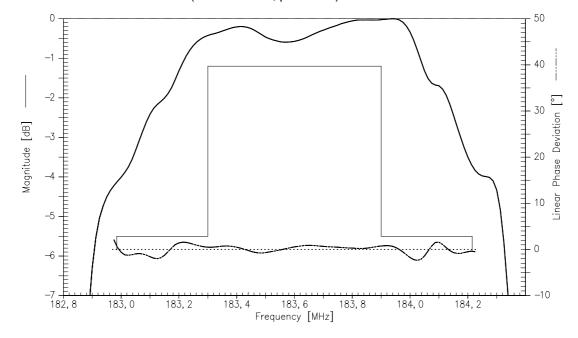
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Normalized transfer function (measurement):



Normalized transfer function (measurement, passband):





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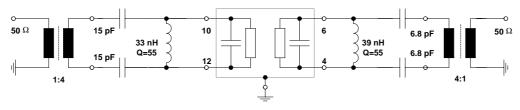
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Test matching network

(Element values depend on pcb layout)



pins 2,8 GND balanced input: pins 10,12 balanced output: pins 4,6 to be grounded: 1,3,7,9

Published by EPCOS AG Surface Acoustic Wave Components Division, SAW MC WT P.O. Box 80 17 09, 81617 Munich, GERMANY

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