



SAW Components

Data Sheet B4865

Data Sheet

An abstract, grayscale graphic featuring a stylized, three-dimensional representation of the EPCOS logo. The letters "EPCOS" are rendered in a bold, sans-serif font, appearing to be part of a larger, curved structure that resembles a globe or a stylized wave. The background is dark and textured, with light reflecting off the surfaces of the logo.



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B4865

Low Loss Filter for Mobile Communication

134,04 MHz

Data Sheet



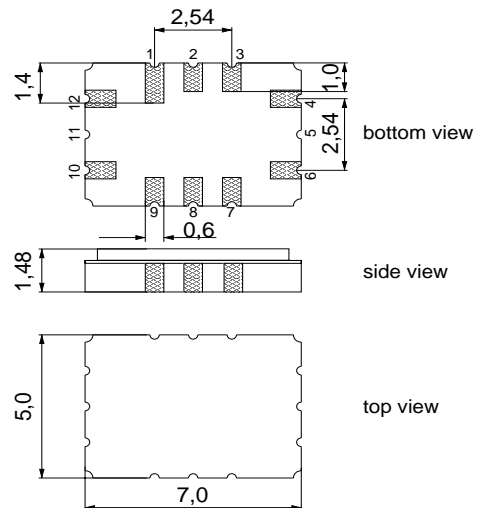
Features

- Low-loss IF filter for mobile telephone
- Channel selection in AMPS/TDMA systems
- Filter surface passivated
- Balanced or unbalanced operation possible
- Package for **Surface Mounted Technology (SMT)**

Terminals

- Ni, gold plated

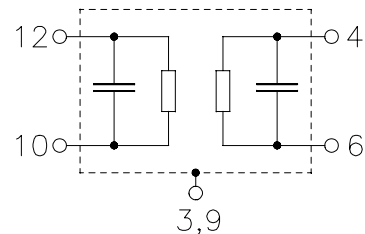
SMD ceramic package QCC12C



Dimensions in mm, approx. weight 0,25 g

Pin configuration

12	Input
6	Output
10	Balanced input or input ground
4	Balanced output or output ground
3,9	Case ground
1,2,7,8	Not connected



Type	Ordering code	Marking and Package according to	Packing according to
B4865	B39131-B4865-H310	C61157-A7-A95	F61074-V8132-Z000

Electrostatic Sensitive Device (ESD)

Maximum ratings

Operable temperature range	T	- 30/+ 85	°C
Storage temperature range	T_{stg}	- 40/+ 85	°C
DC voltage	V_{DC}	13	V
Source power	P_s	10	dBm



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Characteristics

Operating temperature range:	$T = -30^{\circ}\text{C} \dots 85^{\circ}\text{C}$
Terminating source impedance:	$Z_S = 850\ \Omega \parallel -0,8\ \text{pF}$
Terminating load impedance:	$Z_L = 850\ \Omega \parallel -0,8\ \text{pF}$

		min.	typ.	max.	
Nominal frequency	f_N	—	134,04	—	MHz
3 dB Bandwidth		—	57	—	kHz
Minimum insertion attenuation (including losses in the matching network)	α_{\min}	—	3,6	4,5	dB
Amplitude ripple (p-p)	$\Delta\alpha$				
$f_N - 10,0\ \text{kHz} \dots f_N + 10,0\ \text{kHz}$		—	0,2	1,5	dB
$f_N - 13,0\ \text{kHz} \dots f_N + 13,0\ \text{kHz}$		—	0,5	3,0	dB
Group delay ripple (p-p)	$\Delta\tau$				
$f_N - 13,0\ \text{kHz} \dots f_N + 13,0\ \text{kHz}$		—	3,5	10,0	μs
Relative attenuation (relative to α_{\min})	α_{rel}				
$f_N \pm 60\ \text{kHz}$		14	24	—	dB
$f_N \pm 120\ \text{kHz}$		40	50	—	dB
$f_N \pm 240\ \text{kHz}$		50	56	—	dB
$f_N \pm 330\ \text{kHz}$		55	65	—	dB
$f_N \pm 480\ \text{kHz}$		55	62	—	dB
$f_N \pm 660\ \text{kHz}$		55	70	—	dB
Spurious responses $f_N \pm 480\ \text{kHz} \dots 1\ \text{MHz}$		40	45	—	dB
Impedance within the passband					
Input: $Z_{\text{IN}} = R_{\text{IN}} \parallel C_{\text{IN}}$		—	850 \parallel 0,8	—	$\Omega \parallel \text{pF}$
Output: $Z_{\text{OUT}} = R_{\text{OUT}} \parallel C_{\text{OUT}}$		—	850 \parallel 0,8	—	$\Omega \parallel \text{pF}$
Temperature coefficient of frequency ¹⁾	TC_f	—	-0,034	—	ppm/K ²
Turnover temperature	T_0	—	25	—	$^{\circ}\text{C}$

¹⁾ Temperature dependance of f_c : $f_c(T) = f_c(T_0)(1 + TC_f(T - T_0)^2)$



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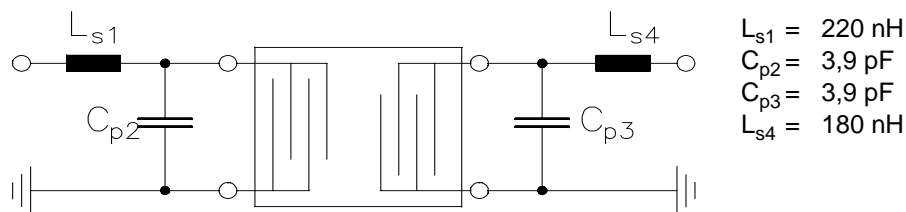
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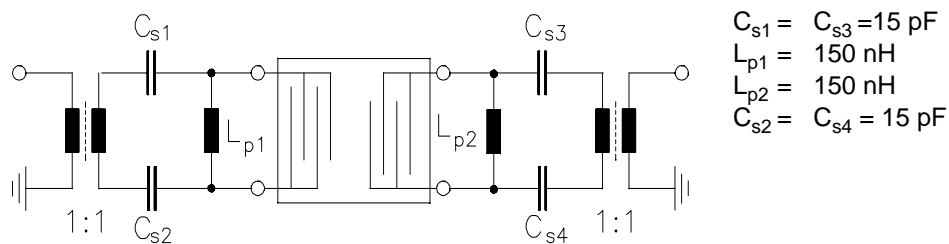


Test matching networks to 50 Ω (element values depend on pcb layout)

a) Unbalanced - unbalanced matching network



b) Balanced - balanced matching network



Note :

The balanced - balanced network is realized using TOKO 1:1 baluns B5FL. The insertion attenuation of each balun is 0,6 dB at f_N . The loss of the baluns is not included in the specified filter insertion attenuation.

The level of ultimate suppression may be limited by electromagnetic feedthrough depending on the layout of the pcb and the arrangement of the matching components.

The above mentioned characteristics can be realized either in balanced or in unbalanced mode of operation.

For more details see our application note *PCB Layout for Highly Selective IF Filters*.



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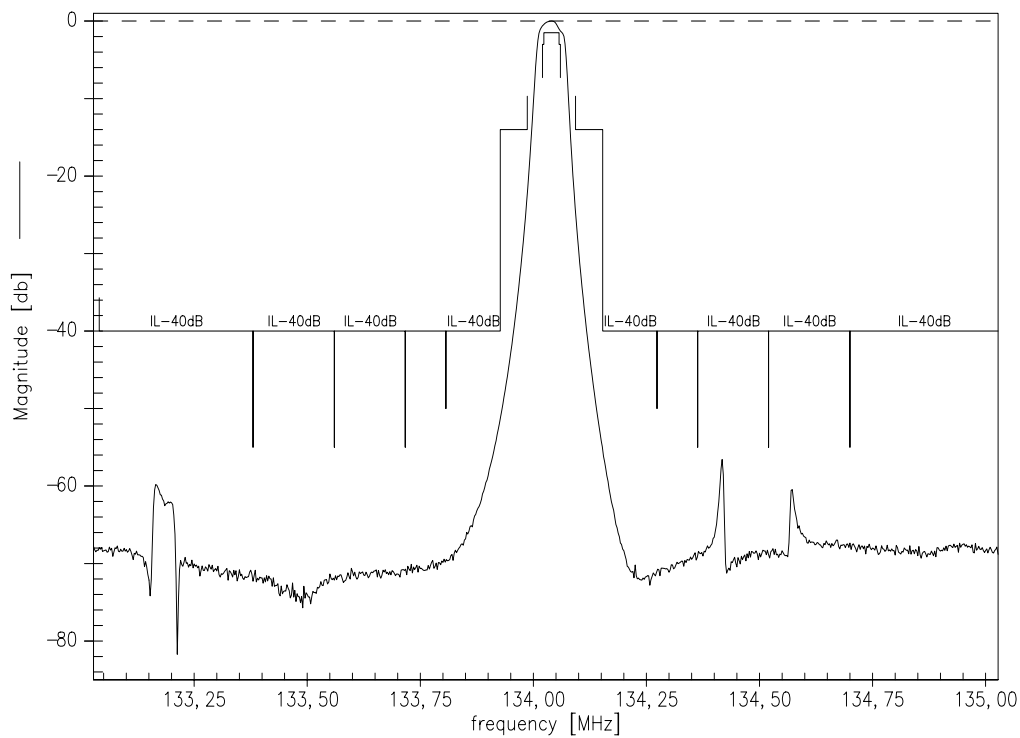
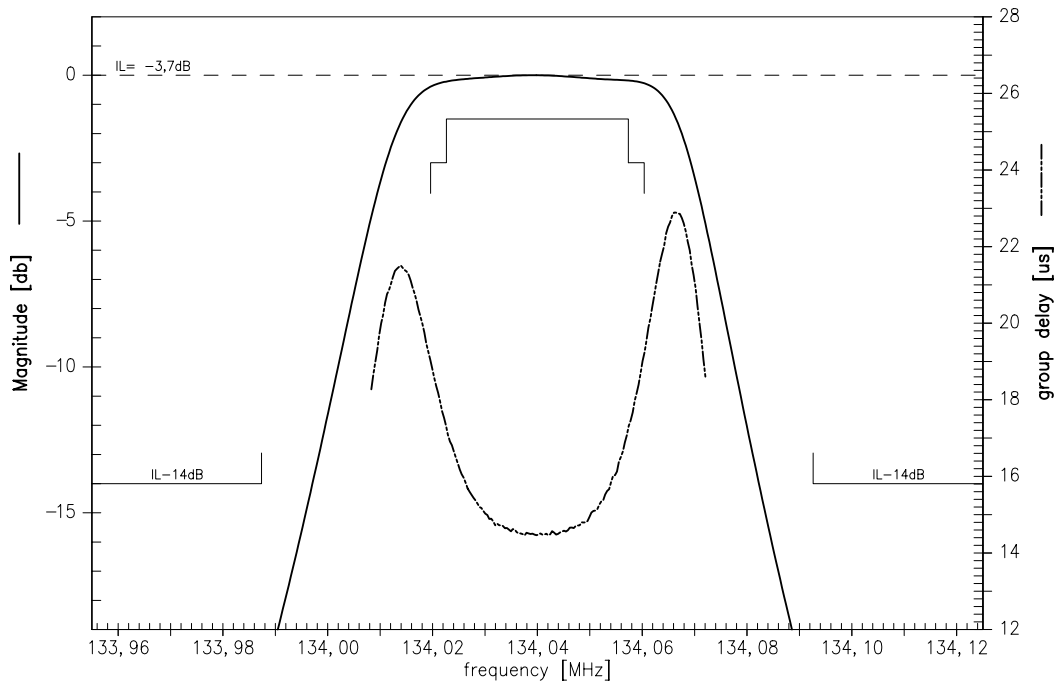
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Transfer functions passband and wideband, measured se/se on PCB





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