

COMPOUND TRANSISTOR

on-chip resistor NPN silicon epitaxial transistor For mid-speed switching

-/// R1 $\mathbf{o}\mathbf{c}$

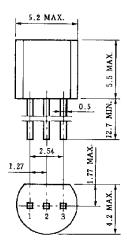
FEATURES

- On-chip bias resistor (R₁ = 10 kΩ)
- Complementary transistor with AN1A4Z

Symbol	Ratings	Unit
Vсво	60	V
VCEO	50	V
Vebo	5	V
IC(DC)	100	mA
IC(pulse) *	200	mA
Рт	250	mW
Tj	150	°C
Tstg	–55 to +150	°C
	VCBO VCEO VEBO IC(DC) IC(pulse) * PT Tj	VCBO 60 VCEO 50 VEBO 5 Ic(DC) 100 Ic(pulse) * 200 PT 250 Tj 150

ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

PACKAGE DRAWING (UNIT: mm)



Electrode Connection

1. Emitter ElAJ SC-43B

2. Collector JEDEC : TO-92

3. Base IEC : PA33

* PW \leq 10 ms, duty cycle \leq 50 %

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Collector cutoff current	Ісво	Vcb = 50 V, IE = 0			100	nA
DC current gain	hfe1 **	$V_{CE} = 5.0 \text{ V}, \text{ Ic} = 5.0 \text{ mA}$	135	340	600	I
DC current gain	hfe2 **	$V_{CE} = 5.0 \text{ V}, \text{ Ic} = 50 \text{ mA}$	100	300		-
Collector saturation voltage	V _{CE(sat)} **	Ic = 5.0 mA, I _B = 0.25 mA		0.04	0.2	V
Low level input voltage	VIL **	$V_{CE} = 5.0 \text{ V}, \text{ Ic} = 100 \ \mu\text{A}$		0.55	0.5	V
High level input voltage	VIH **	Vce = 0.2 V, Ic = 5.0 mA	2.0	0.8		V
Input resistance	R1		7.0	10	13.0	kΩ
Turn-on time	ton	$Vcc = 5.0 \text{ V}, \text{ R}_{\text{L}} = 1.0 \text{ k}\Omega$			0.2	μs
Storage time	tstg	$V_{I} = 5.0 V, PW = 2.0 \mu s$			5.0	μs
Turn-off time	t _{off}	duty cycle≤2 %			6.0	μs

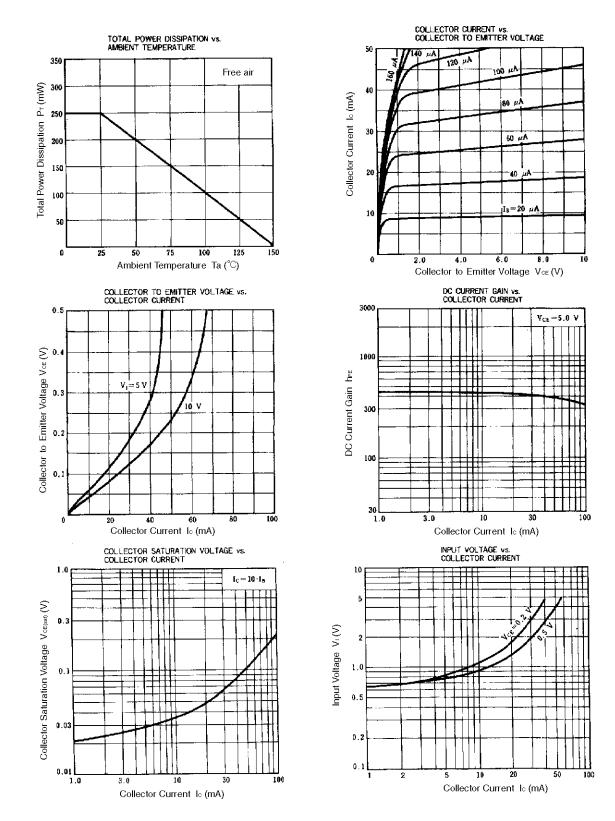
** Pulse test PW \leq 350 μ s, duty cycle \leq 2 %

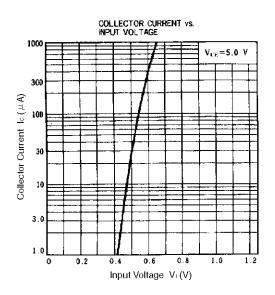
hfe CLASSIFICATION

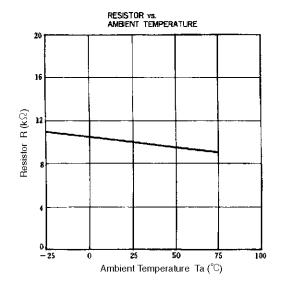
Marking	Q	Р	К
h _{FE1}	135 to 270	200 to 400	300 to 600

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TYPICAL CHARACTERISTICS (Ta = 25°C)







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