

SANYO Semiconductors DATA SHEET

N-Channel Silicon MOSFET

2SK3284 — General-Purpose Switching Device **Applications**

Features

- · Low ON-resistance.
- · Low Qg.
- · Ultrahigh-speed switching.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		400	V
Gate-to-Source Voltage	VGSS		±30	V
Drain Current (DC)	ID		10	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	40	Α
Allowable Power Dissipation	PD	Tc=25°C	50	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

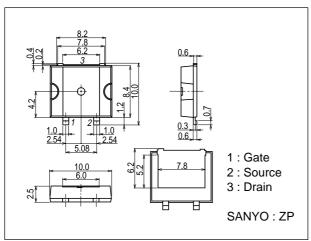
Parameter	Symbol	Conditions	Ratings			Llmit
			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0V	400			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =320V, V _{GS} =0V			1.0	mA
Gate-to-Source Leakage Current	IGSS	V _{GS} =±30V, V _{DS} =0V			±100	nA
Cutoff Voltage	VGS(off)	VDS=10V, ID=1mA	3		4	V
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =6A	2.9	5.8		S
Static Drain-to-Source On-State Resistance	R _{DS} (on)	ID=6A, VGS=15V		0.43	0.55	Ω
Input Capacitance	Ciss	V _{DS} =20V, f=1MHz		1150		pF
Output Capacitance	Coss	V _{DS} =20V, f=1MHz		350		pF
Reverse Transfer Capacitance	Crss	V _{DS} =20V, f=1MHz		150		pF
Total Gate Charge	Qg	V _{DS} =200V, V _{GS} =10V, I _D =10A		40		nC
Turn-ON Delay Time	t _d (on)	See specified Test Circuit.		17		ns
Rise Time	t _r	See specified Test Circuit.		30		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.		150		ns
Fall Time	tf	See specified Test Circuit.		50		ns
Diode Forward Voltage	V _{SD}	I _S =10A, V _{GS} =0V			1.2	V

Note) Although the protection diode is contained between gate and source, be careful of handling enough.

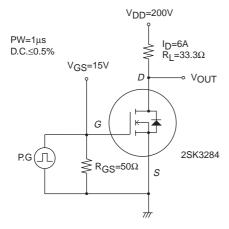
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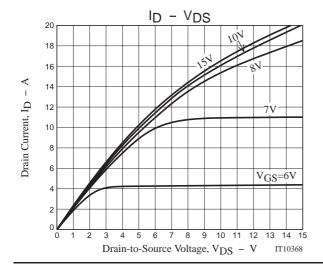
Package Dimensions

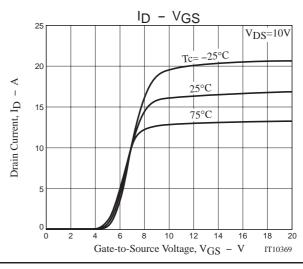
unit : mm 7002-001

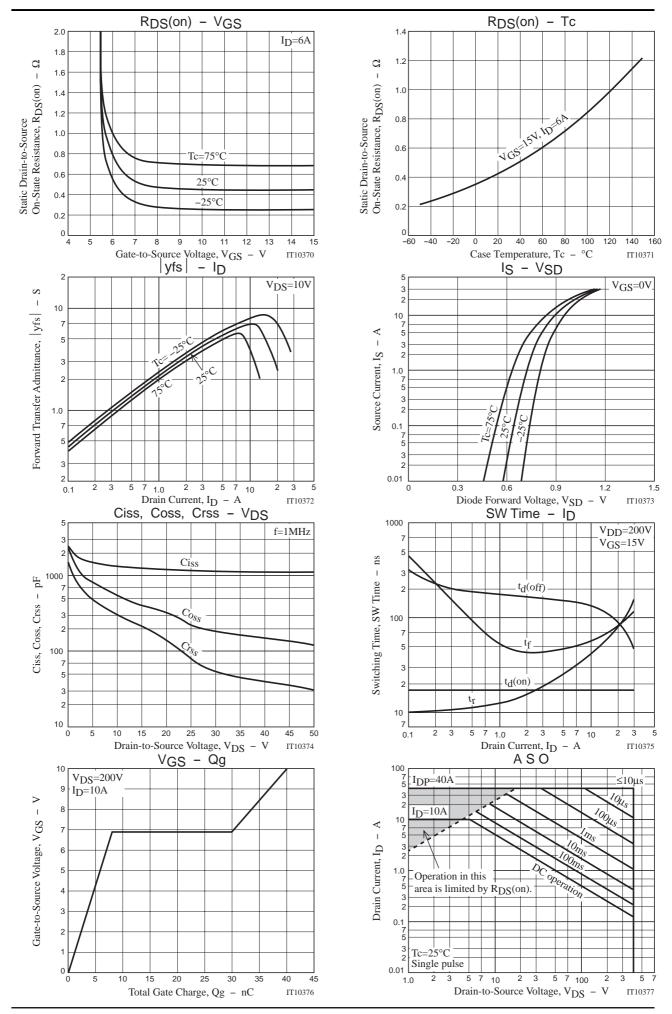


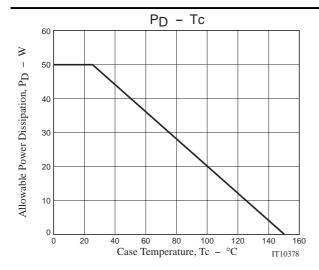
Switching Time Test Circuit











Note on usage: Since the 2SK3284 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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