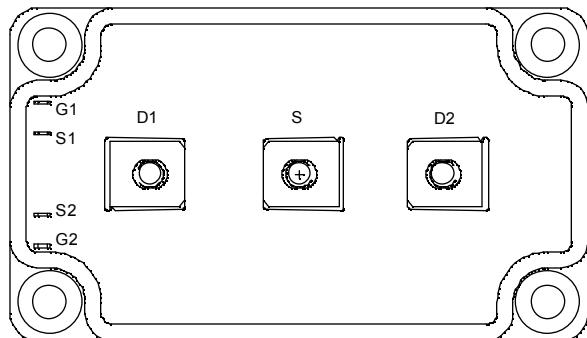
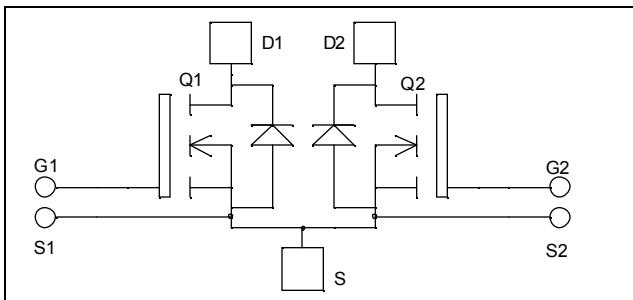


**Dual common source
MOSFET Power Module**

V_{DSS} = 500V
R_{DSon} = 17mΩ typ @ T_j = 25°C
I_D = 180A @ T_c = 25°C



Absolute maximum ratings

Symbol	Parameter	Max ratings	Unit
V _{DSS}	Drain - Source Breakdown Voltage	500	V
I _D	Continuous Drain Current	T _c = 25°C	A
		T _c = 80°C	
I _{DM}	Pulsed Drain current	720	
V _{GS}	Gate - Source Voltage	±30	V
R _{DSon}	Drain - Source ON Resistance	20	mΩ
P _D	Maximum Power Dissipation	T _c = 25°C	W
I _{AR}	Avalanche current (repetitive and non repetitive)	51	A
E _{AR}	Repetitive Avalanche Energy	50	mJ
E _{AS}	Single Pulse Avalanche Energy	3000	

 **CAUTION:** These Devices are sensitive to Electrostatic Discharge. Proper Handing Procedures Should Be Followed. See application note APT0502 on www.microsemi.com

All ratings @ $T_j = 25^\circ\text{C}$ unless otherwise specified

Electrical Characteristics

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
I_{DSS}	Zero Gate Voltage Drain Current	$V_{GS} = 0\text{V}$, $V_{DS} = 500\text{V}$	$T_j = 25^\circ\text{C}$			400	μA
		$V_{GS} = 0\text{V}$, $V_{DS} = 400\text{V}$	$T_j = 125^\circ\text{C}$			2000	
$R_{DS(on)}$	Drain – Source on Resistance	$V_{GS} = 10\text{V}$, $I_D = 90\text{A}$			17	20	$\text{m}\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 10\text{mA}$		3		5	V
I_{GSS}	Gate – Source Leakage Current	$V_{GS} = \pm 30\text{ V}$, $V_{DS} = 0\text{V}$				± 200	nA

Dynamic Characteristics

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
C_{iss}	Input Capacitance	$V_{GS} = 0\text{V}$ $V_{DS} = 25\text{V}$ $f = 1\text{MHz}$		28			nF
C_{oss}	Output Capacitance			5.6			
C_{rss}	Reverse Transfer Capacitance			0.36			
Q_g	Total gate Charge	$V_{GS} = 10\text{V}$ $V_{Bus} = 250\text{V}$ $I_D = 180\text{A}$		560			nC
Q_{gs}	Gate – Source Charge			160			
Q_{gd}	Gate – Drain Charge			280			
$T_{d(on)}$	Turn-on Delay Time		Inductive switching @ 125°C	21			ns
T_r	Rise Time	$V_{GS} = 15\text{V}$		38			
$T_{d(off)}$	Turn-off Delay Time	$V_{Bus} = 333\text{V}$		75			
T_f	Fall Time	$I_D = 180\text{A}$		93			
E_{on}	Turn-on Switching Energy	$V_{GS} = 15\text{V}$, $V_{Bus} = 333\text{V}$ $I_D = 180\text{A}$, $R_G = 0.5\Omega$		4140			μJ
E_{off}	Turn-off Switching Energy			3380			
E_{on}	Turn-on Switching Energy	$V_{GS} = 15\text{V}$, $V_{Bus} = 333\text{V}$ $I_D = 180\text{A}$, $R_G = 0.5\Omega$		6224			μJ
E_{off}	Turn-off Switching Energy			4052			

Source - Drain diode ratings and characteristics

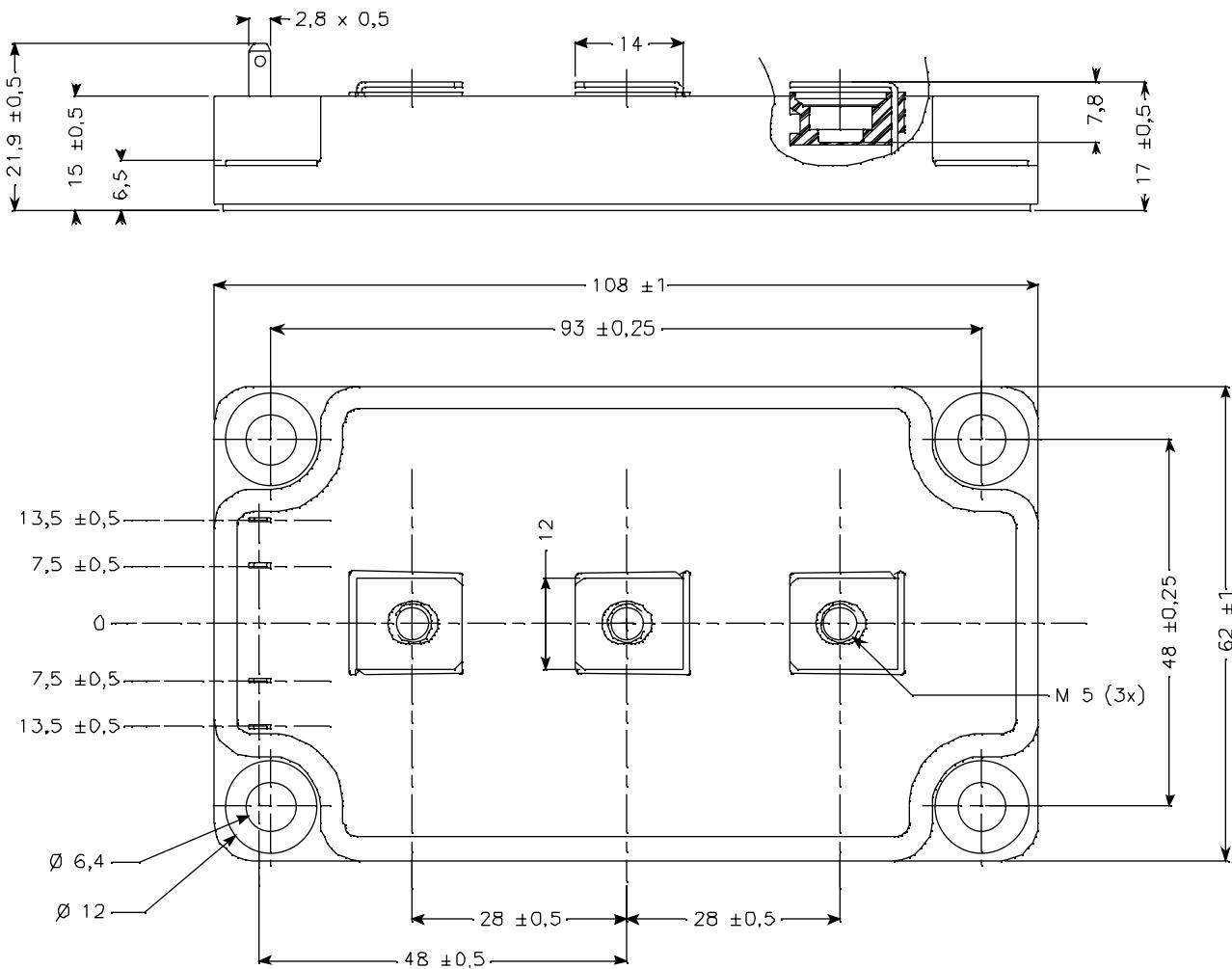
Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
I_S	Continuous Source current (Body diode)		$T_c = 25^\circ\text{C}$			180	A
			$T_c = 80^\circ\text{C}$			135	
V_{SD}	Diode Forward Voltage	$V_{GS} = 0\text{V}$, $I_S = -180\text{A}$				1.3	V
dv/dt	Peak Diode Recovery ①					8	V/ns
t_{rr}	Reverse Recovery Time	$I_S = -180\text{A}$, $V_R = 333\text{V}$ $dI/dt = 400\text{A}/\mu\text{s}$		680			ns
Q_{rr}	Reverse Recovery Charge			61			μC

① dv/dt numbers reflect the limitations of the circuit rather than the device itself.

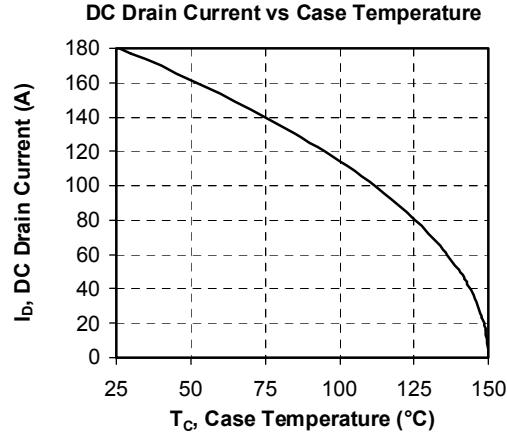
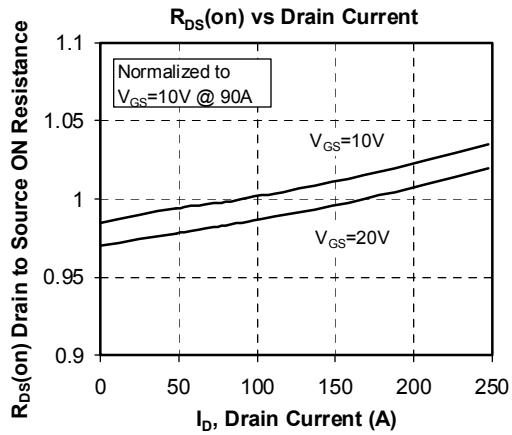
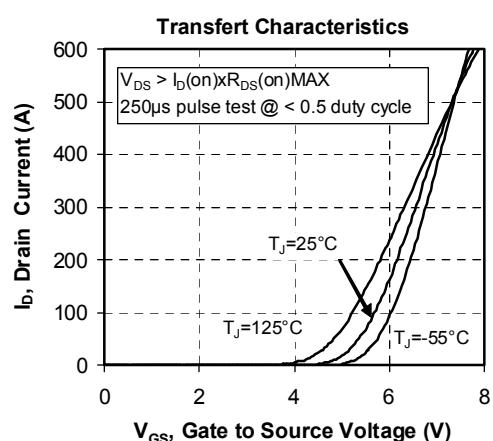
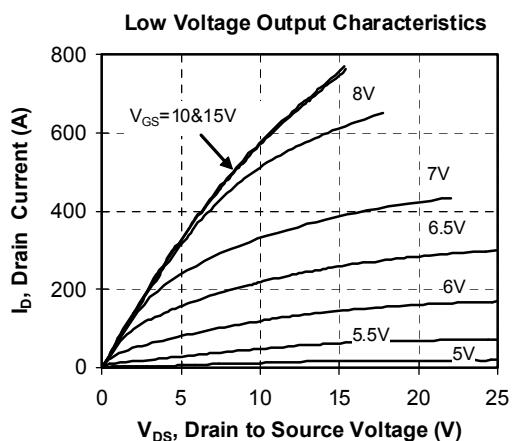
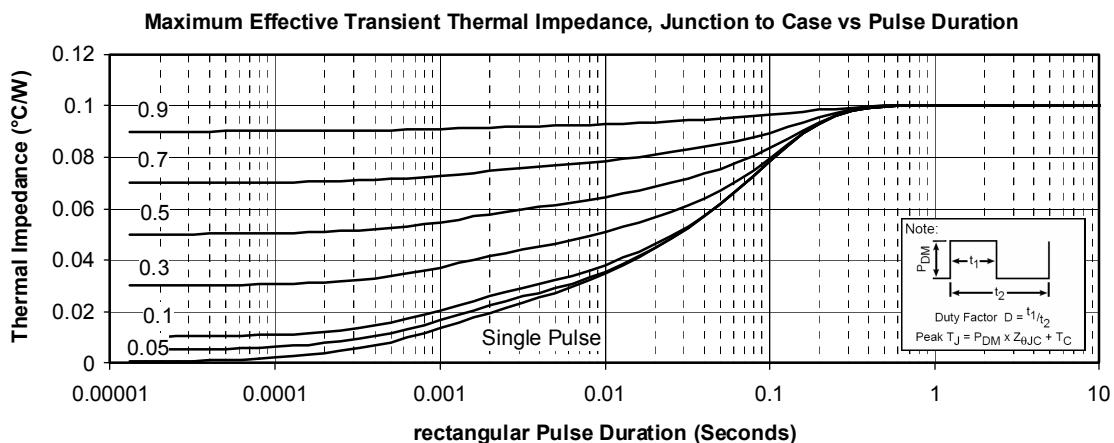
 $I_S \leq -180\text{A}$ $di/dt \leq 700\text{A}/\mu\text{s}$ $V_R \leq V_{DSS}$ $T_j \leq 150^\circ\text{C}$

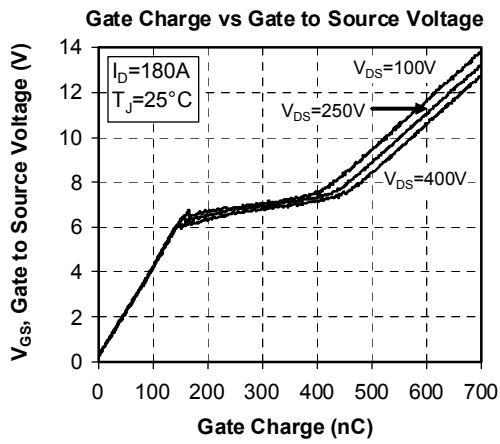
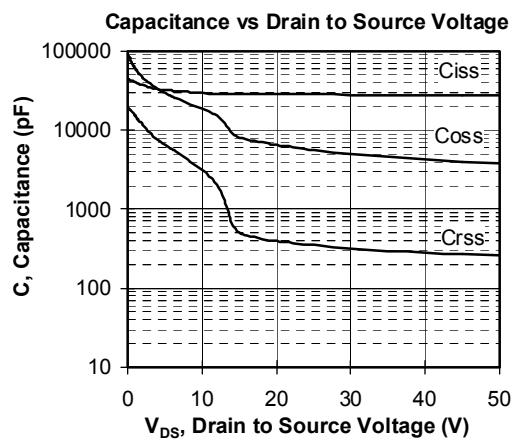
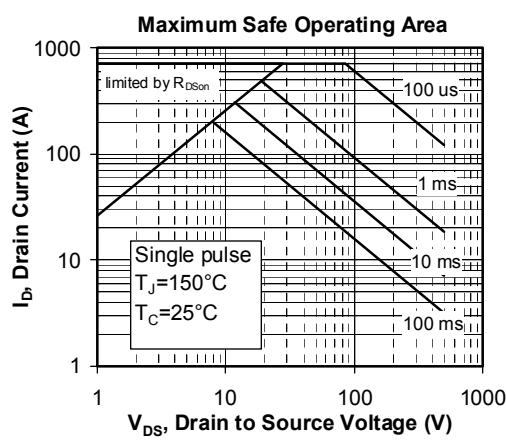
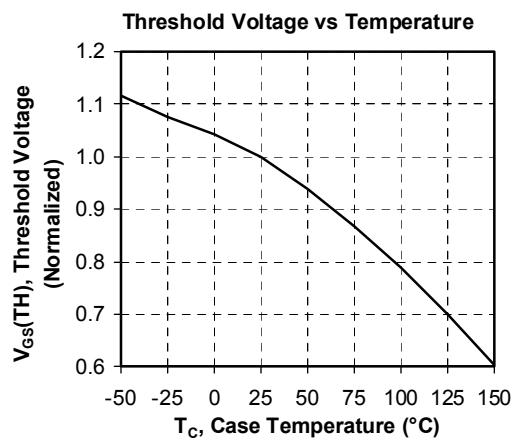
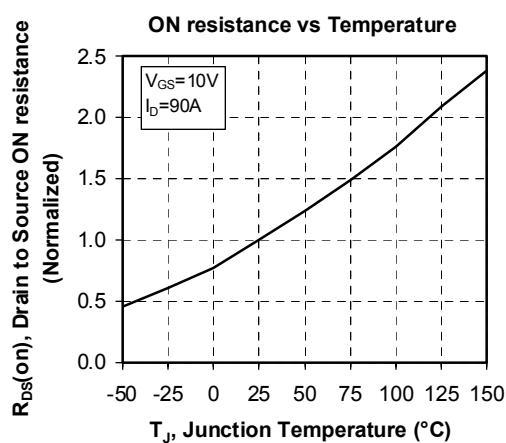
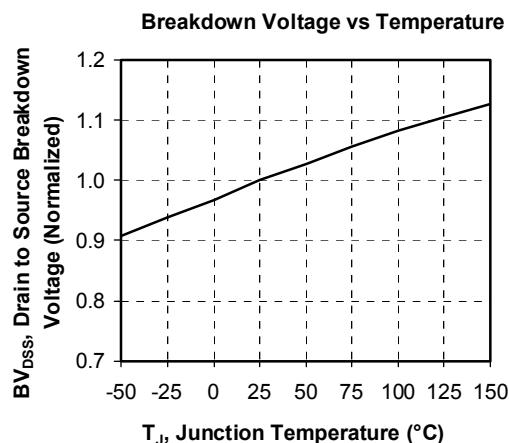
Thermal and package characteristics
Symbol **Characteristic**
Min **Typ** **Max** **Unit**

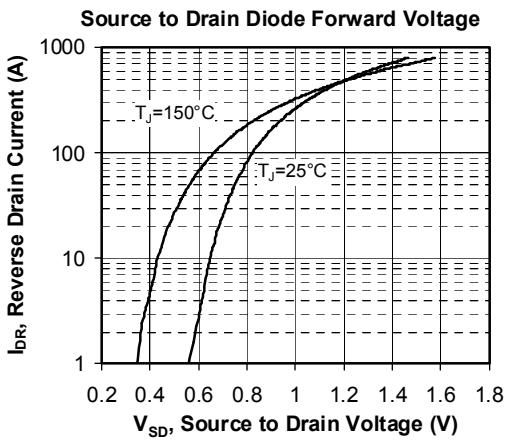
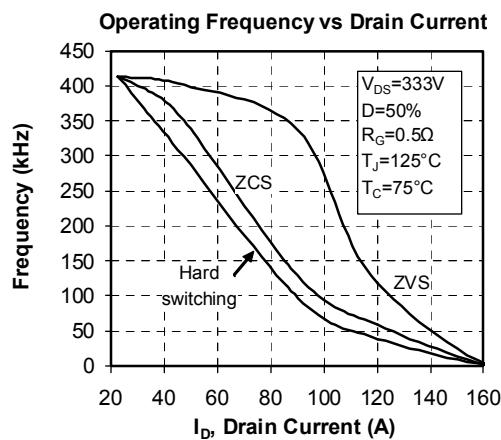
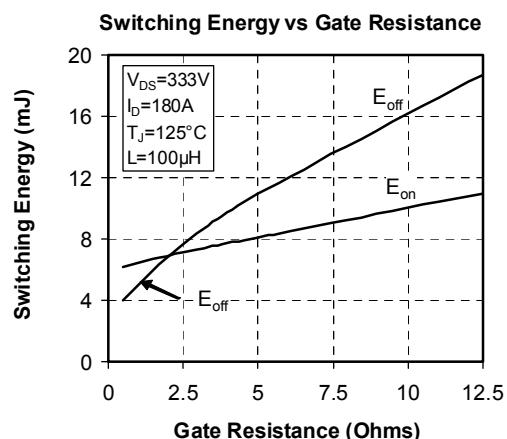
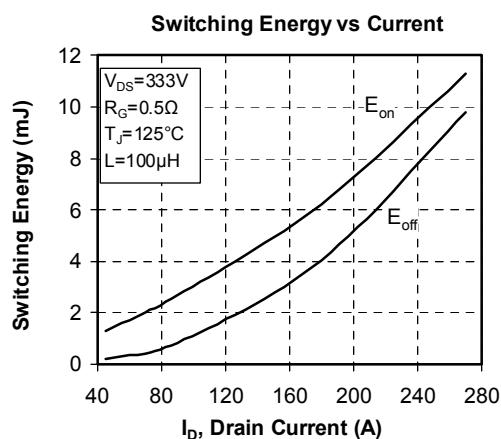
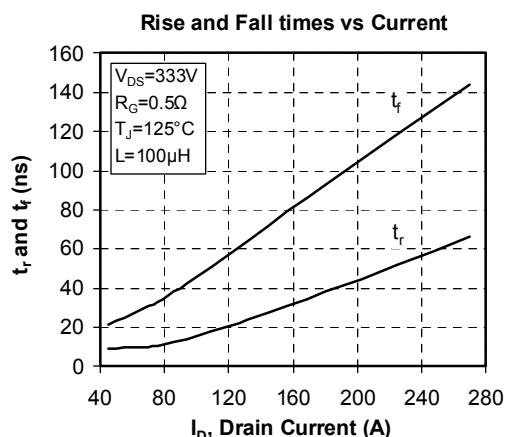
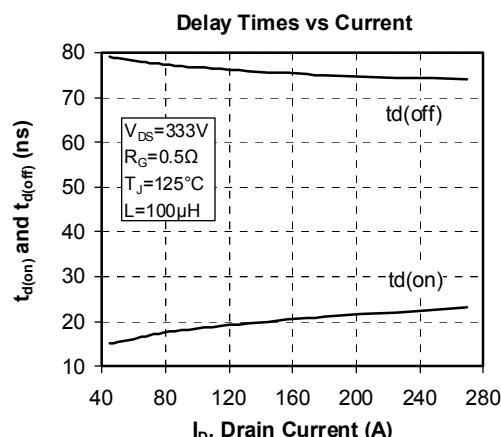
R _{thJC}	Junction to Case Thermal Resistance			0.1	°C/W
V _{ISOL}	RMS Isolation Voltage, any terminal to case t = 1 min, I isol < 1mA, 50/60Hz	2500			V
T _J	Operating junction temperature range	-40		150	
T _{STG}	Storage Temperature Range	-40		125	°C
T _C	Operating Case Temperature	-40		100	
Torque	Mounting torque	To heatsink For terminals	M6 M5	3 2	5 3.5 N.m
Wt	Package Weight			280	g

SP6 Package outline (dimensions in mm)

 See application note APT0601 - Mounting Instructions for SP6 Power Modules on www.microsemi.com

Typical Performance Curve







Microsemi reserves the right to change, without notice, the specifications and information contained herein

Microsemi's products are covered by one or more of U.S patents 4,895,810 5,045,903 5,089,434 5,182,234 5,019,522 5,262,336 6,503,786 5,256,583 4,748,103 5,283,202 5,231,474 5,434,095 5,528,058 and foreign patents. U.S and Foreign patents pending. All Rights Reserved.

