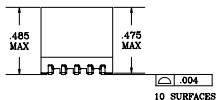
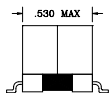
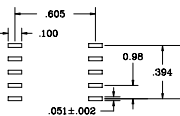
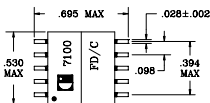


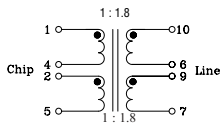
- Designed for use with Level One's ICs SK70721/725 and Zariink's MH88435 and MH88612
- Excellent THD performance
- Designed to meet the supplementary insulation requirements of UL 1950, IEC950 and EN60950 for working voltages up to 250Vrms
- Extended temperature range: -40°C to +85°C


**MECHANICAL AND SCHEMATIC (All dimensions in inches)**

SUGGESTED PAD LAYOUT



SCHEMATIC



UNLESS OTHERWISE SPECIFIED, ALL TOLERANCES ARE  $\pm 0.010$

**ELECTRICAL SPECIFICATION @ 25°C**

Turns Ratio ( $\pm 2\%$ )	Inductance (mH $\pm 10\%$ )	Leakage Inductance ( $\mu\text{H}$ MAX)	Longitudinal Balance (dB MIN @ 135 $\Omega$ )	Return Loss (dB MIN)	DC Resistance ( $\Omega$ MAX)		Isolation Voltage (Vrms)
	Line Side (10-7)(w/ 6-9 shorted)	(10-7)(w/ 6-9, 1-4, 2-5 shorted)			Line Side (10-6)-(9-7)	Chip Side (1-5)(w/ 2-4 shorted)	
1:1.8	3.00	30	50	16.5	1.5	2.3	2000

- THD Typical @ 135 $\Omega$ : -75dB @ 5kHz, 6V p-p