



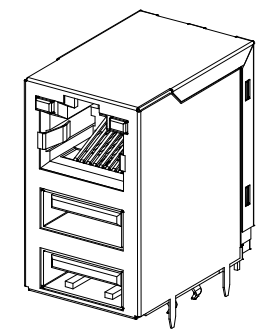
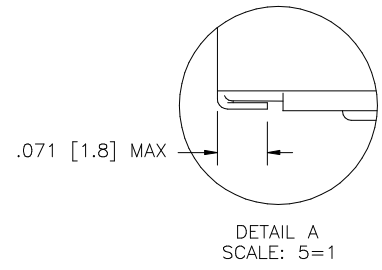
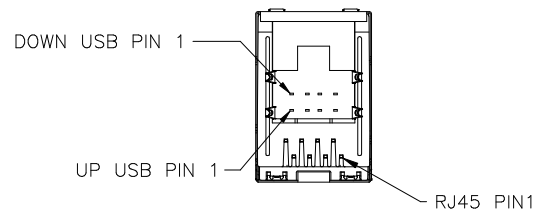
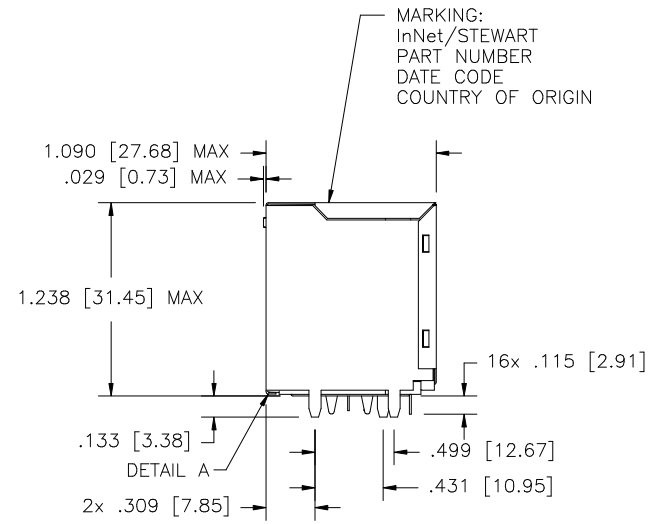
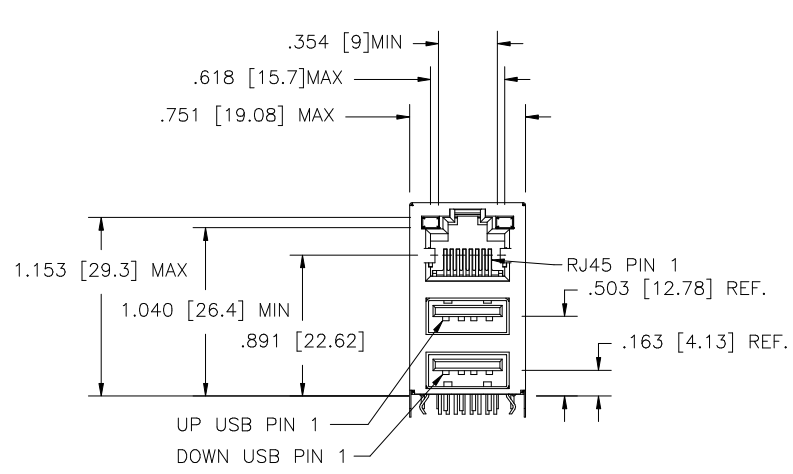
ELECTRICAL SPECIFICATIONS:

- |     |  |                             |
|-----|--|-----------------------------|
| 1.0 | URNS RATIO: TURNS RATIO (P6-P8-P3) : (J6-J3)     | : 1CT : 2.5 ±3%             |
|     | (P2-P7-P1) : (J2-J1)                             | : 1CT : 1 ±3%               |
| 2.0 | INDUCTANCE: (P6-P3)                              | : 35uH MIN. @ 0.01V, 10KHz  |
|     | (P2-P1)  | : 200uH MIN. @ 0.01V, 10KHz |
| 3.0 | LEAKAGE INDUCTANCE: P6-P3 (WITH J6 AND J3 SHORT) | : 0.3uH MAX. @ 1MHz         |
|     | P2-P1 (WITH J2 AND J1 SHORT)                     | : 0.3uH MAX. @ 1MHz         |
| 4.0 | INTERWINDING CAPACITANCE: (P6,P3) TO (J6,J3)     | : 10pf MAX. @ 1MHz          |
|     | (P2,P1) TO (J2,J1)                               | : 10pf MAX. @ 1MHz          |
| 5.0 | DC RESISTANCE: 5.0 DC RESISTANCE (J6-J3)=(J2-J1) | : 1.0 ohms Max.             |
|     | (P6-P8)=(P8-P3)                                  | : 0.3 ohms Max.             |
|     | (P2-P7)=(P7-P1)                                  | : 0.3 ohms Max.             |
| 6.0 | DIELECTRIC WITHSTAND: (P6,P3) TO (J6,J3)         | : 1500VAC                   |
|     | (P2,P1) TO (J2,J1)                               | : 1500VAC                   |

NOTES



1.0 PINS WITHOUT ELECTRICAL CONNECTION ARE OMITTED.

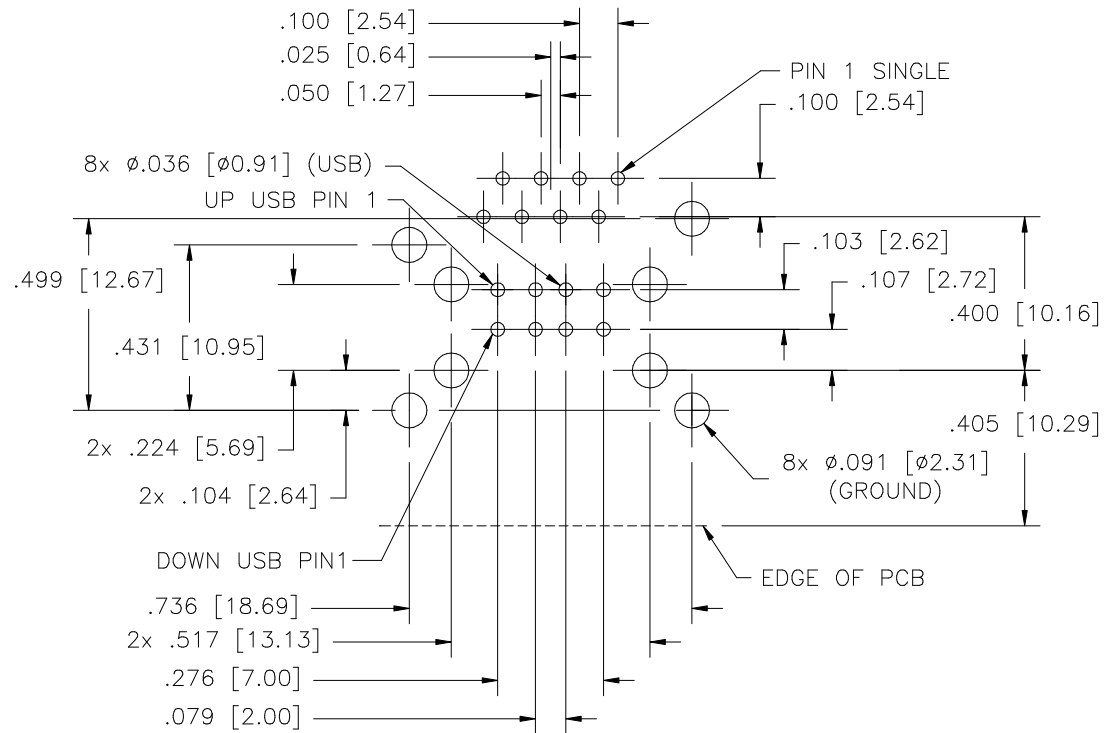
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	 <b>Stewart Connector Systems</b> <a href="http://www.stewartconnector.com">http://www.stewartconnector.com</a>	
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NOTES:

- 1.0 TOLERANCE COMPLY WITH FCC DIMENSION REQUIREMENTS.
- 2.0 PIN NOT ELECTRICALLY CONNECTED MAY BE OMITTED. SEE ELECTRICAL DRAWING FOR OMITTED PINS.
- 3.0 MATERIAL:  
 PLASTIC: UL94V-Ø  
 TERMINAL USB: PLATING PER USB SPEC 2.0  
 TERMINAL RJ45: 50 MICROINCHES Au OVER 50 MICROINCHES Ni OVER PHOSBRONZE SHIELD, BOTH USB AND RJ45: BRASS PLATED W/EITHER Ni OR TIN-LEAD
- 4.0 GENERAL TOLERANCE: ±.005 [.127]

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PC BOARD LAYOUT COMPONENT SIDE SHOWN  
 TOLERANCE:  $\pm$ .003 [.08] UNLESS OTHERWISE SPECIFIED

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