

1.0 DESIGNED TO PROTECT AGAINST GR1089 INTRABUILDING AND ESD SURGES.

2.0 PINS WITHOUT ELECTRICAL CONNECTION ARE OMITTED.

3.0 ALL RESISTORS ARE ±5% TOLERANCE.

ELECTRICAL SPECIFICATIONS:

1.0 TURNS RATIO: (P4-P5-P6) : (J3-J6) (P3-P2-P1) : (J1-J2)

: 1CT : 1CT± 3%

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2.0 INDUCTANCE: (P4-P6) (P3-P1)

: 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias

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3.0 DC RESISTANCE: (J6-J3)=(J2-J1)

: 2.0 ohms Max.

Bel Stewart Connector 11118 Susquehanna Trail, South Glen Rock, Pa 17327-9199 717.234.7512

http://www.stewartconnector.com

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SHEET DRAWING NO. 1 OF 4

SI - 53024

RECEIVE

: 1.1 dB TYP

4.0 RETURN LOSS: APPLY 100 OHMS LOAD EACH TO (P4-P6) AND (P1-P3)

 1MHz TO 30MHz
 : 18dB MIN.

 31MHz TO 80MHz
 : 12dB MIN.

NOTE: SOURCE APPLIED TO (J2-J1) OR (J3-J6).

5.0 INSERTION LOSS: RSource=RLoad=100 OHMS

100KHz TO 100MHz

6.0 CROSS TALK: 1-100 MHz : 30 dB TYP

7.0 COMMON TO COMMON MODE ATTENUATION: 1MHz TO 100MHz : 35dB TYP

8.0 RISE TIME: RS=100 OHMS AND RL = 100 OHMS

OUTPUT VOLTAGE = 1 V peak : 3.0 nS MAX PULSE WIDTH= 112nS : 3.0 nS MAX

9.0 DIELECTRIC WITHSTAND: (J1, J2) TO (P1, P3) : 1500 VAC

(J3, J6) TO (P4,P6) : 1500 VAC

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SHEET

2 OF 4

MagJack^o

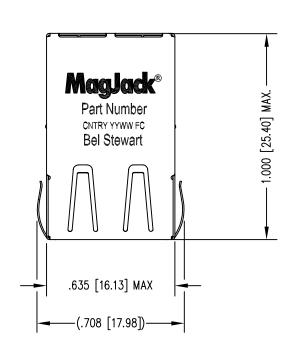
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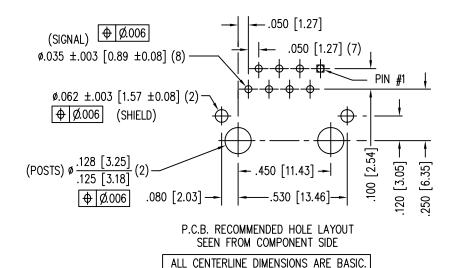
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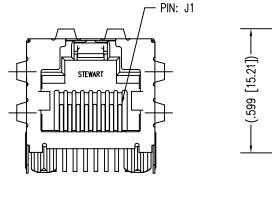
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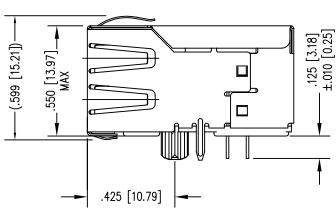
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1 **rev**.0 3









NOTES:

1. CONNECTOR MATERIALS:

HOUSING: THERMOPLASTIC UL94 V-O CONTACT/SHIELD: COPPER ALLOY SHIELD PLATING: NICKEL OR TIN CONTACT PLATING: SELECTIVE GOLD,

50 MICRO-INCHES MIN. IN CONTACT AREA.

- 2. PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED. SEE ELECTRICAL DRAWING FOR OMITTED PINS.
- 3. TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS.
- 4. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ±.005 [0.13]
- 5. WAVE SOLDER COMPATIBLE PREHEAT 125°C/90SECS.
 HIGH TEMPERATURE REFLOW COMPATABLE 230°C/90 SEC MAX.

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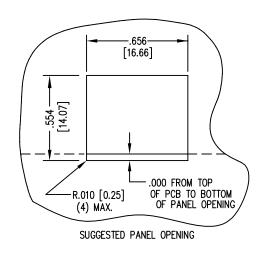
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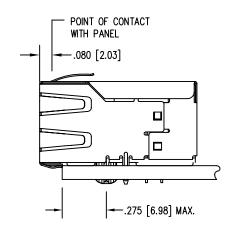
SHEET DRAWING NO. 3 OF 4

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SI-53024

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- 1. THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY.
- 2. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ±.005 [0.13]

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SHEET DRAW

DRAWING NO. SI-53024

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