

ELECTRICAL SPECIFICATIONS:

1.0 TURNS RATIO (P4-P5-P6) : (J3-J6)

(P3-P2-P1) : (J1-J2)

11CT : 1CT± 3%

: 1CT : 1CT ± 3%

<u>NOTES</u>

1.0 PINS WITHOUT ELECTRICAL CONNECTION ARE OMITTED.

2.0 INDUCTANCE (P6-P4)

(P3-P1)

: 350uH MIN. @ 0.1V , 100KHz, 8mA DC Blas

: 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias

3.0 LEAKAGE INDUCTANCE P6-P5-P4 (WITH J6 AND J3 SHORT)

: 0.3 MAX. @ 1MHz

P3-P2-P1 (WITH J2 ND J1 SHORT)

: 0.3 MAX. @ 1MHz

4.0 INTERVINDING CAPACITANCE (P6,P5,P4) TO (J6,J3)

: 30pf MAX @ 1MHz

(P3,P2,P1) T0 (J2,J1)

· 30pf MAX, @ 1MHZ

5.0 DC RESISTANCE (J6-J3)=(J2-J1)

: 1.2 ohms Max.

InNet Technologies Inc.

http://www.innet-tech.com

Stewart Connector Systems

http://www.stewartconnector.com

SHEET 1 | 1 | 1 | 4 |

DRAWING NO. SI-50029

REV.

6.0 RETURN LOSS: (P6-P4)=100 OHMS AND (P1-P3)=100 OHM REF. 1MHz T□ 30MHz 16dB MIN. 30MHz TD 80MHz : 12dB MIN. NOTE: 100 OHMS CONNECTED TO (J2-J1) OR (J6-J3). 7.0 VOLTAGE WITHSTAND (J1, J2) T0 (P1, P3) : 1500 VAC : 1500 VAC (J3, J6) TD (P4,P6) 8.0 INSERTION LOSS: RS=RL=100 ohms 1.1 dB TYP 100KHz T□ 100MHz 9.0 RISE TIME: RS=100 \square HMS AND RL = 100 \square HMS DUTPUT VOLTAGE = 1 V peak 3.0 nS MAX PULSE WIDTH= 112nS 3.0 nS MAX 10.0 CROSS TALK: 1-100 MHz 30 dB TYP 11.0 COMMON TO COMMON MODE ATTENUATION:

1MHz T□ 100MHz

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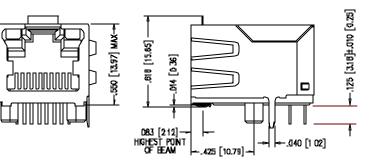
Stewart Connector Systems

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SHEET DRAWING NO. SI-50029

35dB TYP

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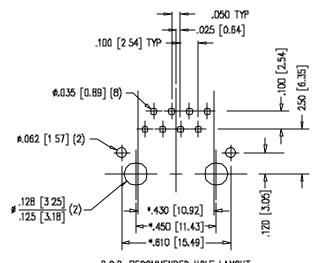


NOTES:

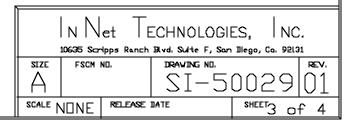
- TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS
- DIMENSIONS SHOWN WITH "*" TO BE CENTRAL ABOUT CENTER LINE
- DIMENSIONS SHOWN ARE SUBJECT TO CHANGE WITHOUT NOTICE.
- PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED.
 SEE ELECTRICAL DRAWING FOR OMITTED PINS.

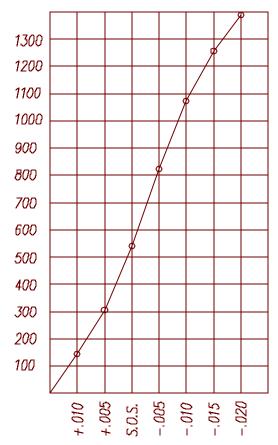
AVAILABLE WITH:

- STANDARD 50 MICRO-INCH SELECTIVE GOLD PLATING

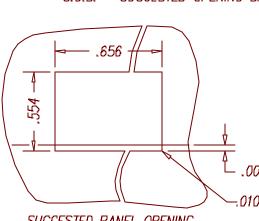


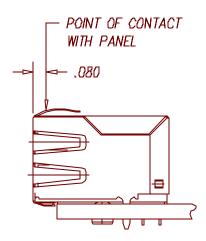
P.C.B. RECOMMENDED HOLE LAYOUT SEEN FROM COMPONENT SIDE TOLERANCE ±.003 [0.DB] UNLESS OTHERWISE SPECIFIED





PANEL GROUNDING BEAM DEFLECTION S.O.S. = SUGGESTED OPENING SIZE





THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY. THESE VARIABLES CAN BE ADJUSTED IN EITHER DIRECTION BUT MAY CARRY SOME CONSEQUENCES IN THE FORM OF LOWER MATING FORCES OR TIGHTER ASSEMBLY TOLERANCES. FORCE VALUES ON THE GRAPH ARE GENERAL AVERAGES TAKEN AT THE POINT OF CONTACT SHOWN ABOVE. THE SUGGESTED PANEL OPENING INCLUDES APPROXIMATELY .020 CLEARANCE ON THE SIDES AND TOP AND .013 ON THE BOTTOM, AT PANEL OPENING.

.000 (TOP OF PCB TO BOTTOM OF OPENING) -.010 MAX. RADIUS(4)

SUGGESTED PANEL OPENING

CT720034X1/24-001302

ECHNOLOGIES. 10635 Scripps Ranch Blvd. Suite F, San Illego, Ca. 92131 SIZE FSCM NO. DRAWING NO. SCALE NONE RELEASE DATE