Features

- **Local Oscillator Output Power: -6 dBm at 3.3 GHz**
- **Supply-voltage Range 3 V to 4.6 V**
- **Fast Settling Synthesizer**
- **Supports Multiple Reference Clocks (10.368 MHz/13.824 MHz)**

Benefits

- **Extended Battery Operating Time**
- **Low Current Consumption**
- **Very Small QFN32 5 mm $\times$ 5 mm Package**
- **Few Low Cost External Components**

Electrostatic sensitive device.
Observe precautions for handling.

**3.3 GHz
LO + PLL-chip**

ATR2807

Summary

Preliminary

Description

The ATR2807 is an RF building block. It is used as a 3.344 GHz output frequency local oscillator. The QFN32-packaged IC includes an LO buffer, synthesizer together with fully integrated VCO and supply voltage regulators.
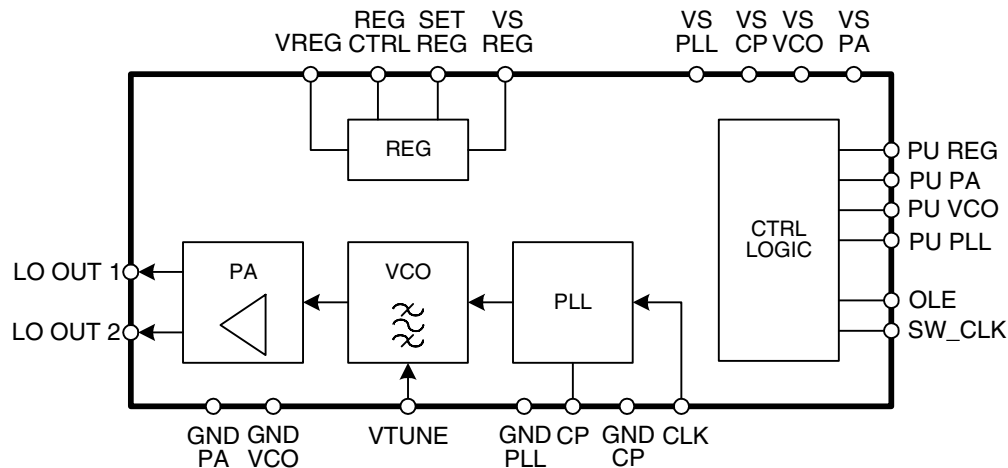
Figure 1. Block Diagram

Table 1. Functional Block Description

Name	Description
REG	Voltage regulator
CP	Charge pump
PLL	Phase locked loop
PA	Buffer amplifiers for LO_OUT
VCO	Voltage-controlled oscillator

Note: This is a summary document. A complete document is available under NDA. For more information, please contact your local Atmel sales office.

Ordering Information

Extended Type Number	Package	Remarks
ATR2807	QFN32	Taped and reeled

Package Information

Package: QFN 32 – 5x5
Exposed pad 3.15x3.15
(acc. JEDEC OUTLINE No. MO-220)
Dimensions in mm

technical drawings
according to DIN
specifications

Drawing-No.: 6.543-5087.01-4
Issue: 2; 24.01.03

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