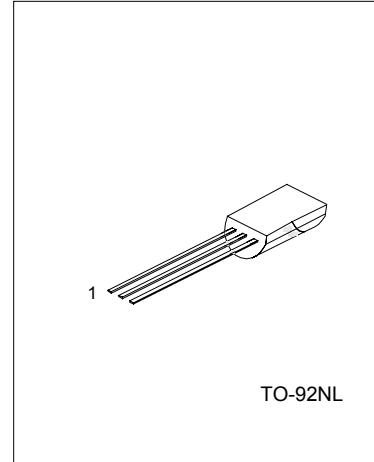


SILICON PNP EPITAXIAL

APPLICATION

* Low frequency power amplifier



1:EMITTER 2:COLLECTOR 3:BASE

ABSOLUTE MAXIMUM RATINGS (Ta=25°C, unless otherwise specified)

PARAMETER	SYMBOL	VALUE	UNIT
Collector-Base Voltage	V _{CB0}	-120	V
Collector-Emitter Voltage	V _{CEO}	-80	V
Emitter-Base Voltage	V _{EB0}	-5	V
Collector Current	I _c	-1	A
Collector Peak Current	I _{c(peak)}	-2	A
Collector Power Dissipation	P _c	0.9	W
Junction Temperature	T _j	150	°C
Storage Temperature	T _{stg}	-55 ~ +150	°C

ELECTRICAL CHARACTERISTICS (Ta=25°C, unless otherwise specified)

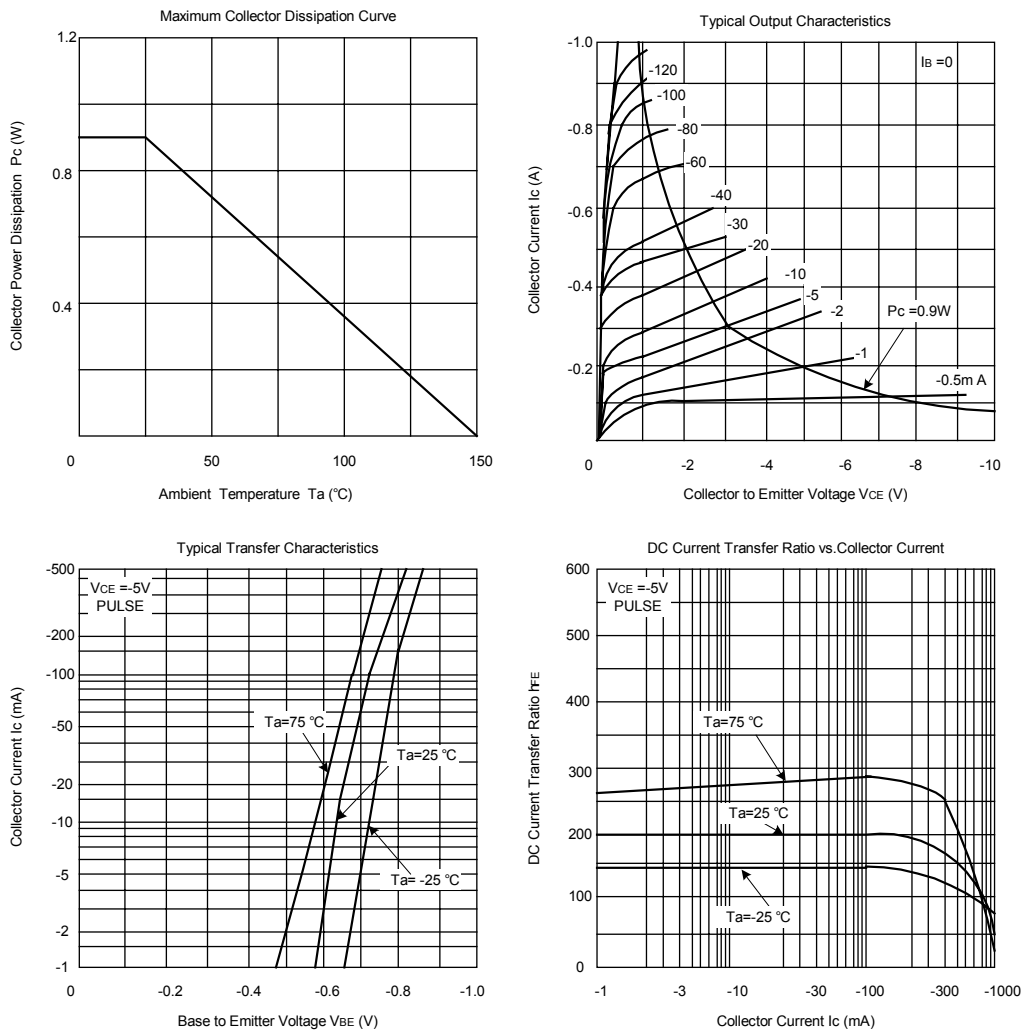
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector-Base Breakdown Voltage	V(BR)CBO	I _c = -10μA, I _E = 0	-120			V
Collector-Emitter Breakdown Voltage	V(BR)CEO	I _c = -1mA, R _{BE} = ∞	-80			V
Emitter-Base Breakdown Voltage	V(BR)EBO	I _E = -10μA, I _c = 0	-5			V
Collector Cut-Off Current	I _{CBO}	V _{CB} = -100V, I _E = 0			-10	μA
DC Current Transfer Ratio	h _{FE1}	V _{CE} = -5V, I _c = -150mA(note)	60		320	
	h _{FE2}	V _{CE} = -5V, I _c = -500mA(note)	30			
Collector-Emitter Saturation Voltage	V _{CE(sat)}	I _c = -500mA, I _B = -50 mA(note)			-1	V
Base-Emitter Saturation Voltage	V _{BE}	V _{CE} = -5V, I _c = -150mA(note)			-1.5	V
Gain Bandwidth Product	f _T	V _{CE} = -5V, I _c = -150mA		140		MHz
Collector Output Capacitance	C _{ob}	V _{CB} = -10V, I _E = 0, f = 1MHz		20		pF

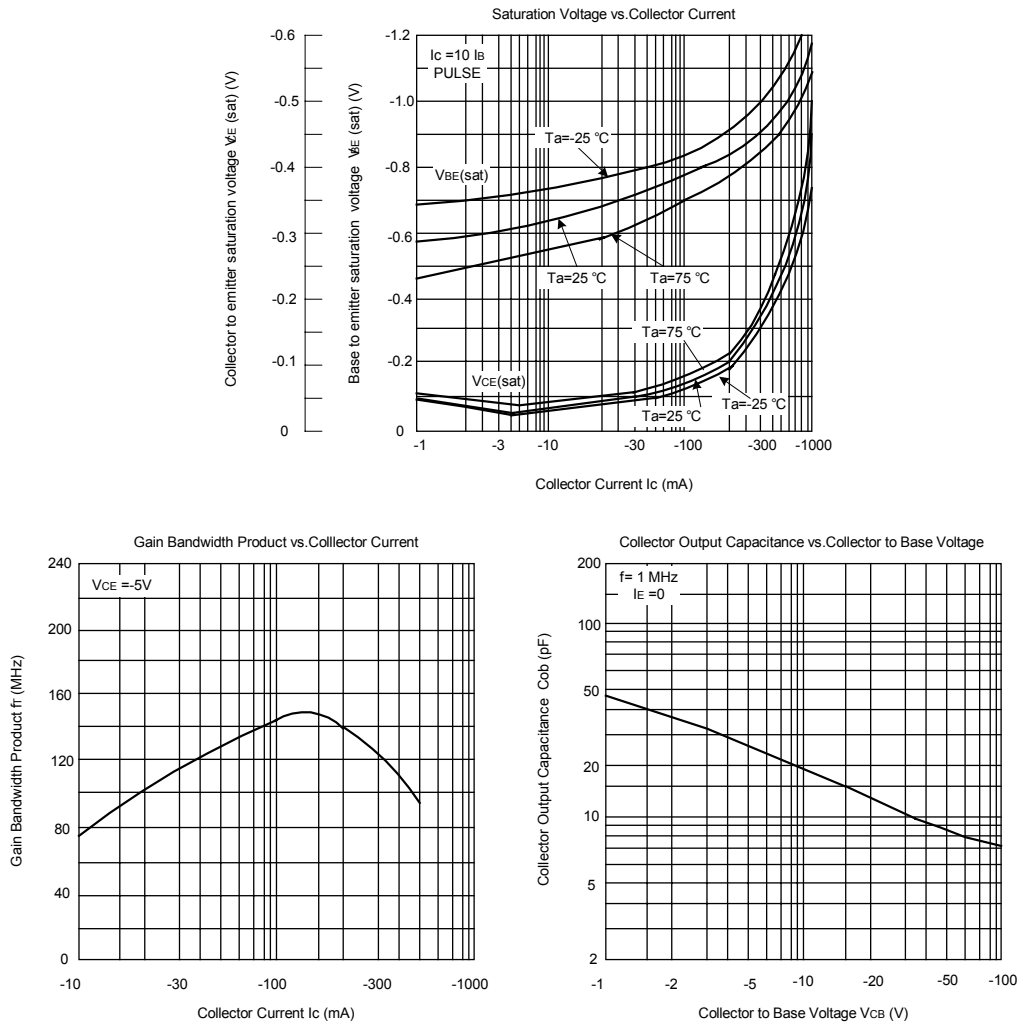
Note: Pulse test

CLASSIFICATION OF h_{FE1}

RANK	B	C	D
RANGE	60-120	100-200	160-320

TYPICAL PERFORMANCE CHARACTERISTICS





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