



# MMSTA42

### NPN SMALL SIGNAL SURFACE MOUNT TRANSISTOR

### **Features**

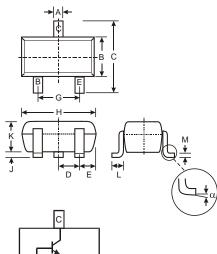
- Epitaxial Planar Die Construction
- Complementary PNP Type Available (MMSTA92)
- Ideal for Low Power Amplification and Switching
- Ultra-Small Surface Mount Package
- Lead Free/RoHS Compliant (Note 2)
- "Green" Device (Note 3 and 4)

### **Mechanical Data**

Case: SOT-323

 Case Material: Molded Plastic, "Green" Molding Compound, Note 4. UL Flammability Classification Rating 94V-0

- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminal Connections: See Diagram
- Terminals: Solderable per MIL-STD-202, Method 208
- Lead Free Plating (Matte Tin Finish annealed over Alloy 42 leadframe).
- Marking (See Page 2): K3M
- Ordering & Date Code Information: See Page 2
- Weight: 0.006 grams (approximate)



SOT-323										
Dim	Min	Max								
Α	0.25	0.40								
В	1.15	1.35								
С	2.00	2.20								
D	0.65 N	ominal								
E	0.30	0.40								
G	1.20	1.40								
Н	1.80	2.20								
J	0.0	0.10								
K	0.90	1.00								
L	0.25	0.40								
М	0.10	0.18								
	0°	8°								
All Dimensions in mm										

### **Maximum Ratings** @ T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit		
Collector-Base Voltage	V <sub>CBO</sub>	300	V		
Collector-Emitter Voltage	VCEO	300	V		
Emitter-Base Voltage	V <sub>EBO</sub>	6.0	V		
Collector Current (Note 1)	Ic	200	mA		
Power Dissipation (Note 1)	P <sub>d</sub>	200	mW		
Thermal Resistance, Junction to Ambient (Note 1)	R JA	625	°C/W		
Operating and Storage and Temperature Range	T <sub>j</sub> , T <sub>STG</sub>	-55 to +150	°C		

Note: 1. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

- 2. No purposefully added lead.
- 3. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead\_freeindex.php.
- 4. Product manufactured with Date Code 0627 (week 27, 2006) and newer are built with Green Molding Compound. Product manufactured prior to Date Code 0627 are built with Non-Green Molding Compound and may contain Halogens or Sb2O3 Fire Retardants.



### Electrical Characteristics @ T<sub>A</sub> = 25°C unless otherwise specified

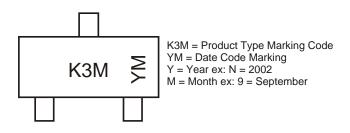
Characteristic	Symbol	Min	Max	Unit	Test Condition				
OFF CHARACTERISTICS (Note 5)									
Collector-Base Breakdown Voltage	V <sub>(BR)CBO</sub>	300		V	$I_C = 100 \mu A, I_E = 0$				
Collector-Emitter Breakdown Voltage	V <sub>(BR)CEO</sub>	300		V	$I_C = 1.0 \text{mA}, I_B = 0$				
Emitter-Base Breakdown Voltage	V <sub>(BR)EBO</sub>	6.0		V	$I_E = 100 \mu A, I_C = 0$				
Collector Cutoff Current	I <sub>CBO</sub>		100	nA	V <sub>CB</sub> = 200V, I <sub>E</sub> = 0				
Collector Cutoff Current	I <sub>EBO</sub>		100	nA	$V_{CE} = 6.0V, I_{C} = 0$				
ON CHARACTERISTICS (Note 5)									
DC Current Gain	h <sub>FE</sub>	25 40 40			I <sub>C</sub> = 1.0mA, V <sub>CE</sub> = 10V I <sub>C</sub> = 10mA, V <sub>CE</sub> = 10V I <sub>C</sub> = 30mA, V <sub>CE</sub> = 10V				
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>		0.5	V	I <sub>C</sub> = 20mA, I <sub>B</sub> = 2.0mA				
Base-Emitter Saturation Voltage	V <sub>BE(SAT)</sub>		0.9	V	I <sub>C</sub> = 20mA, I <sub>B</sub> = 2.0mA				
SMALL SIGNAL CHARACTERISTICS									
Output Capacitance	C <sub>cb</sub>		3.0	pF	V <sub>CB</sub> = 20V, f = 1.0MHz, I <sub>E</sub> = 0				
Current Gain-Bandwidth Product	f <sub>T</sub>	50		MHz	V <sub>CE</sub> = 20V, I <sub>C</sub> = 10mA, f = 100MHz				

### Ordering Information (Note 4 and 6)

Device	Packaging	Shipping			
MMSTA42-7-F	SOT-323	3000/Tape & Reel			

- Notes: 4. Product manufactured with Date Code 0627 (week 27, 2006) and newer are built with Green Molding Compound. Product manufactured prior to Date Code 0627 are built with Non-Green Molding Compound and may contain Halogens or Sb2O3 Fire Retardants.
  - 5. Short duration pulse test used to minimize self-heating effect.
  - 6. For Packaging Details go our website at http://www.diodes.com/datasheets/ap02007.pdf.

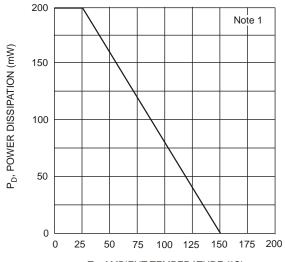
## **Marking Information**



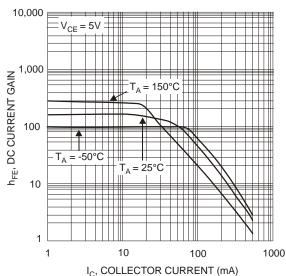
### Date Code Key

Year	1998	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012
Code	J	K	L	М	N	Р	R	S	Т	J	V	W	Х	Y	Z
Month			Jan	Feb	March	Apr	May	Jun	Jul	Aug	Sep	00	et	Nov	Dec
Code			1	2	3	4	5	6	7	8	9	С	)	N	D

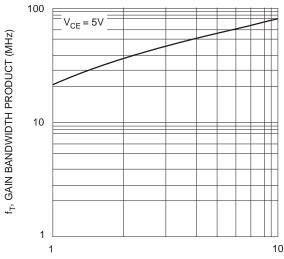




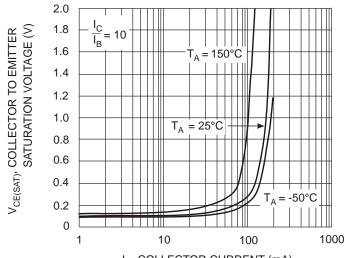
T<sub>A</sub>, AMBIENT TEMPERATURE (°C) Fig. 1, Max Power Dissipation vs Ambient Temperature



COLLECTOR CURRENT (m. Fig. 3, DC Current Gain vs Collector Current



I<sub>C</sub>, COLLECTOR CURRENT (mA) Fig. 5, Gain Bandwidth Product vs Collector Current



I<sub>C</sub>, COLLECTOR CURRENT (mA)
Fig. 2, Collector Emitter Saturation Voltage
vs. Collector Current

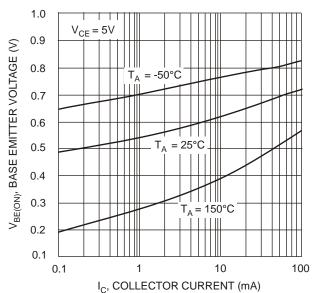


Fig. 4, Base Emitter Voltage vs Collector Current



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