

# LB11983

# 3-Phase Sensorless Motor Driver for Fan Motor Driver for Refrigerator

### **Overview**

The LB11983 is a 3-phase full-wave current linear sensorless motor driver. It is optimal for refrigerator fan motor drive.

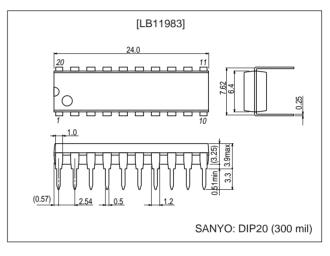
### **Features**

- current linear driving technique
- current limiter circuit
- Over saturation prevention circuit for output stage
- Provides coil back EMF FG output
- Thermal shoutdown circuit
- · Beat lock pervention circuit

# **Package Dimensions**

unit: mm

3021C-DIP20



## **Specifications**

#### Absolute Maximum Ratings at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	V <sub>CC</sub> max		14.5	V
Output application voltage	V <sub>O</sub> max		14.5	V
Input application voltage	V <sub>I</sub> max		-0.3 to V <sub>CC</sub> + 0.3	V
Output current	I <sub>O</sub> max		1.0	A
Internal allowable loss	Pd max	Independent IC	1.0	W
Operating temperature	Topr		-30 to +85	°C
Storage temperature	Tstg		-55 to +150	°C

#### Operating Conditions at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	V <sub>CC</sub>		7 to 13.8	V

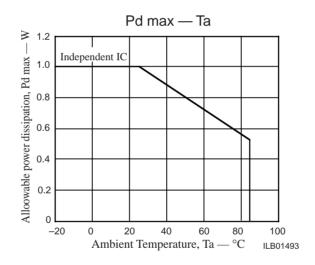
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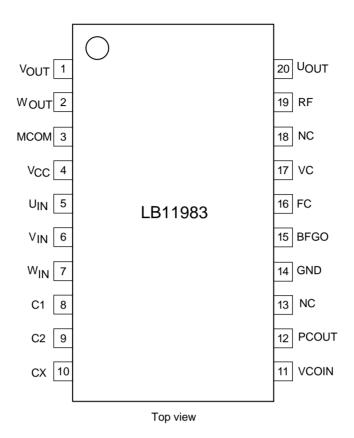
# Electrical Characteristics at Ta = 25°C, $V_{CC}$ = 12.0 V

Parameter	Symbol	Conditions	Ratings			Unit
Parameter	Symbol Conductions		min	typ	typ max	
Supply current	Icc	VC = V <sub>CC</sub>		20	30	mA
Output saturation voltage 1	V <sub>O</sub> sat1	I <sub>O</sub> = 0.4 A, Source + Sink		1.4	2.0	V
Output saturation voltage 2	V <sub>O</sub> sat2	$I_{O}$ = 0.8 A, Source + Sink, RF = 0 $\Omega$		1.8	2.6	V
MCOM pin common-mode input voltage range	VIC		0		V <sub>CC</sub> – 2	V
PCOUT output current 1	IPCOU	Source side		-90		μA
PCOUT output current 2	IPCOD	Sink side		90		μA
VCOIN input current	IVCOIN	VCOIN = 5 V		0.1	0.2	μA
VCO minimum frequency	fVCOMIN	VCOIN = open		400		Hz
VCO maximum frequency	fVCOMAX	VCOIN = 5 V		18.5		kHz
C1, C2 source current ratio	RSOURCE	IC1SOURCE/IC2SOURCE	-12		+12	%
C1, C2 sink current ratio	RSINK	IC1SINK/IC2SINK	-12		+12	%
C1 source and sink current ratio	RC1	IC1SOURCE/IC1SINK	-35		+15	%
C2 source and sink current ratio	RC2	IC2SOURCE/IC2SINK	-35		+15	%
Counter FG output ON voltage	V <sub>OL</sub>				0.4	V
Counter FG output OFF voltage	V <sub>OH</sub>		4			V
Thermal shutdown operating temperature	TTSD	Design target value*	150	180	210	°C
Thermal shutdown hysteresis	∆TTSD	Design target value*		15		°C

Note\*: These items are design target values and are not tested.



### **Pin Arrangement**



#### **Pin Functions**

Pin No.	Symbol	Pin voltage	Description	Equivalent circuit
20	U <sub>OUT</sub>			Vcc
1	V <sub>OUT</sub>		Drum motor driver output pin.	50hb
2	W <sub>OUT</sub>			
19	RF		Minimum potential of the drum motor driver output transistor. This voltage is detected for constant-current control.	$\begin{array}{c} & & & & \\ 3.9\Omega \\ & & & \\ 30k\Omega \\ & & \\ 19 \end{array} \begin{array}{c} & & \\ & & \\ \end{array} \begin{array}{c} 20 \\ 1 \\ 2 \end{array} \begin{array}{c} & 5 \\ 6 \\ 7 \end{array} \begin{array}{c} & \\ & \\ \end{array} \end{array}$
			The current limiter is also activated upon detection of this potential.	
4	V <sub>CC</sub>	8 to 13.8 V	Power supply pin	

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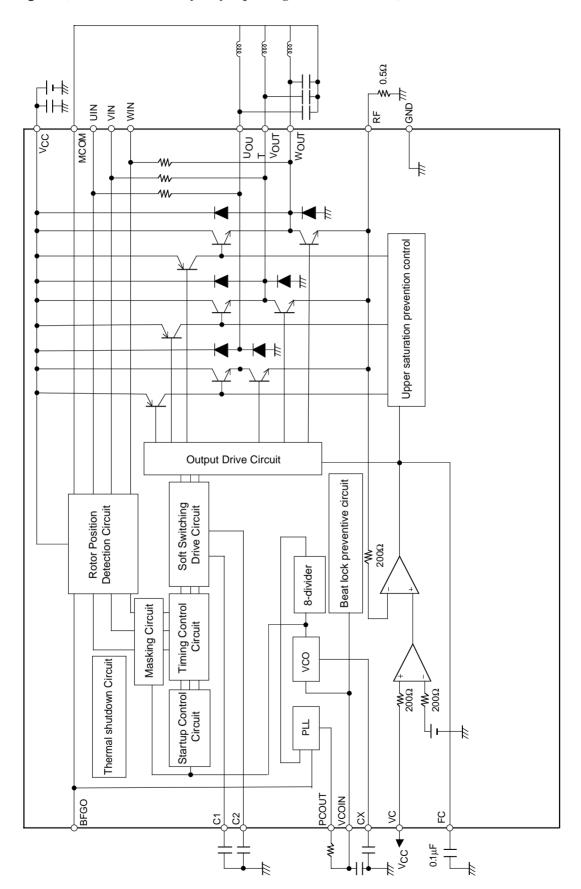
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Pin No.	Symbol	Pin voltage	Description	Equivalent circuit	
3	МСОМ		Middle point input pin of motor coil. The coil waveform is detected with reference to this voltage.	$V_{CC}$ $(20)$ $(1)$ $(2)$	
5	U <sub>IN</sub>				
6	V <sub>IN</sub>	-	Input pin of the coil waveform detection comparator. Connected to each phase output with a built-in resistor of 10 $k\Omega$		
7	W <sub>IN</sub>				
8	C1		Triangular wave generating capacitor connection pin. This triangsular wave causes soft switching of		
9	C2		coil output waveform.		
10	сх		The operating frequency range and minimum operating frequency are determined from the value of capacitor connected to this pin and GND in the VCO circuit.	VREG VCC VREG VIOL VREG	
11	VCOIN		VCO circuit voltage input pin Inputs the PCOUT pin voltage through CR filtering.	VREG	

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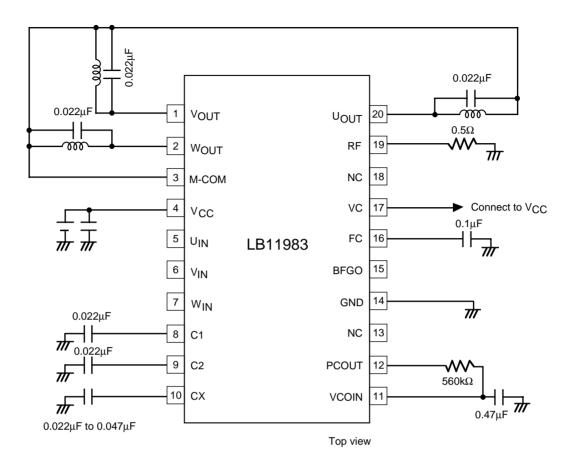
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Pin No.	Symbol	Pin voltage	Description	Equivalent circuit
12	PCOUT	The fold go	VCO circuit PLL output pin.	VREG VCC
14	GND		GND for others than the output transistor.	
15	BFGO		FG output to detect motor reverse feeder voltage. (Composition of three phases)	VREG+VF VREG VREG VREG VREG
16	FC		Frequency characteristics compensation pin. Insertion of a capacitor between this pin and GND stops oscillation of the current control closed loop.	VREG VCC (16) (16) (16) (19) (19) (19) (19) (19) (19) (19) (19
17	VC		Speed control pin. The control is a constant-current control under current feedback from RF. Normally, this pin is connected to V <sub>CC</sub> for use.	VCC ΨΠ0G 200Ω 40kΩ CSHP2 CSHP



Block Diagram (External constant may vary depending on the motor used.)

#### Sample Application Circuit (Reference)



Notes 1. Be sure to connect the VC pin to  $V_{CC}$  directly before use.

2. For the constant of capacitor, etc., our value established through examination is given for reference.

Adjust the value according to the motor to be used when considering this IC.

3. If the output is not oscillated with the motor used, a capacitor inserted between output coil ends is not necessary.

4. Pins 5 through 7 (U<sub>IN</sub>, V<sub>IN</sub>, and W<sub>IN</sub>) are not to be used by a user. These are connected inside IC and should always be kept independent and open.

5. NC pins (14 and 18) are not connected inside IC and can be used as relay pins.

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