

HRW0203A

Silicon Schottky Barrier Diode for Rectifying

HITACHI

Rev. 2
Sep. 1994

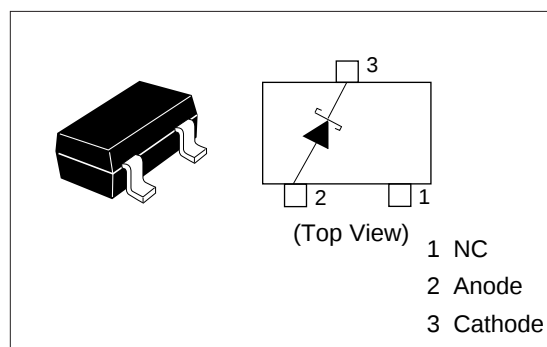
Features

- Low forward voltage drop and suitable for high efficiency rectifying.
- MPAK package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Code
HRW0203A	S 5	MPAK

Pin Arrangement



Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Value	Unit
Repetitive peak reverse voltage	V_{RRM}	30	V
Average forward current	I_O^*	200	mA
Non-Repetitive peak forward surge current	I_{FSM}^{**}	2	A
Junction temperature	T_j	125	°C
Storage temperature	T_{stg}	-55 to +125	°C

* Square wave, Duty (1/2)

** 10msec sine wave 1 pulse

Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse current	I_R	—	—	50	μA	$V_R = 30 V$
Forward voltage	V_F	—	—	0.5	V	$I_F = 200 mA$
Capacitance	C	—	40	—	pF	$V_R = 0 V, f = 1 MHz$

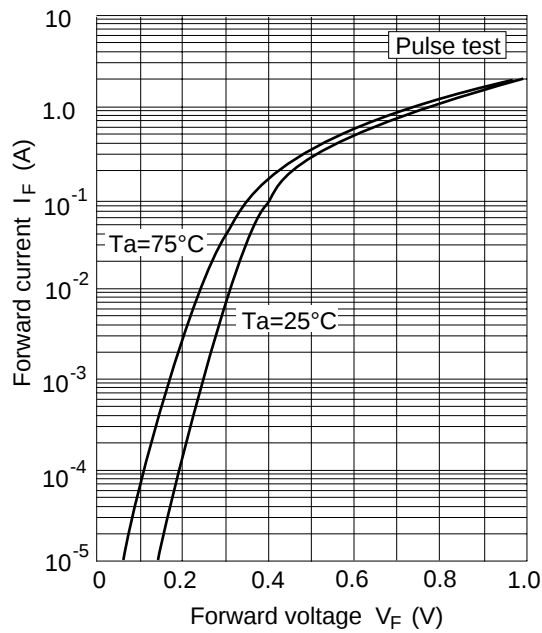


Fig.1 Forward current Vs. Forward voltage

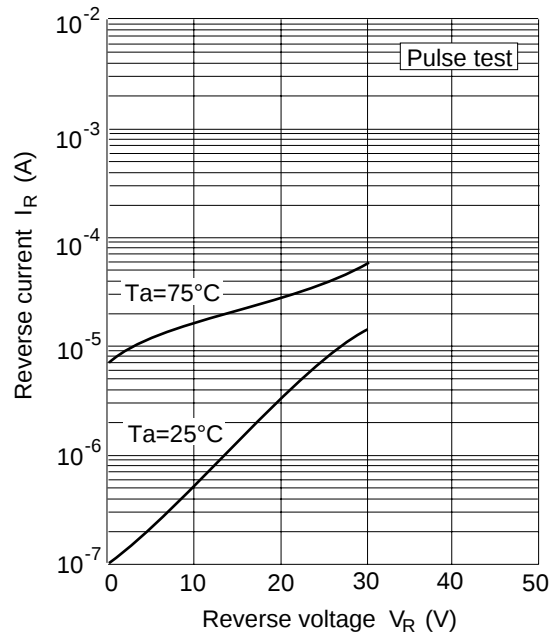


Fig.2 Reverse current Vs. Reverse voltage

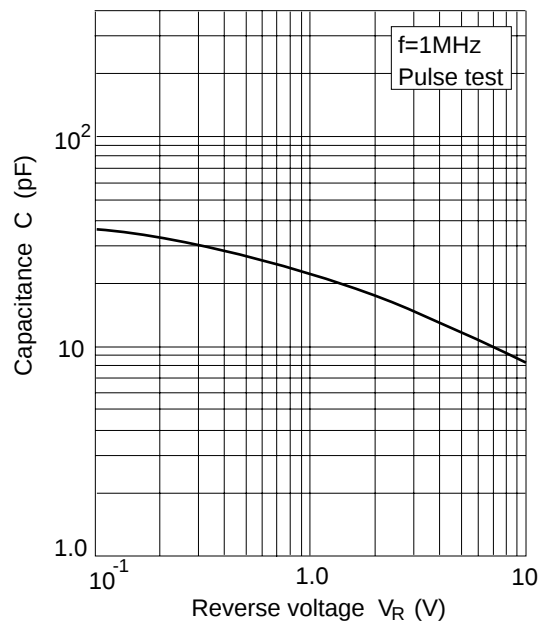


Fig.3 Capacitance Vs. Reverse voltage

Package Dimensions

Unit: mm

