TOSHIBA FIELD EFFECT TRANSISTOR SILICON P CHANNEL MOS TYPE

# S S M 3 J 0 2 F

### POWER MANAGEMENT SWITCH

HIGH SPEED SWITCHING APPLICATIONS

Small Package

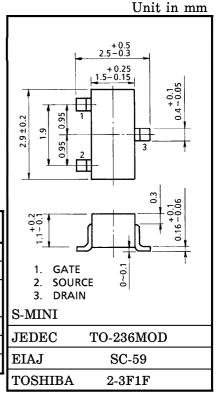
Low on Resistance :  $R_{on} = 0.5 \Omega \text{ (Max.) } (@V_{GS} = -4 \text{ V})$ 

:  $R_{on} = 0.7 \Omega (Max.) (@V_{GS} = -2.5 V)$ 

Low Gate Threshold Voltage

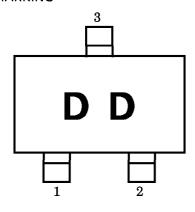
#### MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT	
Drain-Source Voltage		${ m v_{DS}}$	-30	V	
Gate-Source Voltage		$v_{GSS}$	±10	V	
Drain Current	DC	$I_{\mathrm{D}}$	-600	mA	
	Pulse	$I_{ m DP}$	-1200		
Drain Power Dissipation (Ta = 25°C)		$P_{\mathbf{D}}$	200	mW	
Channel Temperature		$\mathrm{T_{ch}}$	150	°C	
Storage Temperature Range		$\mathrm{T_{stg}}$	-55~150	$^{\circ}\mathrm{C}$	

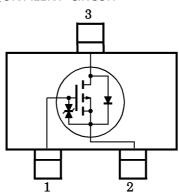


Weight: 0.012 g

#### **MARKING**







## HANDLING PRECAUTION

When handling individual devices (which are not yet mounted on a circuit board), be sure that the environment is protected against electrostatic electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

- TOSHIBA is continually working to improve the quality and the reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to observe standards of safety, and to avoid situations in which a malfunction or failure of a TOSHIBA product could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified
- operating ranges as set forth in the most recent products specifications. Also, please keep in mind the precautions and conditions set forth in the TOSHIBA Semiconductor Reliability Handbook.

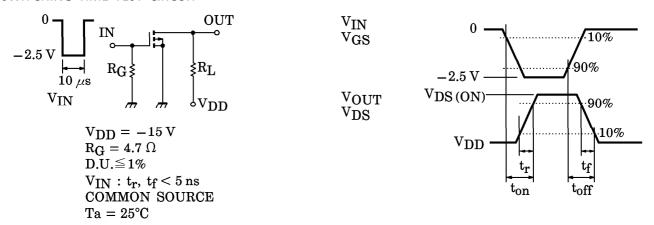
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- The information contained herein is subject to change without notice.

# ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACT	ERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT	
Gate Leakage Current		$I_{ m GSS}$	$V_{GS} = \pm 10 \text{ V}, V_{DS} = 0$	_	_	±1	$\mu$ <b>A</b>	
Drain-Source Breakdown Voltage		V (BR) DSS	$I_D = -1 \text{ mA}, V_{GS} = 0$	-30	_	_	V	
Drain Cut-off C	Drain Cut-off Current $I_{DSS}$ $V_{DS} = -30 \text{ V}, V_{GS} = 0$		_	_	-1	$\mu$ A		
Gate Threshold	Voltage	$ m V_{th}$	$V_{ m DS} = -3   m V,  I_{ m D} = -0.1  mA$	-0.6	_	-1.1	V	
Forward Transfe Admittance	er	Y <sub>fs</sub>	$V_{ m DS} = -3   m V,  I_{ m D} = -0.3   m A   (Note)$	0.6	_	1	S	
Drain-Source ON Resistance		R <sub>DS</sub> (ON)	$I_{\rm D} = -0.3  {\rm A},  {\rm V}_{\rm GS} = -4  {\rm V}   ({ m Note})$		0.4	0.5		
			$I_D = -0.3 \text{ A}, V_{GS} = -2.5 \text{ V}$ (Note)	_	0.55	0.7	Ω	
Input Capacitan	ce	Ciss	$V_{DS} = -10 \text{ V}, V_{GS} = 0,$ f = 1 MHz	_	150	_	pF	
Reverse Transfe Capacitance	r	$\mathrm{C}_{\mathrm{rss}}$	$V_{DS} = -10 \text{ V}, V_{GS} = 0,$ f = 1 MHz		21		pF	
Output Capacita	ance	Coss	$V_{DS} = -10 \text{ V}, V_{GS} = 0,$ f = 1 MHz		61	_	pF	
Switching To	urn-on Time	ton	$V_{DD} = -15 V, I_D = -0.3 A,$		55	_	ns	
Time T	urn-off Time	${ m t_{off}}$	$V_{GS} = 0 \sim -2.5 \text{ V}, R_{G} = 4.7 \Omega$	_	52		115	

(Note): Pulse test

# SWITCHING TIME TEST CIRCUIT



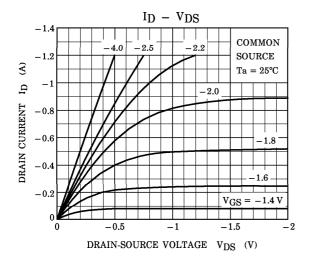
### **PRECAUTION**

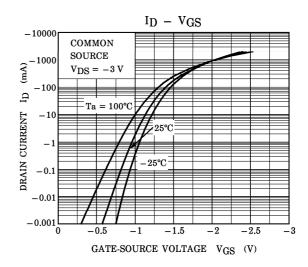
 $V_{th}$  can be expressed as voltage between gate and source when low operating current value is  $I_D=-100\,\mu\text{A}$  for this product. For normal switching operation,  $V_{GS\,(ON)}$  requires higher voltage than  $V_{th}$  and  $V_{GS\,(off)}$  requires lower voltage than  $V_{th}$ .

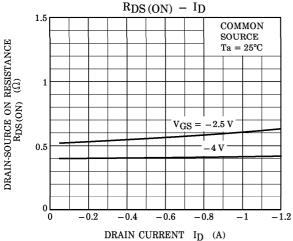
(Relationship can be established as follows:  $V_{GS(off)} < V_{th} < V_{GS(ON)}$ )

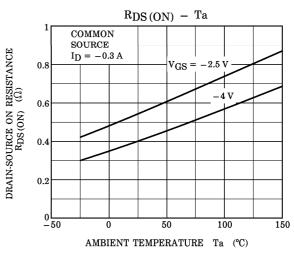
Please take this into consideration for using the device.

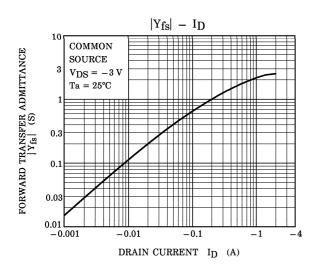
 $V_{GS}$  recommended voltage of  $-2.5\,\mathrm{V}$  or higher to turn on this product.

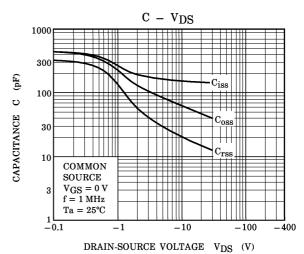


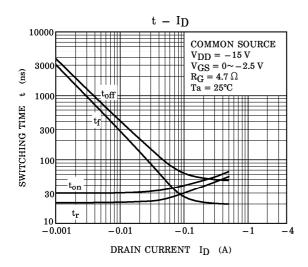


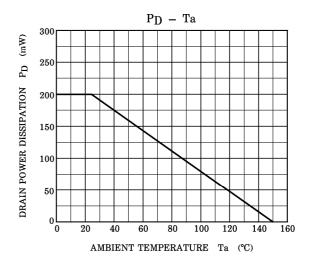


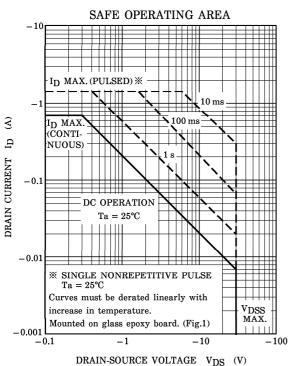












(Fig.1):  $25.4 \, \text{mm} \times 25.4 \, \text{mm} \times 1.6 \, \text{t}$  (a Cu pad of  $0.8 \, \text{mm}^2$  area)

