

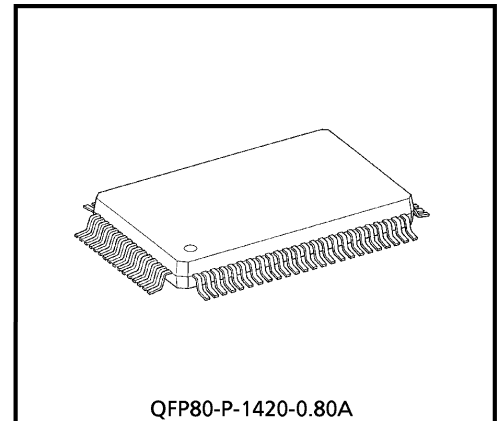
T6M72, JT6M72-AS

T6M72, JT6M72-AS CMOS SINGLE-CHIP LSI FOR LCD CALCULATOR

The T6M72, JT6M72-AS is a CMOS single-chip microcomputer for 14-digit or 12-digit 2-memory calculator.

T6M72, JT6M72-AS is the complete single-chip CMOS LSI for calculator with single power supply operation. Wide operating voltage range and low-power consumption make it suitable for 1.5 V solar battery operated.

Besides T6M72, JT6M72-AS can selectable with a pin-programmable to function of Power timer and Memory hold. With the following features.



QFP80-P-1420-0.80A

Weight : 1.52 g (Typ.)

FEATURES

- Display : 14 digits or 12 digits (selectable with a pin-programmable) of data, 2 digits of sign, error symbol, memory load symbol.
- Algebraic mode.
- Standard 4 functions (+, -, ×, ÷)
- Memory and grand total (GT) memory calculation.
- Accumulating GT memory register with count up (down) item counter.
- Automatic percentage operation with add-on, discount.
- Automatic delta percentage, mark-up and markdown operations.
- Square root.
- Constant calculation.
- Chain calculation.
- Change sign.
- Floating point or momentary mode (selectable with a switch).
- Fixed point ("0", "1", "2", "3", "4" or "6" places) or floating point (selectable with a switch).
- Adding point mode (selectable with a switch).

980910EBA2

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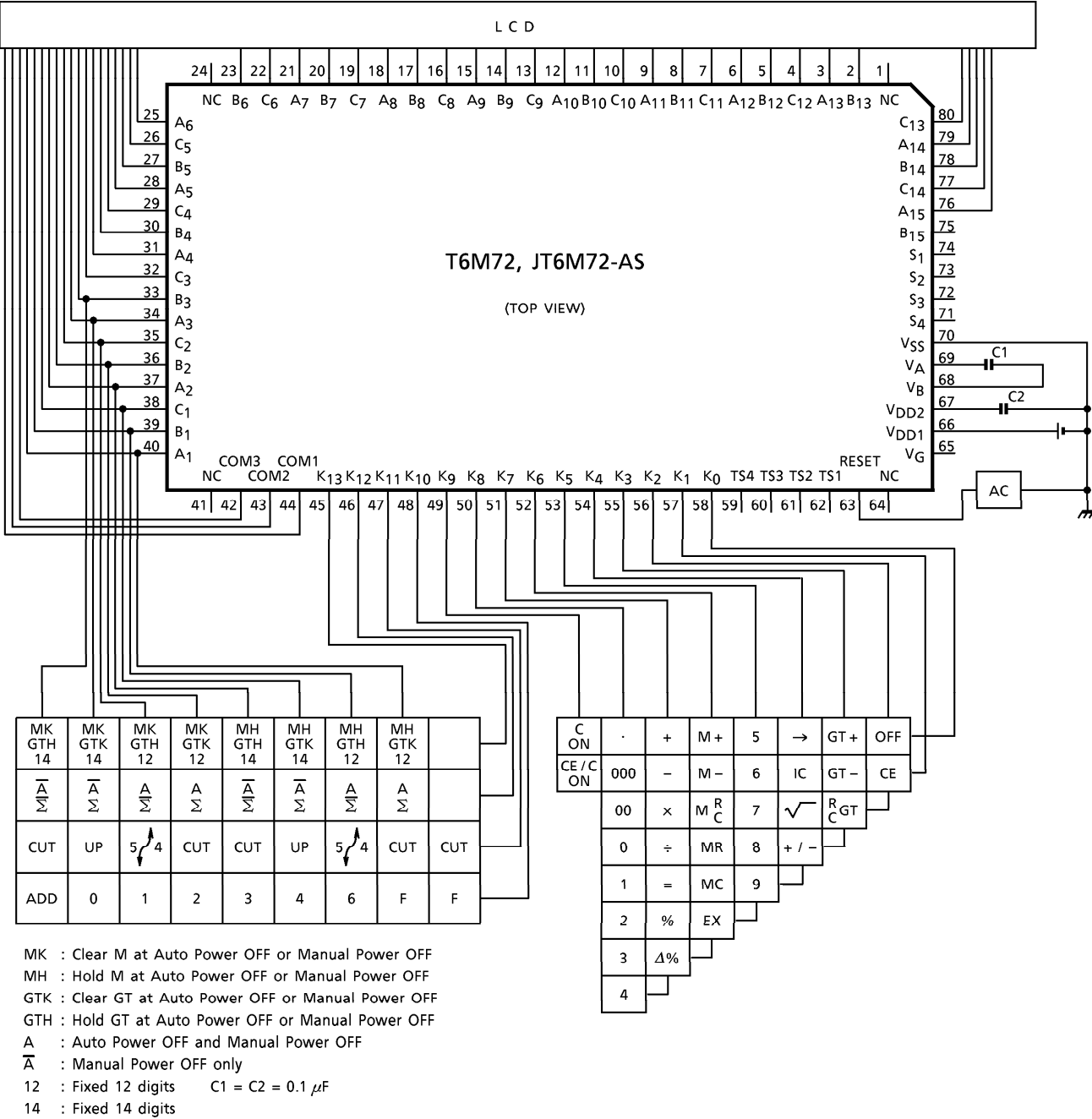
- Rounding switches (rounding up, down and off).
- Leading zero suppression.
- Trailing zero suppression.
- Punctuation on display, commas for thousands.
- Memory and GT memory contents indicator, turned on with non-zero in the memory and GT memory.
- Registration overflow, indicating that too many digits are entered (the most significant digit are protected).
- Result overflow, indicating during calculation (most function key are locked as it happened).
- Memory overflow indicating to flashing of memory load mark.
- Key roll over function.
- Floating minus.

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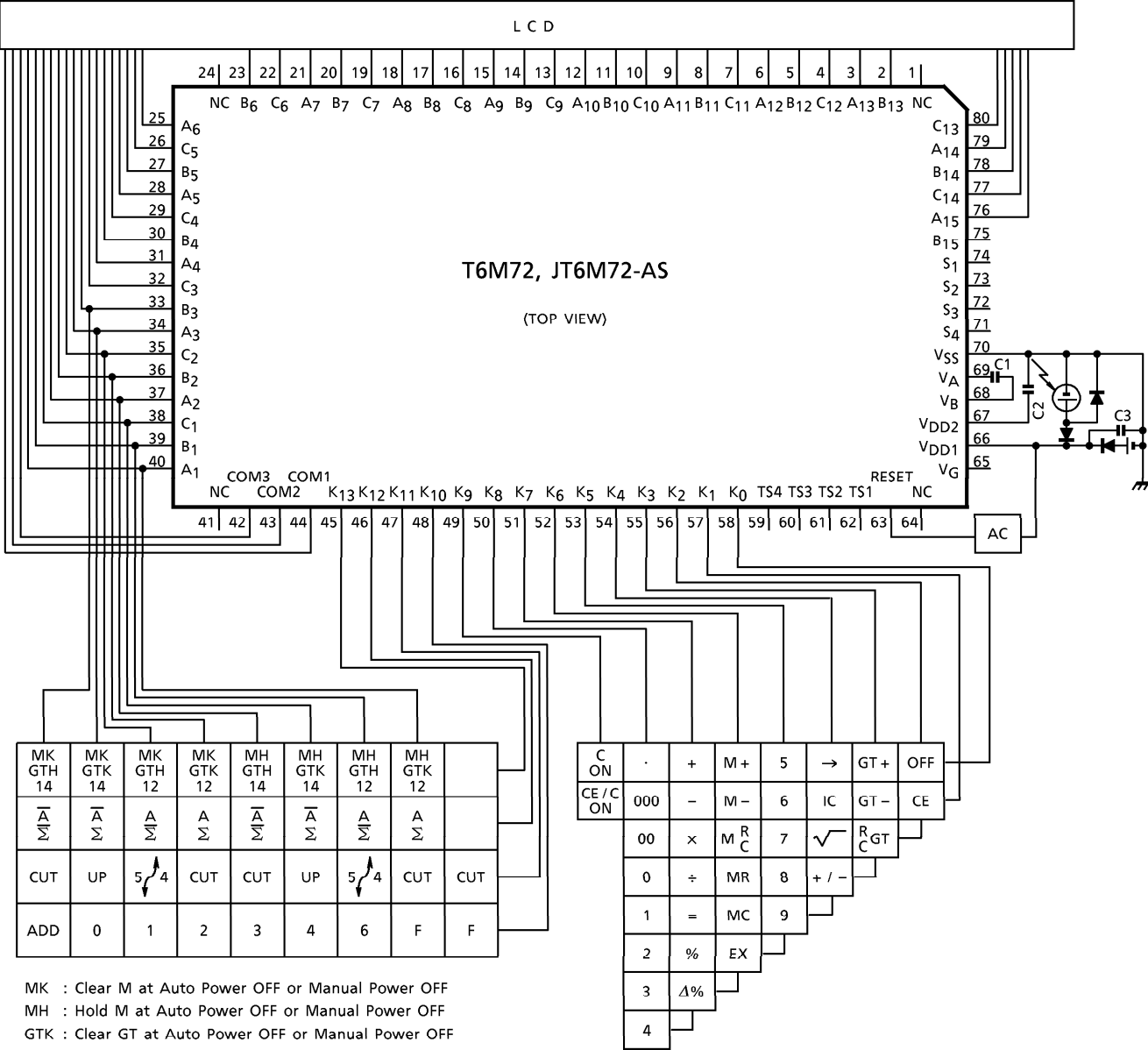
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SYSTEM BLOCK DIAGRAM

Battery Type

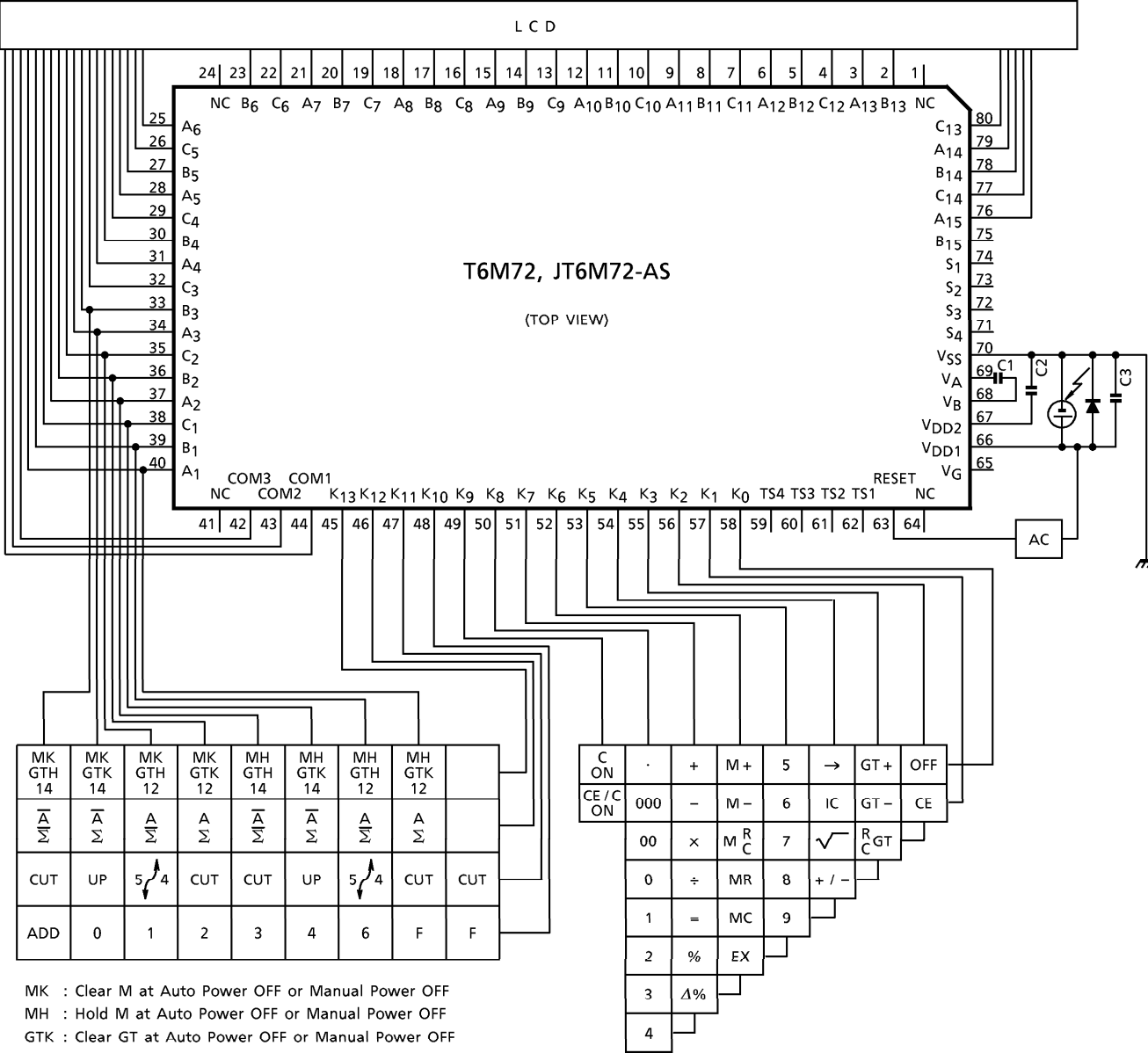


Dual Type



MK : Clear M at Auto Power OFF or Manual Power OFF
MH : Hold M at Auto Power OFF or Manual Power OFF
GTK : Clear GT at Auto Power OFF or Manual Power OFF
GTH : Hold GT at Auto Power OFF or Manual Power OFF
A : Auto Power OFF and Manual Power OFF
 $\bar{A}$: Manual Power OFF only
12 : Fixed 12 digits C1 = C2 = 0.1 μ F
14 : Fixed 14 digits C3 = 10 μ F

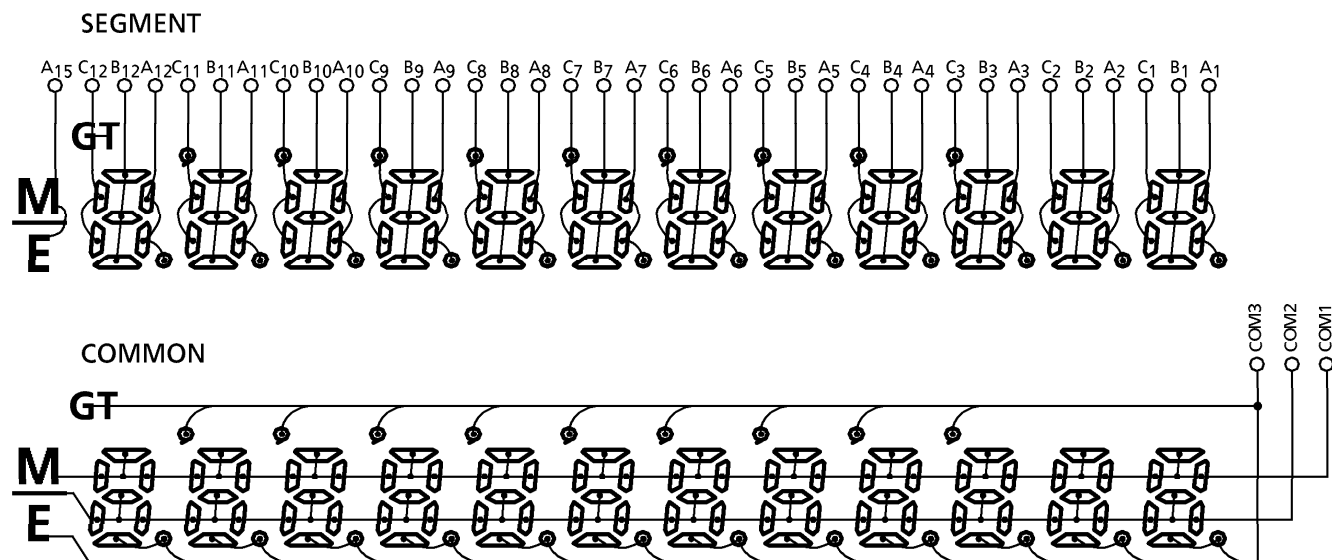
Solar Type



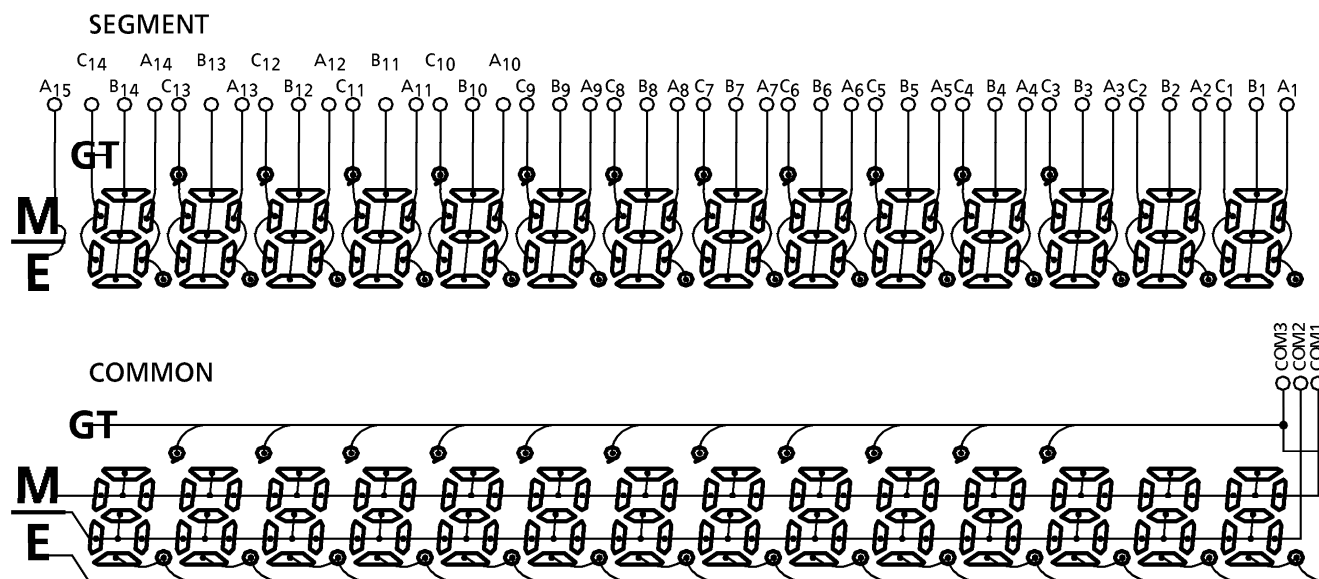
MK : Clear M at Auto Power OFF or Manual Power OFF
MH : Hold M at Auto Power OFF or Manual Power OFF
GTK : Clear GT at Auto Power OFF or Manual Power OFF
GTH : Hold GT at Auto Power OFF or Manual Power OFF
A : Auto Power OFF and Manual Power OFF
 $\bar{A}$: Manual Power OFF only
12 : Fixed 12 digits C1 = C2 = 0.1 μ F
14 : Fixed 14 digits C3 = 10 μ F

CONNECTION OF LCD

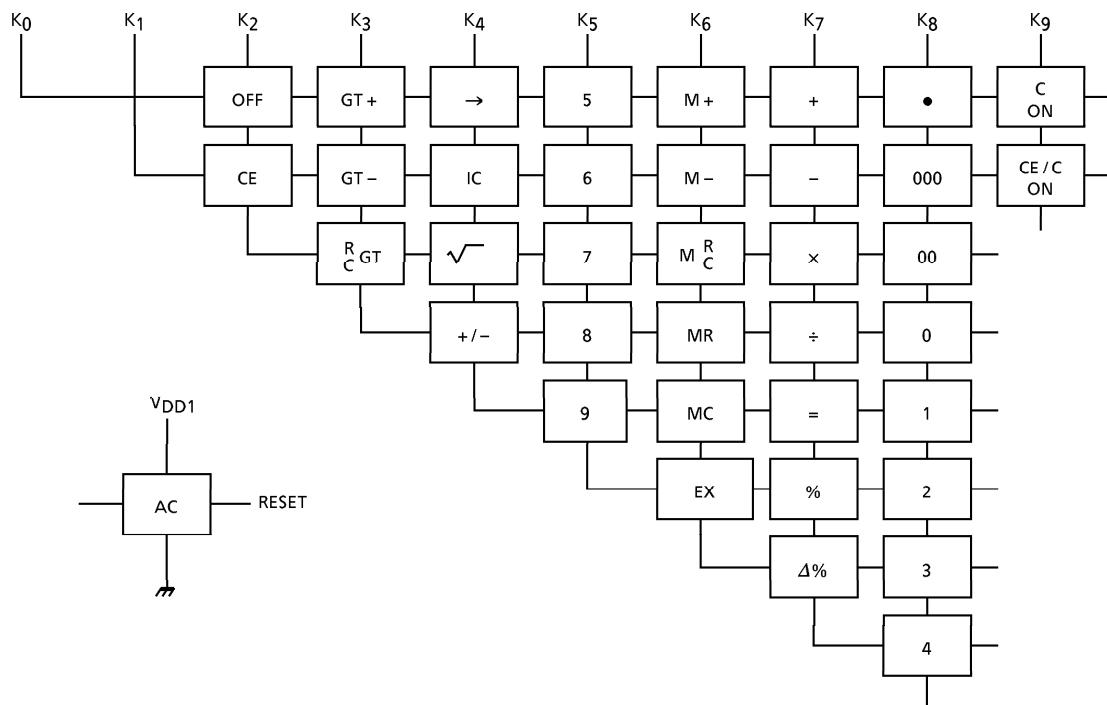
(1) Select of 12 digits



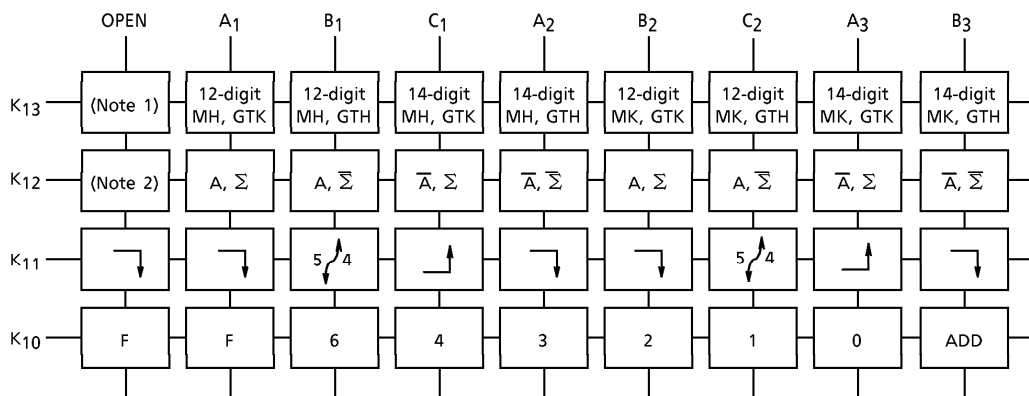
(2) Select of 14 digits



KEY CONNECTION



Touch Key

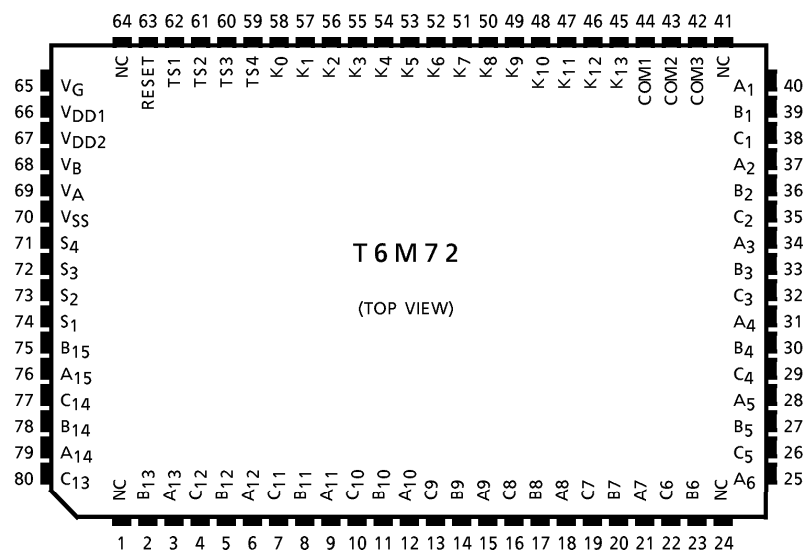


Lock Key

- K13 Selectable with Calculated Digits and Memory Hold Status.
 MH (Memory Hold), MK (Memory Kill), GTH (GT memory Hold) and GTK (GT memory Kill) at Auto Power OFF or OFF Key.
- K12 Selectable with Auto Power OFF mode and Total switch.
- K11 Rounding switches.
- K10 Selectable with Fixed point or Floating mode.

(Note 1, Note 2) : K12 or K13 line is No choose then keep condition.
 K12 or K13 line is No choose at the system power on then initial condition is 12-digits $\overline{A}$, $\overline{\Sigma}$ Mode selected.

PIN CONNECTION



SPECIFICATION OF CALCULATOR

Speed of calculation

Numeral		22.4~38.5 ms
Function	{ 1 $\boxed{+}$ 1 $\boxed{+}$ 2 $\boxed{+}$	45.5 ms 80.7 ms
Addition and Subtract	{ 1 2 3 $\boxed{+}$ 1 $\boxed{=}$ 99999999999999 $\boxed{-}$ 0.0000000000001 $\boxed{=}$	106.2 ms 135.4 ms
Multiply	{ 1 2 3 $\boxed{\times}$ 2 $\boxed{=}$ 1 $\boxed{\times}$ 99999999999999 $\boxed{=}$	125.9 ms 291.9 ms
Device	{ 1 2 3 $\boxed{\div}$ 3 $\boxed{=}$ 99999999999999 $\boxed{\div}$ 1 $\boxed{=}$	171.2 ms 334.8 ms
Memory calculation	{ 2 $\boxed{M+}$ 99999999999999 $\boxed{\div}$ 1 $\boxed{M+}$	90.5 ms 329.1 ms
Square root	{ 99999999999999 $\boxed{\sqrt{}}$ 2 $\boxed{\sqrt{}}$	132.6 ms 131.7 ms

Operation Example

1. Fixed point calculations

① Key	Display	Fixed point Place
C	0.	DP = 3 (5 / 4)
2	2.	
÷	2.	
3	3.	
=	0.667	
2	2.	
·	2.	
3	2.3	
+	2.3	
4	4.	
M +	6.300	
1	1.	
·	1.	
2	1.2	
M +	1.200	
MR	7.5	DP = 4

② Key	Display	Fixed point Place
C	0.	DP = 0 (↱)
1	1.	
·	1.	
2	1.2	
3	1.23	
+	1.23	
1	1.	
·	1.	
1	1.1	
=	3.	
9	9.	
√	3.	
×	3.	
1	1.	
·	1.	
1	1.1	DP = F
=	3.3	

2. Adding point mode calculations

Key	Display	Key	Display	Key	Display
$\boxed{C}$	0.	$\boxed{M+}$	0.02M	$\boxed{=}$	33.27M –
1	1.	3	3.M	2	2.M
23	123	$\boxed{\cdot}$	3.M	$\boxed{+}$	0.02M
$\boxed{+}$	1.23	123	3.123M	9	9.M
3	3.	$\boxed{M+}$	3.12M	$\boxed{\cdot}$	9.M
$\boxed{=}$	1.26	$\boxed{MR}$	3.14M	$\boxed{\sqrt{}}$	3.M
3	3.	$\boxed{C}$	0.M	$\boxed{=}$	3.02M
2	32.	1	1.M		
$\boxed{\times}$	32.	23	123M		
3	3.	$\boxed{-}$	1.23M		
$\boxed{\cdot}$	3.	3	3.M		
000	3.000	4	34.M		
$\boxed{=}$	96.00	$\boxed{\cdot}$	34.M		
2	2.	5	34.5M		

3. Constant calculations

① Multiplication

Key	Display	Constant
k	k	
$\boxed{\times}$	k	
a	a	
$\boxed{=}$	k·a	k ×
b	b	k ×
$\boxed{=}$	k·b	k ×

③ Addition

a	a	
$\boxed{+}$	a	
k	k	
$\boxed{=}$	a + k	+ k
b	b	+ k
$\boxed{=}$	b + k	+ k

② Division

Key	Display	Constant
a	a	
$\boxed{\div}$	a	
k	k	
$\boxed{=}$	a / k	÷ k
b	b	÷ k
$\boxed{=}$	b / k	÷ k

④ Subtraction

a	a	
$\boxed{-}$	a	
k	k	
$\boxed{=}$	a – k	– k
b	b	– k
$\boxed{=}$	b – k	– k

⑤ Percentage

Key	Display	Constant
k	k	
$\times$	k	
a	a	
%	$k \cdot a / 100$	k x
b	b	k x
%	$k \cdot b / 100$	k x

⑦ Add-on

k	k	
+	k	
a	a	
%	$k \cdot (1 + a / 100)$	k +
b	b	k +
%	$k \cdot (1 + b / 100)$	k +

4. Δ% calculations

① Key Display

a	a
+	a
b	b
Δ%	$100 \cdot (a + b) / b$

5. Mark-up, mark-down calculations

① Mark-up

Key	Display
a	a
÷	a
b	b
Δ%	$a / (1 - b / 100)$
Δ%	$ a / (1 - b / 100) $

⑥ Percentage

Key	Display	Constant
a	a	
÷	a	
k	k	
%	$100 \cdot a / k$	÷ k
b	b	÷ k
%	$100 \cdot b / k$	÷ k

⑧ Discount

k	k	
-	k	
a	a	
%	$k \cdot (1 - a / 100)$	k -
b	b	k -
%	$k \cdot (1 - b / 100)$	k -

② Key Display

a	a
-	a
b	b
Δ%	$100 \cdot (a - b) / b$

② Mark-down

Key	Display
a	a
÷	a
b	b
+/-	- b
Δ%	$a / (1 + b / 100)$
Δ%	$a / (1 + b / 100) - a$

6. Add-on, discount calculations

Add-on

Key	Display
① a	a
$\times$	a
b	b
$\%$	$a \cdot b / 100$
$+$	$a \cdot b / 100$
$=$	$a (1 + b / 100)$
③ a	a
$+$	a
b	b
$\%$	$a (1 + b / 100)$
⑤ a	a
$\times$	a
b	b
$\Delta\%$	$a \cdot (1 + b / 100)$

Discount

Key	Display
② a	a
$\times$	a
b	b
$\%$	$a \cdot b / 100$
$-$	$a \cdot b / 100$
$=$	$a (1 - b / 100)$
④ a	a
$-$	a
b	b
$\%$	$a (1 - b / 100)$
⑥ a	a
$\times$	a
b	b
$+/-$	-b
$\Delta\%$	$a (1 - b / 100)$

7. Average operation use of the item counter

Key	Display	Item Counter
a	a	0
$+$	a	1
b	b	1
$+$	a + b	2
c	c	2
$+$	a + b + c	3
d	d	3
$+$	a + b + c + d	4

Key	Display	Item Counter
$-$	a + b + c + d	2
d	d	2
$+$	a + b + c	3
e	e	3
$=$	a + b + c + e	4
$\div$	a + b + c + e	4
IC	4	4
$=$	$(a + b + c + e) / 4$	5

MAXIMUM RATINGS

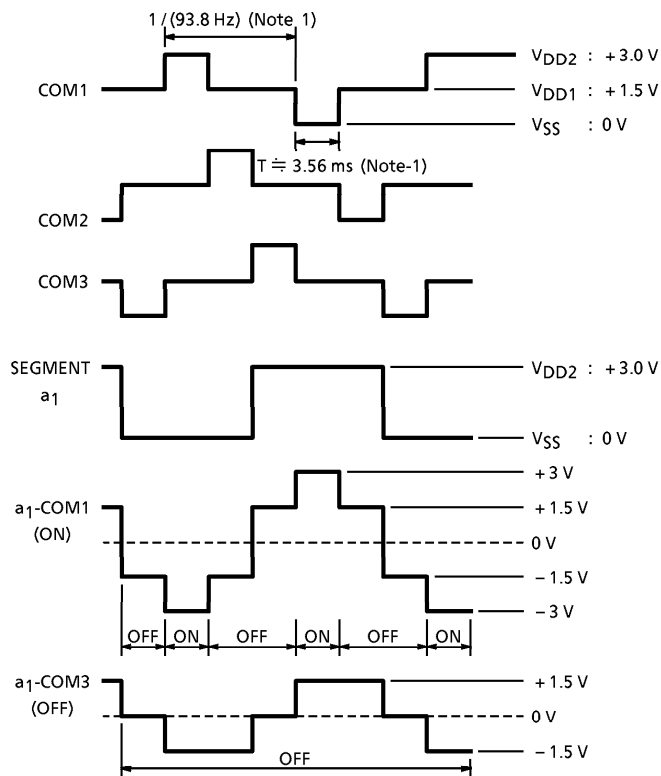
CHARACTERISTICS	SYMBOL	RATING	UNIT
Supply Voltage	V_{DD1}	$-0.3 \sim 2.0$	V
Input Voltage	V_{IN}	$-0.3 \sim V_{DD1} + 0.3$	V
Operating Temperature	T_{opr}	$0 \sim 40$	°C
Storage Temperature	T_{stg}	$-55 \sim 125$	°C

ELECTRICAL CHARACTERISTICS ($V_{DD1} = 1.5 \pm 0.2$ V, $V_{DD2} = 3.0 \pm 0.4$ V, $V_{SS} = 0$ V, $T_a = 25^\circ\text{C}$)

CHARACTERISTICS	SYMBOL	TEST CIR-CUIT	PIN NAME	TEST CONDITION	MIN	TYP.	MAX	UNIT
Operating Voltage	V_{DD1}	—	—	—	1.2	1.5	2.0	V
"1" Input Voltage	$V_{IH}(1)$	—	K ₂ ~K ₉ RESET	—	$V_{DD1} - 0.4$	—	V_{DD1}	V
"1" Input Voltage	$V_{IH}(2)$	—	K ₁₀ ~K ₁₃	—	$V_{DD2} - 0.4$	—	V_{DD2}	V
"0" Input Voltage	V_{IL}	—	K ₂ ~K ₁₃ RESET	—	0	—	0.4	V
"1" Output Voltage	$V_{OH}(1)$	—	SEGMENT COM1~3	—	$V_{DD2} - 0.2$	—	V_{DD2}	V
"0" Output Voltage	$V_{OL}(1)$	—	SEGMENT COM1~3	—	0	—	0.2	V
"M" Output Voltage	V_{OM}	—	COM1~3	—	$V_{DD1} - 0.2$	—	$V_{DD1} + 0.2$	V
"1" Output Voltage	$V_{OH}(2)$	—	K ₁ ~K ₉	—	$V_{DD1} - 0.2$	—	V_{DD1}	V
"0" Output Voltage	$V_{OL}(2)$	—	K ₁ ~K ₁₃	—	0	—	0.2	V
"1" Output Resistance	R_{OH}	—	SEGMENT COM1~3	$V_{OUT} = V_{DD2} - 0.5$ V	—	—	70	k Ω
"0" Output Resistance	R_{OL}	—	SEGMENT COM1~3	$V_{OUT} = 0.5$ V	—	—	70	k Ω
KEY Pull Up Resistance	$R_{KEYH}(1)$	—	RESET	$V_{OUT} = V_{DD1} - 0.5$ V	—	—	25	k Ω
	$R_{KEYH}(2)$	—	K ₀ ~K ₉	$V_{OUT} = V_{DD1} - 0.5$ V	—	—	14	k Ω
	$R_{KEYH}(3)$	—	K ₁₀ ~K ₁₃	$V_{OUT} = 0$ V	120	—	800	k Ω
KEY Pull Down Resistance	$R_{KEYL}(1)$	—	RESET (1)	$V_{OUT} = V_{DD1}$	100	—	300	k Ω
	$R_{KEYL}(2)$	—	RESET (2)	$V_{OUT} = V_{DD1}$	18	—	300	k Ω
	$R_{KEYL}(3)$	—	K ₀ ~K ₉ (1)	$V_{OUT} = 0.5$ V	—	—	50	k Ω
	$R_{KEYL}(4)$	—	K ₀ ~K ₉ (2)	$V_{OUT} = V_{DD1}$	72	—	170	k Ω
Oscillating (WAIT)	$f_{\phi WAIT}$	—	—	$V_{DD1} = 1.5$ V	5.4	9.0	15.5	kHz
Frequency (OPERATE)	$f_{\phi OP}$	—	—	$V_{DD1} = 1.5$ V	20.0	34	61.3	kHz
Frame Frequency	f_F	—	SEGMENT COM1~3	$V_{DD1} = 1.5$ V	56.3	93.8	161.5	Hz

CHARACTERISTICS		SYMBOL	TEST CIR- CUIT	PIN NAME	TEST CONDITION	MIN	TYP.	MAX	UNIT
Supply Current	1 (WAIT)	I_{DDWAIT}	—	—	$V_{DD1} = 1.5\text{ V}$	—	—	3.3	μA
	2 (OPERATE)	I_{DDOP}	—	—	$V_{DD1} = 1.2\text{ V}$	—	—	8.9	μA
	3 (OFF)	I_{DDOFF}	—	—	$V_{DD1} = 1.5\text{ V}$	—	—	2.0	μA
Power Off Timer Times		T	—	—	$V_{DD1} = 1.5\text{ V}$	429	600	1001	s

WAVEFORMS FOR DISPLAY



(Note 1) : at $f_{\phi} = 9\text{ kHz}$

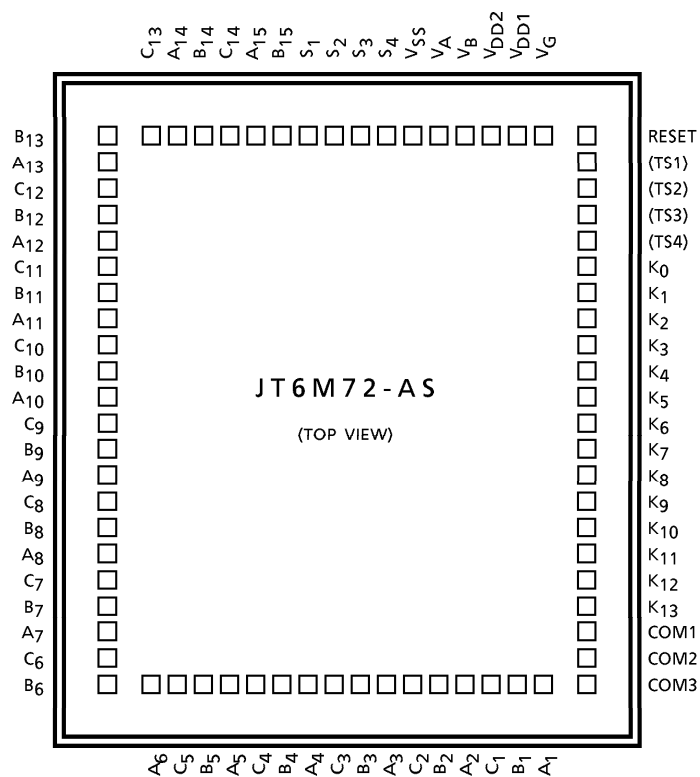
PAD LOCATION TABLE

(μm)

NAME	X POINT	Y POINT	NAME	X POINT	Y POINT
B ₆	- 1757	- 1680	RESET	1757	1680
C ₆	- 1757	- 1520	(TS1)	1757	1520
A ₇	- 1757	- 1360	(TS2)	1757	1360
B ₇	- 1757	- 1200	(TS3)	1757	1200
C ₇	- 1757	- 1040	(TS4)	1757	1040
A ₈	- 1757	- 880	K ₀	1757	880
B ₈	- 1757	- 720	K ₁	1757	720
C ₈	- 1757	- 560	K ₂	1757	560
A ₉	- 1757	- 400	K ₃	1757	400
B ₉	- 1757	- 240	K ₄	1757	240
C ₉	- 1757	- 80	K ₅	1757	80
A ₁₀	- 1757	80	K ₆	1757	- 80
B ₁₀	- 1757	240	K ₇	1757	- 240
C ₁₀	- 1757	400	K ₈	1757	- 400
A ₁₁	- 1757	560	K ₉	1757	- 560
B ₁₁	- 1757	720	K ₁₀	1757	- 720
C ₁₁	- 1757	880	K ₁₁	1757	- 880
A ₁₂	- 1757	1040	K ₁₂	1757	- 1040
B ₁₂	- 1757	1200	K ₁₃	1757	- 1200
C ₁₂	- 1757	1360	COM1	1757	- 1360
A ₁₃	- 1757	1520	COM2	1757	- 1520
B ₁₃	- 1757	1680	COM3	1757	- 1680
C ₁₃	- 1089	1753	A ₁	1122	- 1752
A ₁₄	- 929	1753	B ₁	962	- 1752
B ₁₄	- 769	1753	C ₁	802	- 1752
C ₁₄	- 609	1753	A ₂	642	- 1752
A ₁₅	- 449	1753	B ₂	482	- 1752
B ₁₅	- 289	1753	C ₂	322	- 1752
S ₁	- 129	1753	A ₃	162	- 1752
S ₂	31	1753	B ₃	2	- 1752
S ₃	191	1753	C ₃	- 158	- 1752
S ₄	351	1753	A ₄	- 318	- 1752
V _{SS}	511	1753	B ₄	- 478	- 1752
V _A	671	1753	C ₄	- 638	- 1752
V _B	831	1753	A ₅	- 798	- 1752
V _{DD2}	991	1753	B ₅	- 958	- 1752
V _{DD1}	1151	1753	C ₅	- 1118	- 1752
V _G	1388	1753	A ₆	- 1278	- 1752

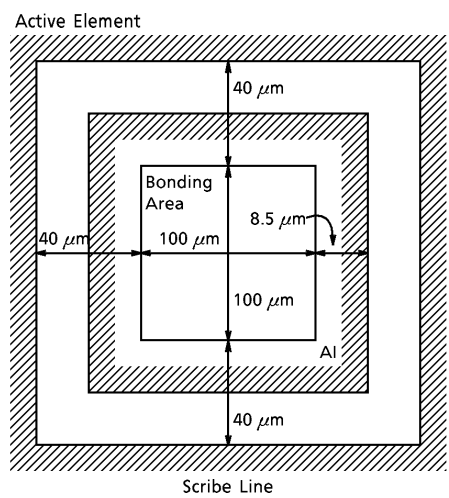
(Note) : () Do not connect.

CHIP LAYOUT



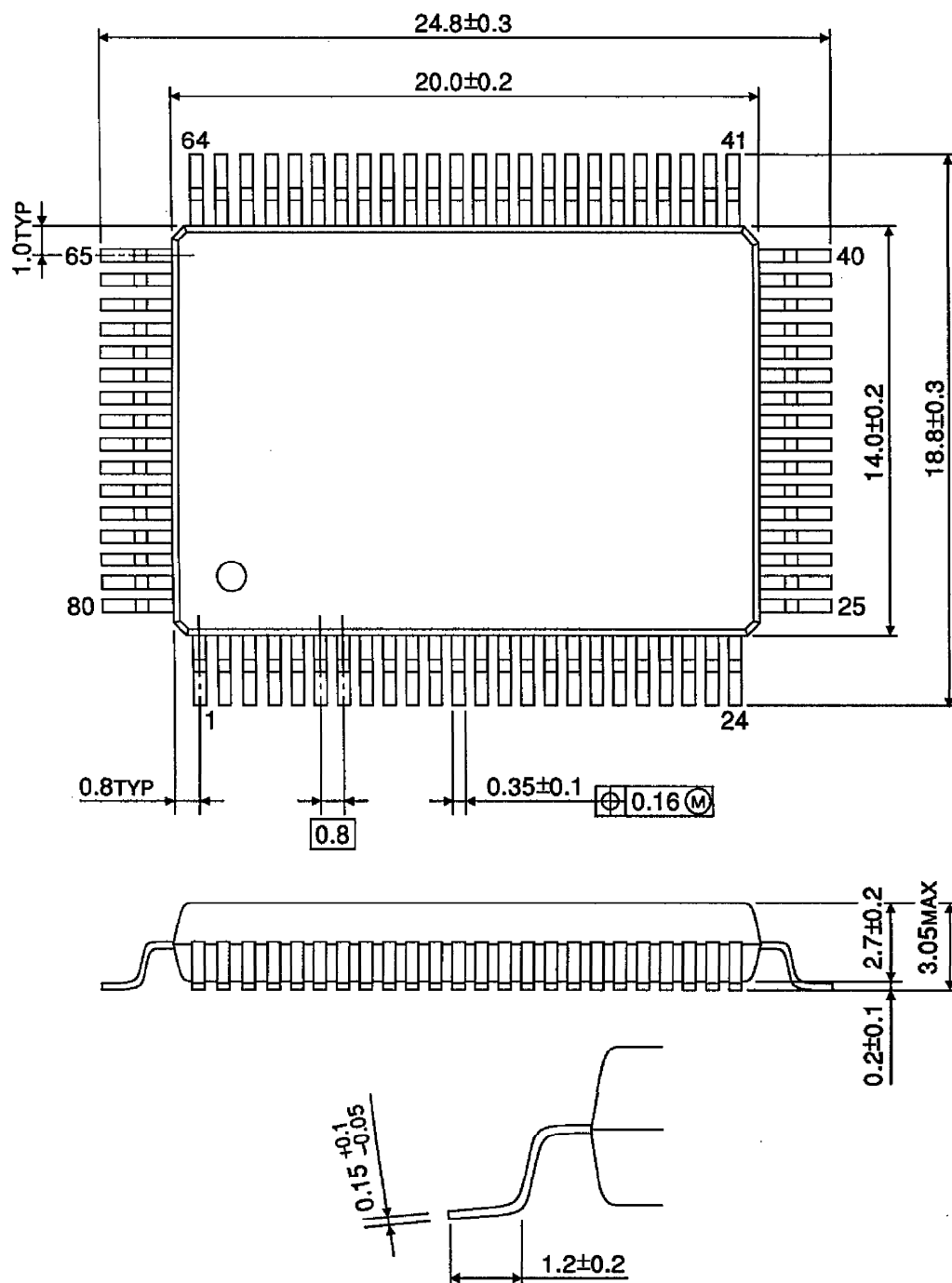
Chip size : 3.79 × 3.84 (mm)
Chip thickness : 440 ± 30 (μm)
Substrate : VSS

PAD LAYOUT



PACKAGE DIMENSIONS
QFP80-P-1420-0.80A

Unit : mm



Weight : 1.52 g (Typ.)