TOSHIBA GATE TURN-OFF THYRISTOR

## SG3000JX24

## INVERTER APPLICATION

Repetitive Peak Off-State Voltage: VDRM=6000V

R.M.S On-State Current  $: I_{T(RMS)} = 1200A$ 

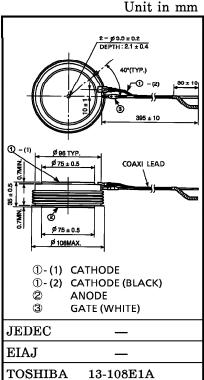
Peak Turn-Off Current :  $I_{TGQM} = 3000A$ 

Critical Rate of Rise of On-State Current :  $di/dt=400A/\mu s$ 

Critical Rate of Rise of Off-State Voltage: dv/dt=1350V/\mu s

## **MAXIMUM RATINGS**

CHARACTERISTIC	SYMBOL	RATING	UNIT
Repetitive Peak Off-State Voltage (Note 1)	$v_{ m DRM}$	6000	V
Repetitive Peak Reverse Voltage	$v_{RRM}$	16	V
Peak Turn-Off Current (Note 2)	ITGQM	3000	Α
R.M.S On-State Current (Note 3)	I <sub>T (RMS)</sub>	1200	A
Peak One Cycle Surge On-State Current (Non-Repetitive, 10ms Width Half Sine Waveform)	$I_{ ext{TSM}}$	16000	A
Critical Rate of Rise of On-state Current (Note 4)	di/dt	400	A/μs
Peak Forward Gate Current	$I_{FGM}$	100	Α
Average Forward Gate Power Dissipation	P <sub>FG (AV)</sub>	50	W
Average Reverse Gate Power Dissipation	P <sub>RG</sub> (AV)	150	W
R.M.S Gate Current (Note 5)	I <sub>G</sub> (RMS)	50	A
Peak Reverse Gate Voltage (at Static)	$v_{ m RGM}$	16	V
Operating Junction Temperature Range	$T_{j}$	-40~125	°C
Storage Temperature Range	$\mathrm{T_{stg}}$	-40~150	°C
Mounting Force	_	33.3±4.9	kN



Weight: 1700g

- $V_{GK} = -2V$ Note 1.
- $V_{DM}^{=5000V},~C_S^{=6}\mu F,~R_S^{=5}\Omega,~di_{GQ}^{}/dt^{=50A}/\mu s,~V_{DSP}^{} \leq 850V,~L_S^{} \leq 0.3\mu H$  50Hz Half Sine Waveform at  $T_f^{=76}^{\circ}C$ Note 2.
- Note 3.
- Note 4.  $V_D = 1/2V_{DRM}, I_{GM} = 25A$
- Note 5. Ambient Temperature of coaxial gate-cathode lead = 90°C

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## **ELECTRICAL CHARACTERISTICS**

CHARACTERISTIC	SYMBOL	TEST CONDITION		MIN.	TYP.	MAX.	UNIT
Repetitive Peak Off-State Current	$I_{ m DRM}$	$V_{DRM} = Rated, V_{GK} = -2V,$ $T_j = 125$ °C		_	_	150	mA
Repetitive Peak Reverse Current	$I_{ m RRM}$	V <sub>RRM</sub> =Rated, T <sub>j</sub> =125°C		_		10	mA
Repetitive Peak Reverse Gate Current	$I_{ m RGM}$	$V_{ m RGM} = 16 V, T_{ m j} = 125 { m ^{\circ} C}$		_	1	10	mA
Peak On-State Voltage	$v_{ m TM}$	$I_{TM} = 3000A, T_j = 125$ °C		_	-	4.3	V
Gate Trigger Voltage	$v_{GT}$	$V_D$ = 24V, $R_L$ = 0.1 $\Omega$	$T_j = -40^{\circ}C$	_	-	1.7	V
			$T_j = 25$ °C	_	ı	1.5	
Gate Trigger Current	$I_{\mathrm{GT}}$		$T_j = -40$ °C	_		10	A
			$T_j = 25$ °C	_	l	3.5	
Turn-On Delay Time	<sup>t</sup> d	$V_{D} = 1/2V_{DRM}$ , $I_{TM} = 3000A$ , $di/dt = 400A/\mu s$ , $I_{GM} = 25A$ , $T_{j} = 25$ °C		_	1	3	$\mu$ s
Turn-On Time	tgt			_		10	$\mu$ s
Critical Rate of Rise of Off- State Voltage	dv/dt	$V_{ m DRM}$ =2/3RATED, T <sub>j</sub> =125°C, $V_{ m GK}$ =-2V		1350		_	V/μs
Storage Time	$t_S$	$\begin{split} &I_{TGQ}\!=\!3000\text{A}, V_{DM}\!=\!5000\text{V},\\ &V_{D}\!=\!1/2V_{DRM},\\ &\text{di}_{GQ}/\text{dt}\!=\!50\text{A}/\mu\text{s},\\ &C_{S}\!=\!6\mu\text{F},R_{S}\!=\!5\Omega,\\ &T_{j}\!=\!125^{\circ}\text{C},L_{S}\!\!\leq\!0.3\mu\text{F} \end{split}$		_	<b>—</b>	28	$\mu$ s
Gate Turn-Off Time	$t_{gq}$			_		30	$\mu$ s
Tail Time	t <sub>tail</sub>			_	-	115	$\mu$ s
Gate Turn-Off Current	$I_{ ext{GQ}}$			_	800	_	Α
Thermal Resistance (Junction to Fin)	R <sub>th (j-f)</sub>	DC		_	_	0.017	°C/W

