

DATA SHEET

BFG505W; BFG505W/X; BFG505W/XR NPN 9 GHz wideband transistors

Product specification
Supersedes data of 1998 Oct 02

2000 Oct 30

NPN 9 GHz wideband transistors

BFG505W; BFG505W/X;
BFG505W/XR

FEATURES

- High power gain
- Low noise figure
- High transition frequency
- Gold metallization ensures excellent reliability.

APPLICATIONS

RF front end applications in the GHz range, such as analog and digital cellular telephones, cordless telephones (CT2, CT3, PCN, DECT, etc.), radar detectors, pagers, satellite television tuners (SATV).

DESCRIPTION

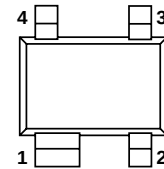
NPN silicon planar epitaxial transistors in 4-pin dual-emitter SOT343N and SOT343R plastic packages.

MARKING

TYPE NUMBER	CODE
BFG505W	N0
BFG505W/X	N1
BFG505W/XR	P0

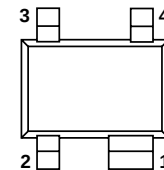
PINNING

PIN	DESCRIPTION
BFG505W (see Fig.1)	
1	collector
2	base
3	emitter
4	emitter
BFG505W/X (see Fig.1)	
1	collector
2	emitter
3	base
4	emitter
BFG505W/XR (see Fig.2)	
1	collector
2	emitter
3	base
4	emitter



Top view MBK523

Fig.1 SOT343N.



Top view MSB842

Fig.2 SOT343R.

QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V_{CBO}	collector-base voltage	open emitter	—	—	20	V
V_{CES}	collector-emitter voltage	$R_{BE} = 0$	—	—	15	V
I_C	collector current (DC)		—	—	18	mA
P_{tot}	total power dissipation	$T_s \leq 85\text{ }^{\circ}\text{C}$	—	—	500	mW
h_{FE}	DC current gain	$I_C = 5\text{ mA}$; $V_{CE} = 6\text{ V}$	60	120	250	
C_{re}	feedback capacitance	$I_C = 0$; $V_{CB} = 6\text{ V}$; $f = 1\text{ MHz}$	—	0.2	—	pF
f_T	transition frequency	$I_C = 5\text{ mA}$; $V_{CE} = 6\text{ V}$; $f = 1\text{ GHz}$; $T_{amb} = 25\text{ }^{\circ}\text{C}$	—	9	—	GHz
G_{UM}	maximum unilateral power gain	$I_C = 5\text{ mA}$; $V_{CE} = 6\text{ V}$; $f = 900\text{ MHz}$; $T_{amb} = 25\text{ }^{\circ}\text{C}$	—	19	—	dB
		$I_C = 5\text{ mA}$; $V_{CE} = 6\text{ V}$; $f = 2\text{ GHz}$; $T_{amb} = 25\text{ }^{\circ}\text{C}$	—	12	—	dB
$ S_{21} ^2$	insertion power gain	$I_C = 5\text{ mA}$; $V_{CE} = 6\text{ V}$; $f = 900\text{ MHz}$; $T_{amb} = 25\text{ }^{\circ}\text{C}$	15	16	—	dB
F	noise figure	$\Gamma_s = \Gamma_{opt}$; $I_C = 1.25\text{ mA}$; $V_{CE} = 6\text{ V}$; $f = 2\text{ GHz}$	—	1.9	—	dB

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LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CBO}	collector-base voltage	open emitter	–	20	V
V_{CES}	collector-emitter voltage	$R_{BE} = 0$	–	15	V
V_{EBO}	emitter-base voltage	open collector	–	2.5	V
I_C	collector current (DC)		–	18	mA
P_{tot}	total power dissipation	$T_s \leq 85\text{ }^{\circ}\text{C}$; see Fig.3; note 1	–	500	mW
T_{stg}	storage temperature		–65	+150	$^{\circ}\text{C}$
T_j	junction temperature		–	175	$^{\circ}\text{C}$

Note

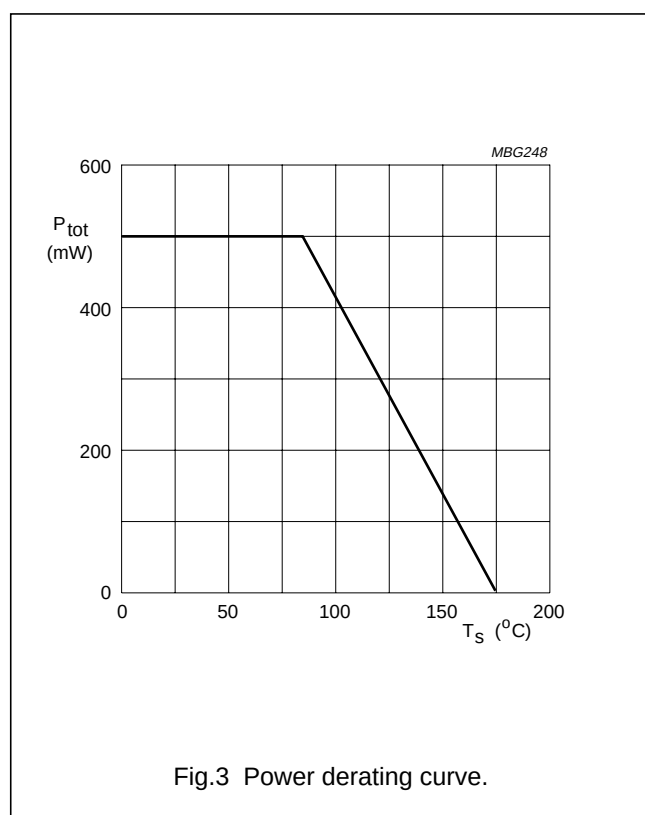
1. T_s is the temperature at the soldering point of the collector pin.

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j-s}$	thermal resistance from junction to soldering point	$T_s \leq 85\text{ }^{\circ}\text{C}$; note 1	180	K/W

Note

1. T_s is the temperature at the soldering point of the collector pin.



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CHARACTERISTICS

 $T_j = 25\text{ °C}$ unless otherwise specified.

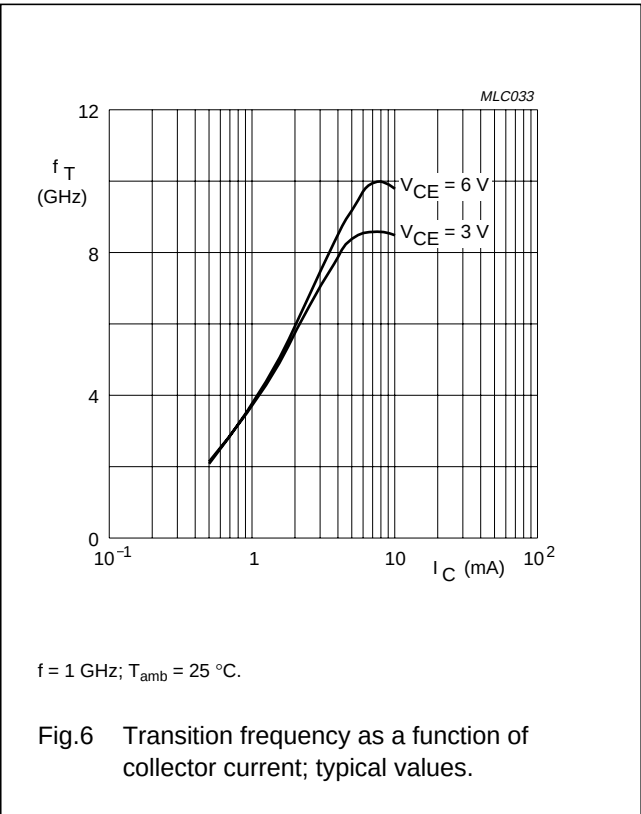
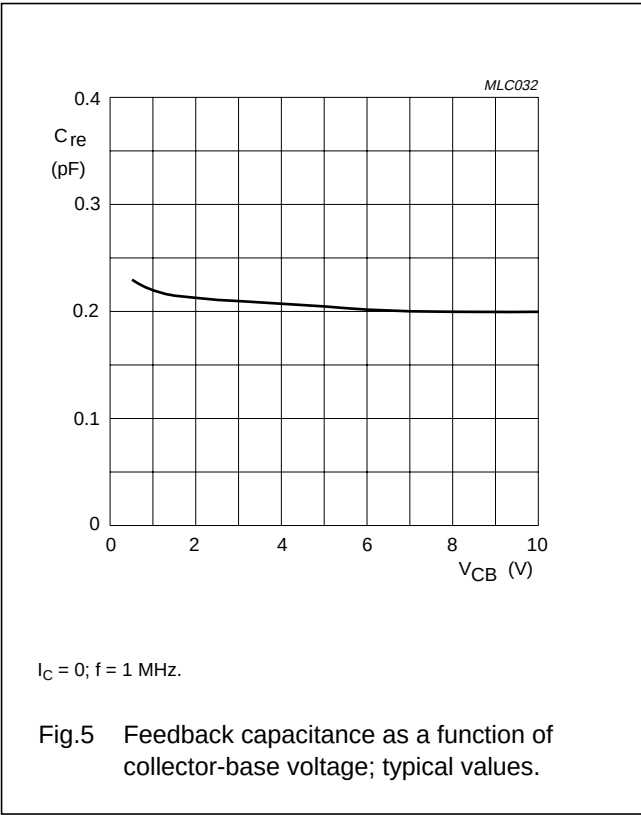
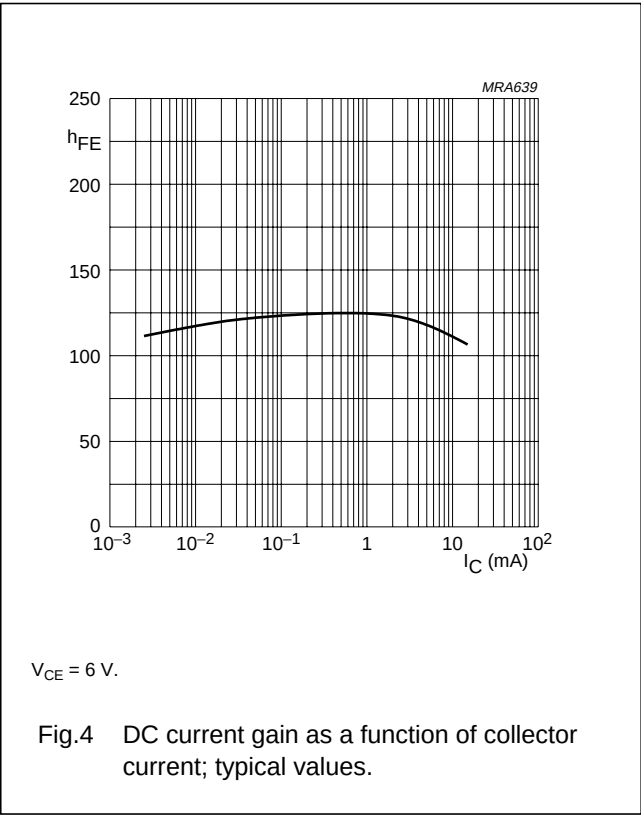
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$V_{(BR)CBO}$	collector-base breakdown voltage	$I_C = 2.5\text{ }\mu\text{A}$; $I_E = 0$	20	–	–	V
$V_{(BR)CES}$	collector-emitter breakdown voltage	$I_C = 10\text{ }\mu\text{A}$; $R_{BE} = 0$	15	–	–	V
$V_{(BR)EBO}$	emitter-base breakdown voltage	$I_E = 2.5\text{ }\mu\text{A}$; $I_C = 0$	2.5	–	–	V
I_{CBO}	collector leakage current	$V_{CB} = 6\text{ V}$; $I_E = 0$	–	–	50	nA
h_{FE}	DC current gain	$I_C = 5\text{ mA}$; $V_{CE} = 6\text{ V}$ see Fig.4	60	120	250	
f_T	transition frequency	$I_C = 5\text{ mA}$; $V_{CE} = 6\text{ V}$; $f = 1\text{ GHz}$; $T_{amb} = 25\text{ °C}$; see Fig.6	–	9	–	GHz
C_c	collector capacitance	$I_E = i_e = 0$; $V_{CB} = 6\text{ V}$; $f = 1\text{ MHz}$	–	0.3	–	pF
C_e	emitter capacitance	$I_C = i_c = 0$; $V_{EB} = 0.5\text{ V}$; $f = 1\text{ MHz}$	–	0.4	–	pF
C_{re}	feedback capacitance	$I_C = 0$; $V_{CB} = 6\text{ V}$; $f = 1\text{ MHz}$; see Fig.5	–	0.2	–	pF
G_{UM}	maximum unilateral power gain; note 1	$I_C = 5\text{ mA}$; $V_{CE} = 6\text{ V}$; $f = 900\text{ MHz}$; $T_{amb} = 25\text{ °C}$	–	19	–	dB
		$I_C = 5\text{ mA}$; $V_{CE} = 6\text{ V}$; $f = 2\text{ GHz}$; $T_{amb} = 25\text{ °C}$	–	12	–	dB
$ s_{21} ^2$	insertion power gain	$I_C = 5\text{ mA}$; $V_{CE} = 6\text{ V}$; $f = 900\text{ MHz}$; $T_{amb} = 25\text{ °C}$	15	16	–	dB
F	noise figure	$\Gamma_S = \Gamma_{opt}$; $I_C = 1.25\text{ mA}$; $V_{CE} = 6\text{ V}$; $f = 900\text{ MHz}$	–	1.2	1.7	dB
		$\Gamma_S = \Gamma_{opt}$; $I_C = 5\text{ mA}$; $V_{CE} = 6\text{ V}$; $f = 900\text{ MHz}$	–	1.6	2.1	dB
		$\Gamma_S = \Gamma_{opt}$; $I_C = 1.25\text{ mA}$; $V_{CE} = 6\text{ V}$; $f = 2\text{ GHz}$	–	1.9	–	dB
P_{L1}	output power at 1 dB gain compression	$I_C = 5\text{ mA}$; $V_{CE} = 6\text{ V}$; $f = 900\text{ MHz}$; $R_L = 50\text{ }\Omega$; $T_{amb} = 25\text{ °C}$	–	4	–	dBm
ITO	third order intercept point	note 2	–	10	–	dBm

Notes

- G_{UM} is the maximum unilateral power gain, assuming s_{12} is zero. $G_{UM} = 10 \log \frac{|s_{21}|^2}{(1 - |s_{11}|^2)(1 - |s_{22}|^2)}$ dB.
- $I_C = 5\text{ mA}$; $V_{CE} = 6\text{ V}$; $R_L = 50\text{ }\Omega$; $T_{amb} = 25\text{ °C}$;
 $f_p = 900\text{ MHz}$; $f_q = 902\text{ MHz}$;
measured at $2f_p - f_q = 898\text{ MHz}$ and $2f_q - f_p = 904\text{ MHz}$.

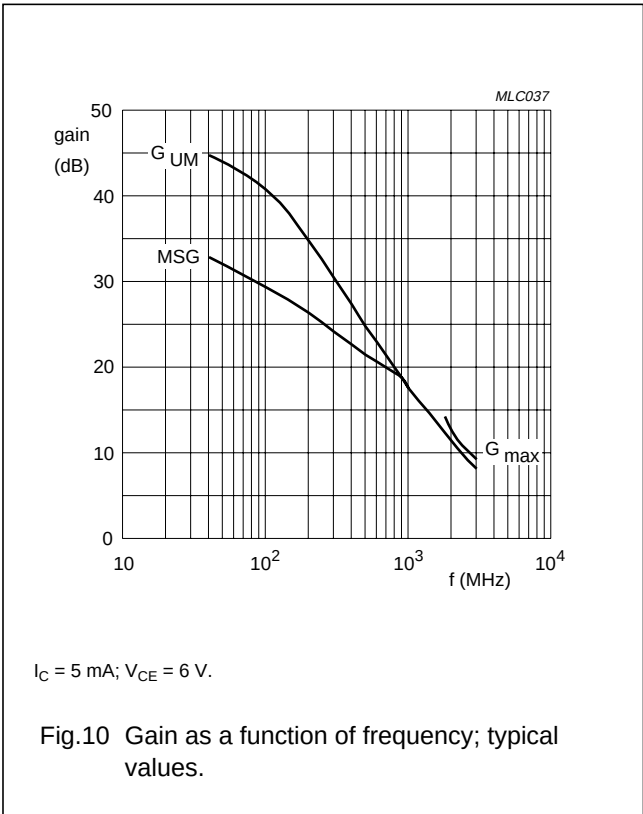
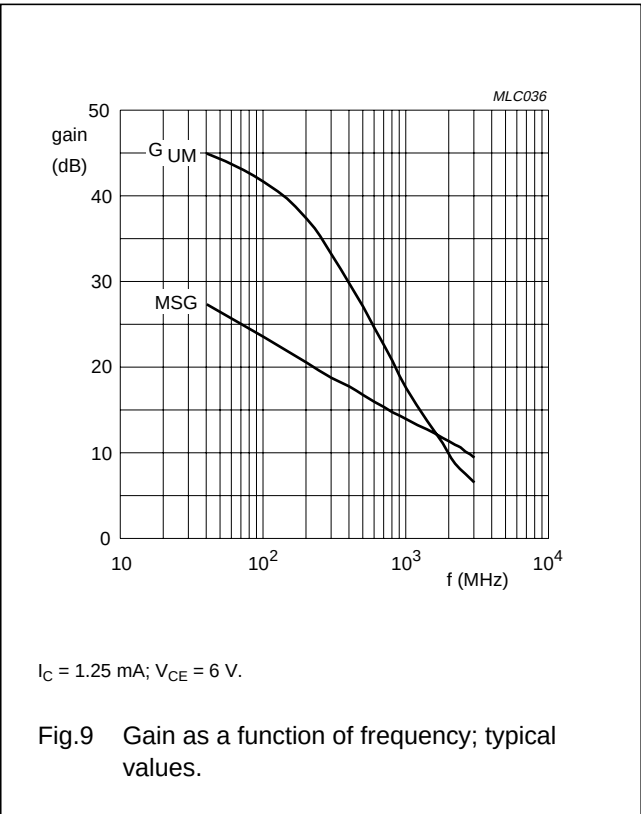
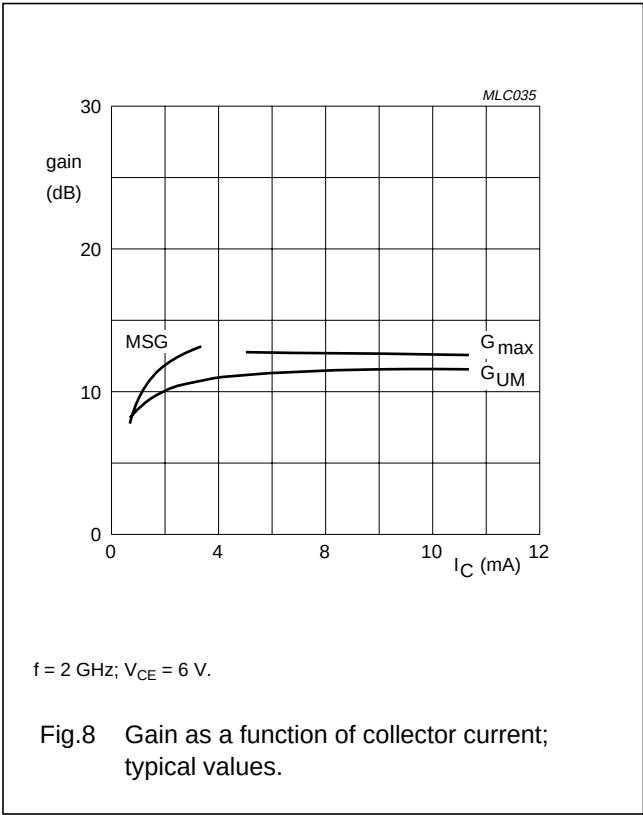
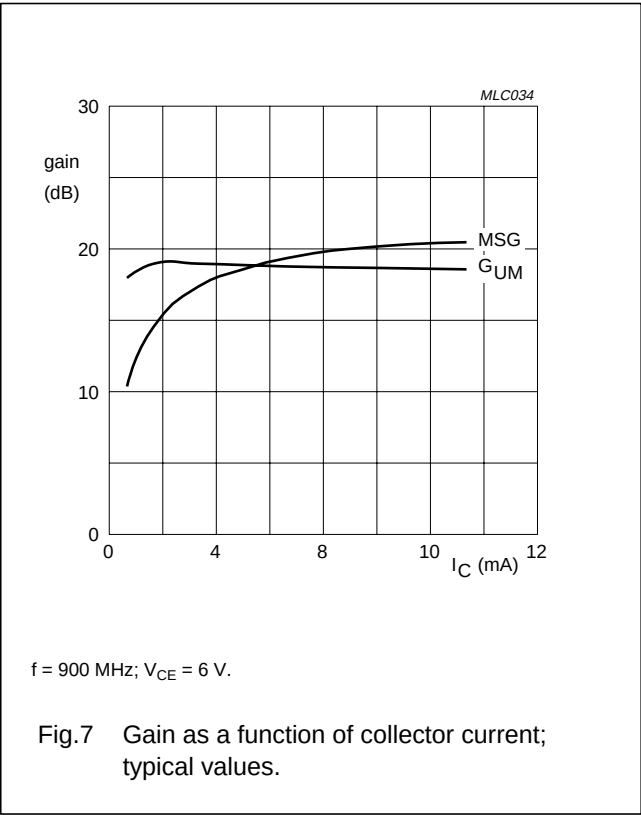
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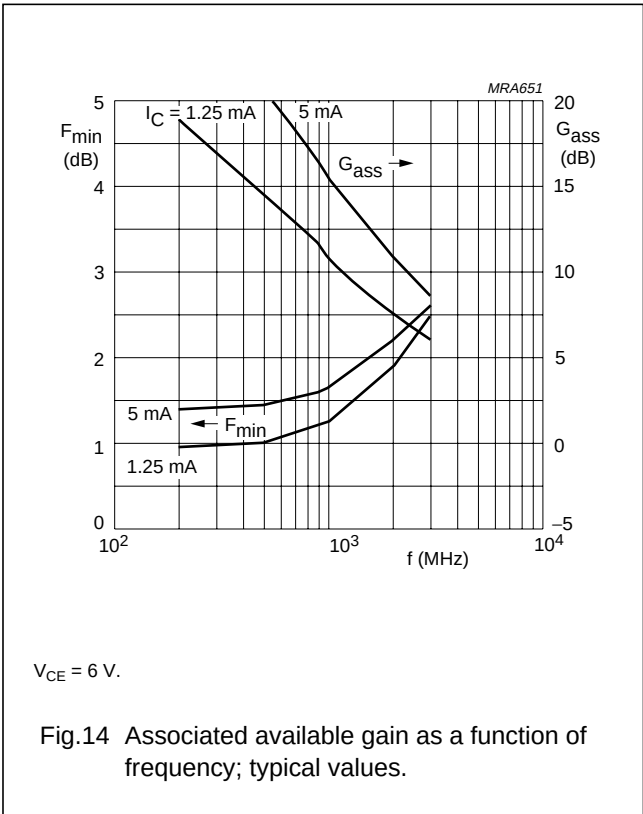
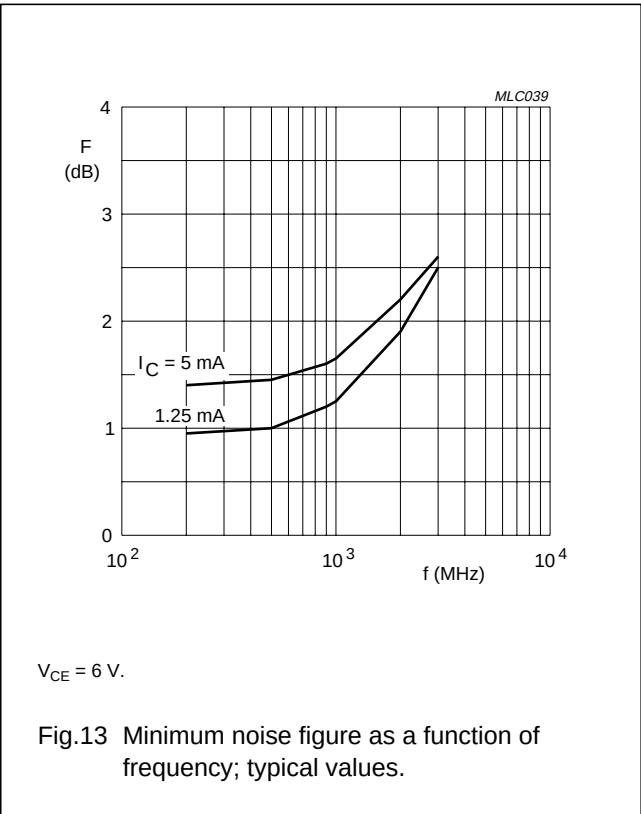
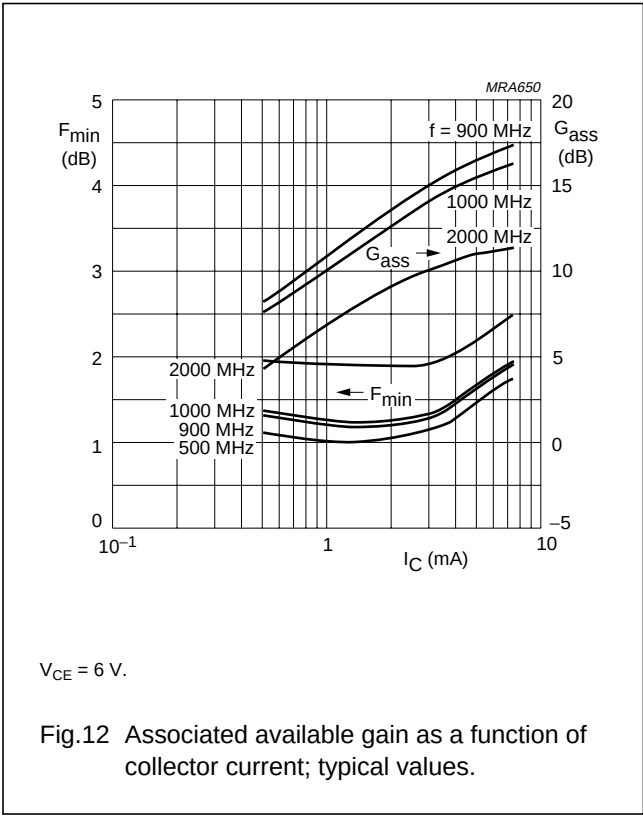
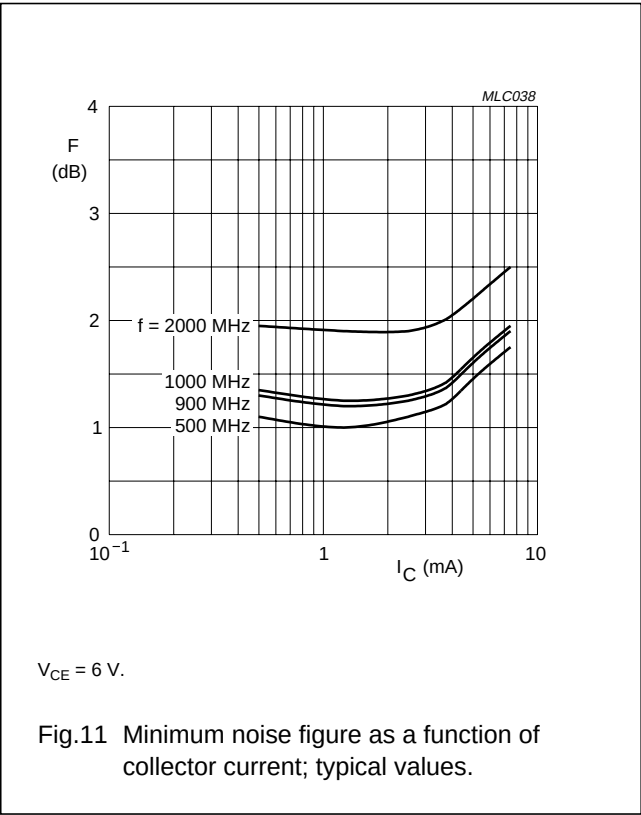
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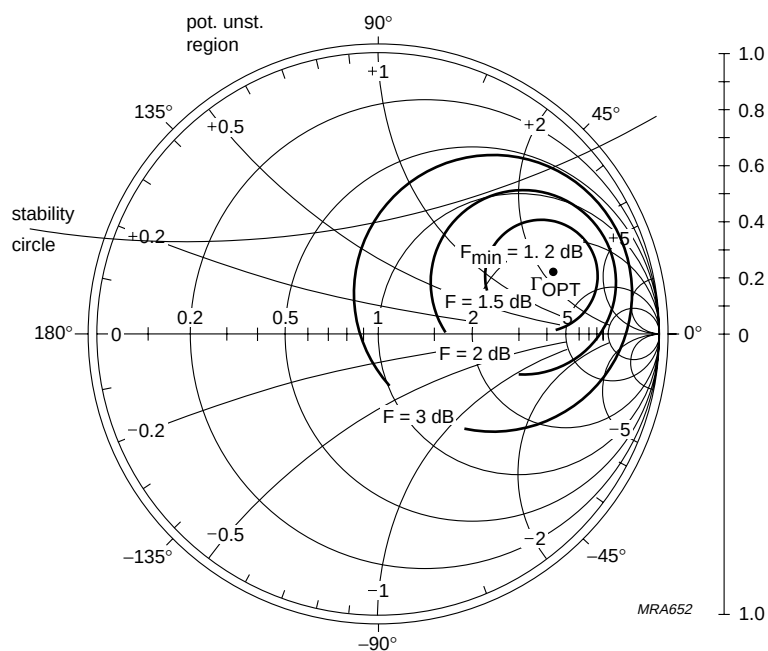


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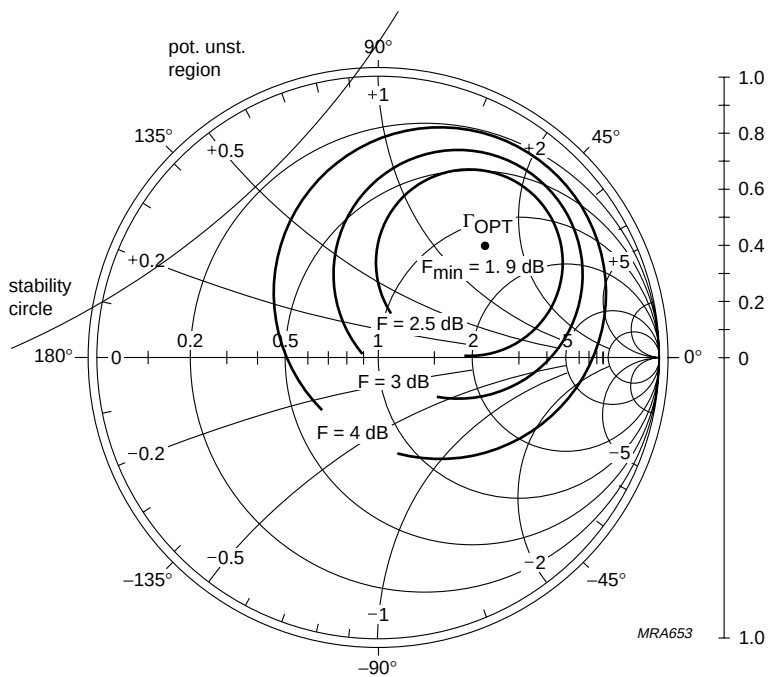


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$f = 900 \text{ MHz}$; $V_{CE} = 6 \text{ V}$; $I_C = 1.25 \text{ mA}$; $Z_0 = 50 \Omega$.

Fig.15 Common emitter noise figure circles; typical values.

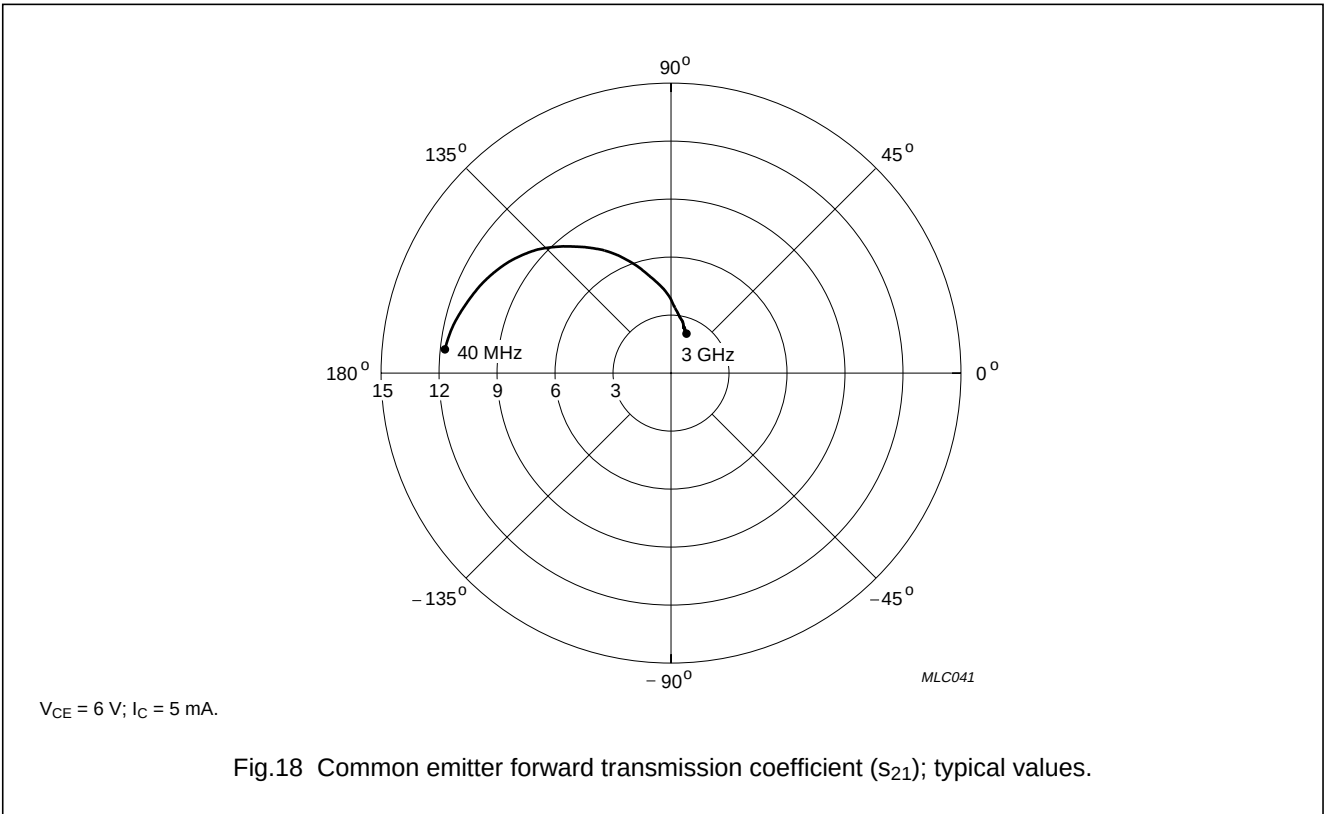
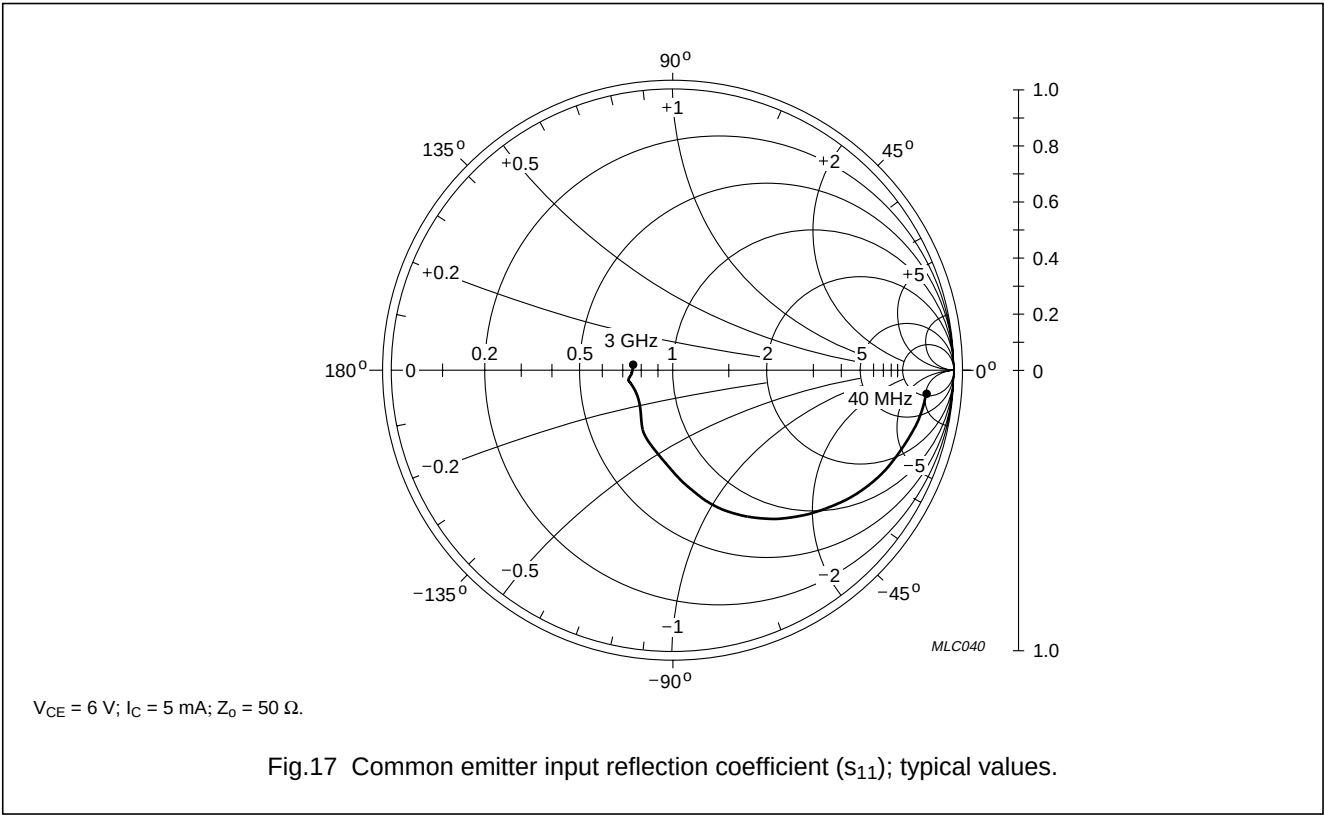


$f = 2 \text{ GHz}$; $V_{CE} = 6 \text{ V}$; $I_C = 1.25 \text{ mA}$; $Z_0 = 50 \Omega$.

Fig.16 Common emitter noise figure circles; typical values.

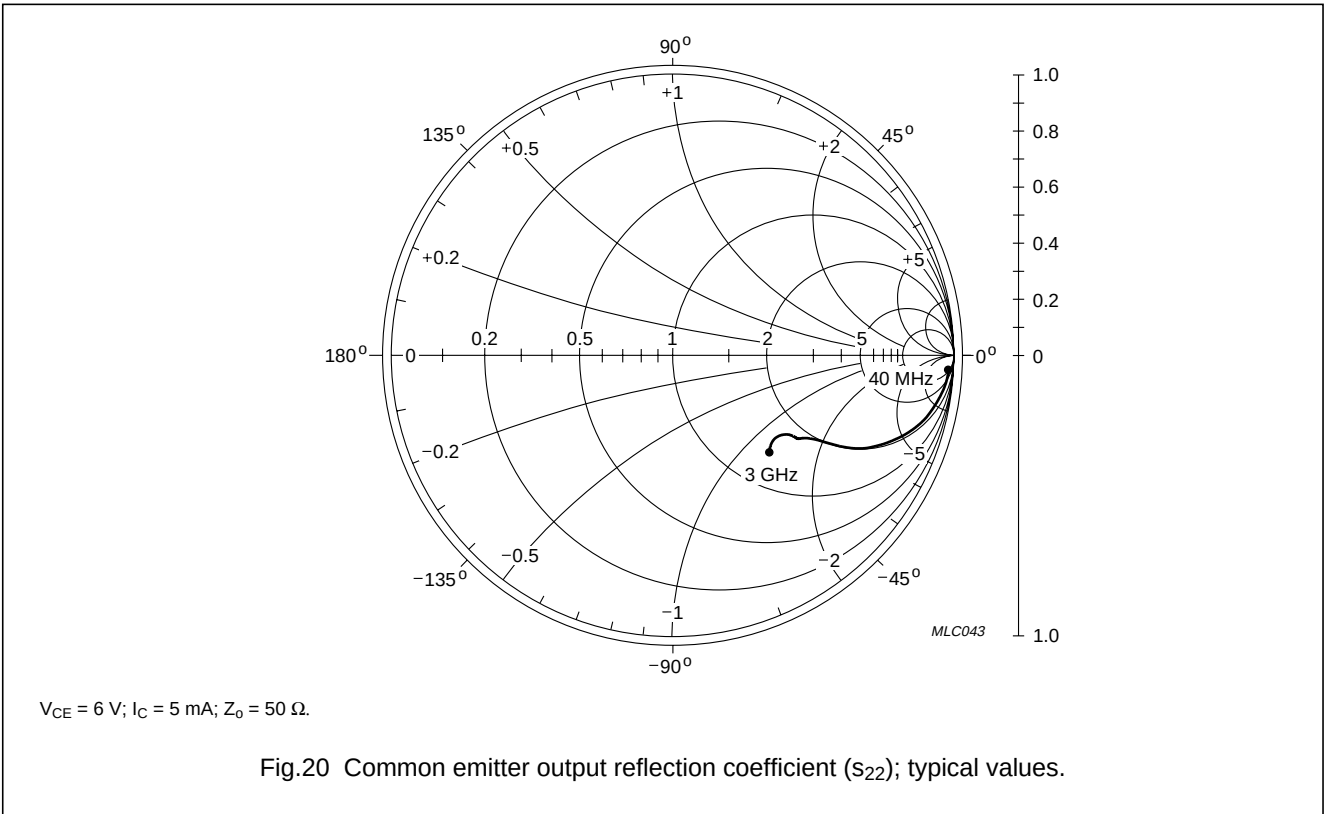
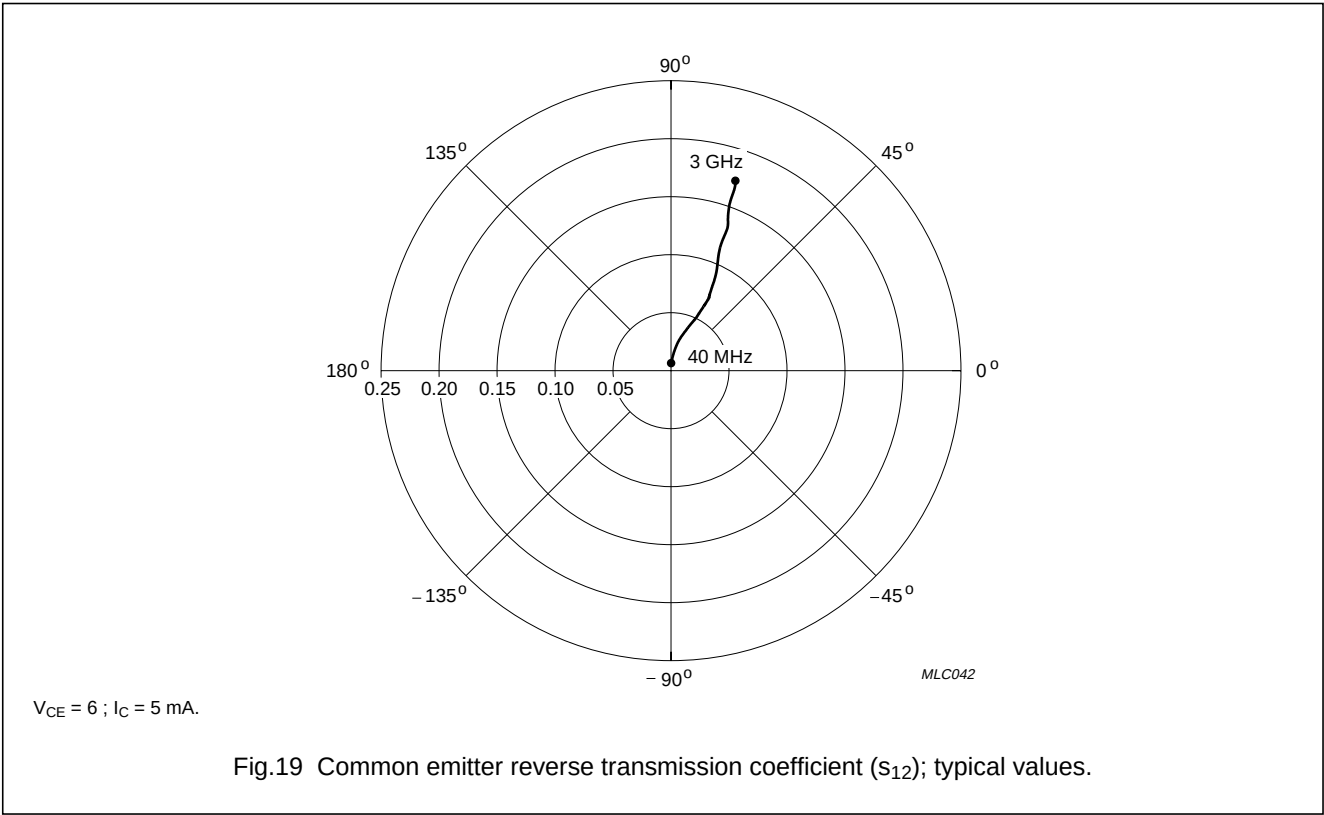
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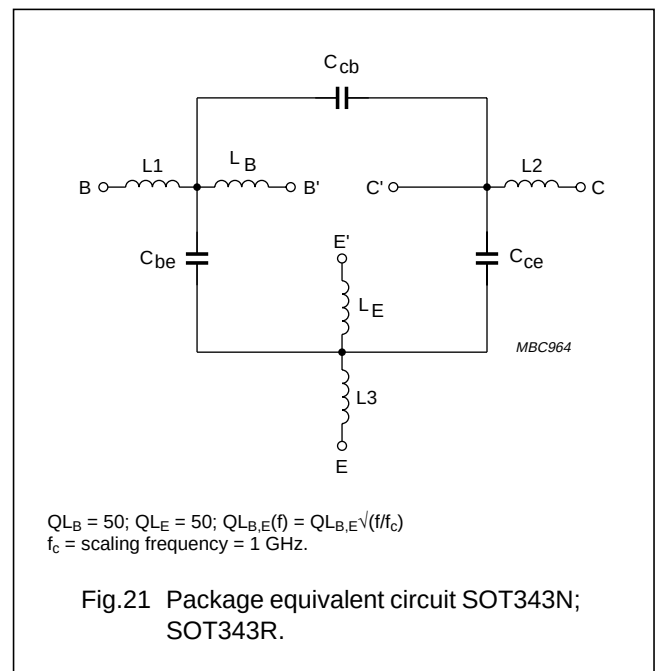
SPICE parameters for the BFG505W die

SEQUENCE No.	PARAMETER	VALUE	UNIT
1	IS	134.1	aA
2	BF	180.0	–
3	NF	0.988	–
4	VAF	38.34	V
5	IKF	150.0	mA
6	ISE	27.81	fA
7	NE	2.051	–
8	BR	55.19	–
9	NR	0.982	–
10	VAR	2.459	V
11	IKR	2.920	mA
12	ISC	17.45	aA
13	NC	1.062	–
14	RB	20.00	Ω
15	IRB	1.000	μ A
16	RBM	20.00	Ω
17	RE	1.171	Ω
18	RC	4.350	Ω
19 ⁽¹⁾	XTB	0.000	–
20 ⁽¹⁾	EG	1.110	eV
21 ⁽¹⁾	XTI	3.000	–
22	CJE	284.7	fF
23	VJE	600.0	mV
24	MJE	0.303	–
25	TF	7.037	ps
26	XTF	12.34	–
27	VTF	1.701	V
28	ITF	30.64	mA
29	PTF	0.000	deg
30	CJC	242.4	fF
31	VJC	188.6	mV
32	MJC	0.041	–
33	XCJC	0.130	–
34	TR	1.332	ns
35 ⁽¹⁾	CJS	0.000	F

SEQUENCE No.	PARAMETER	VALUE	UNIT
36 ⁽¹⁾	VJS	750.0	mV
37 ⁽¹⁾	MJS	0.000	–
38	FC	0.897	–

Note

- These parameters have not been extracted, the default values are shown.



List of components (see Fig.21)

DESIGNATION	VALUE	UNIT
C_{be}	70	fF
C_{cb}	50	fF
C_{ce}	115	fF
L1	0.34	nH
L2	0.10	nH
L3	0.25	nH
L_B	0.40	nH
L_E	0.40	nH

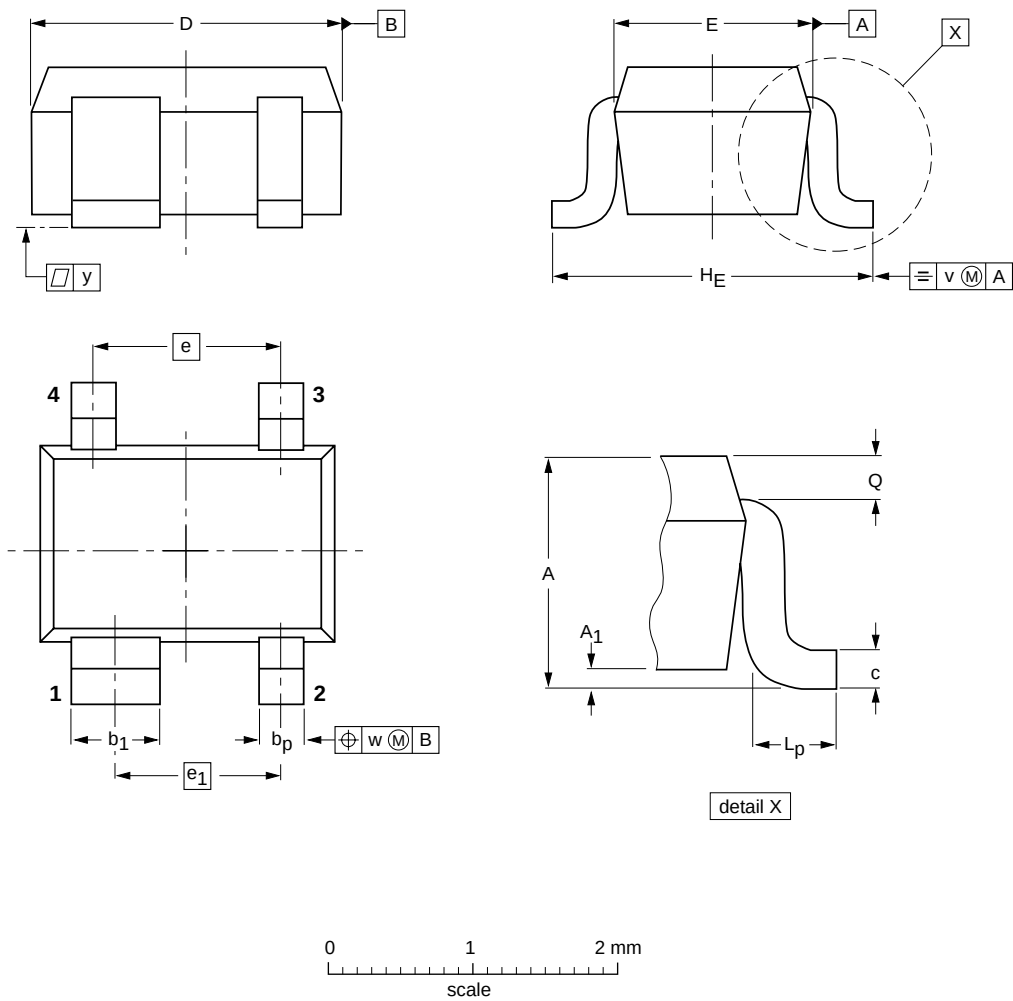
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PACKAGE OUTLINES

Plastic surface mounted package; 4 leads

SOT343N



DIMENSIONS (mm are the original dimensions)

UNIT	A	A ₁ max	b _p	b ₁	c	D	E	e	e ₁	H _E	L _p	Q	v	w	y
mm	1.1 0.8	0.1	0.4 0.3	0.7 0.5	0.25 0.10	2.2 1.8	1.35 1.15	1.3	1.15	2.2 2.0	0.45 0.15	0.23 0.13	0.2	0.2	0.1

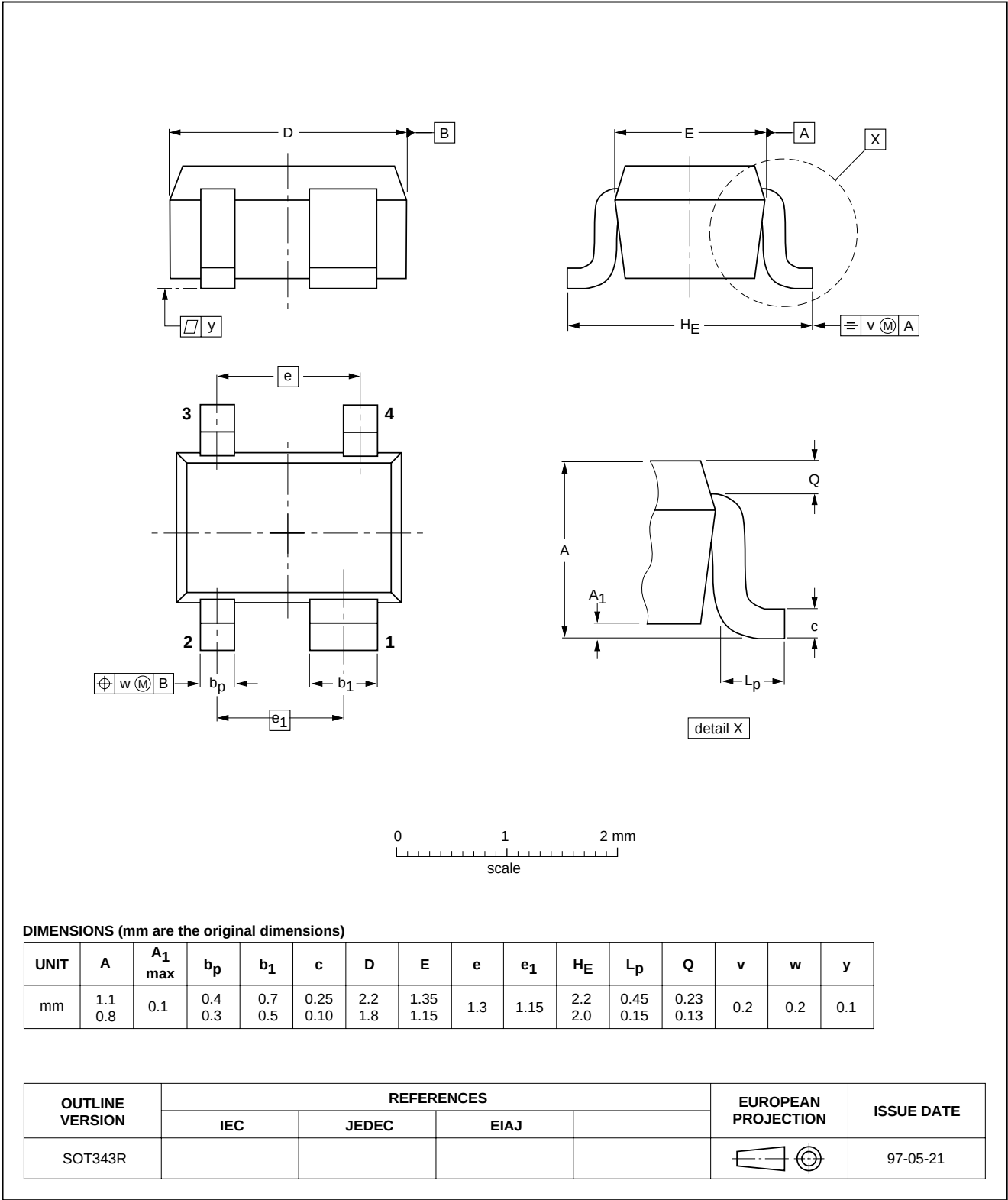
OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT343N						97-05-21

NPN 9 GHz wideband transistors

BFG505W; BFG505W/X;
BFG505W/XR

Plastic surface mounted package; reverse pinning; 4 leads

SOT343R



NPN 9 GHz wideband transistors

BFG505W; BFG505W/X;
BFG505W/XR

DATA SHEET STATUS

DATA SHEET STATUS	PRODUCT STATUS	DEFINITIONS ⁽¹⁾
Objective specification	Development	This data sheet contains the design target or goal specifications for product development. Specification may change in any manner without notice.
Preliminary specification	Qualification	This data sheet contains preliminary data, and supplementary data will be published at a later date. Philips Semiconductors reserves the right to make changes at any time without notice in order to improve design and supply the best possible product.
Product specification	Production	This data sheet contains final specifications. Philips Semiconductors reserves the right to make changes at any time without notice in order to improve design and supply the best possible product.

Note

1. Please consult the most recently issued data sheet before initiating or completing a design.

DEFINITIONS

Short-form specification — The data in a short-form specification is extracted from a full data sheet with the same type number and title. For detailed information see the relevant data sheet or data handbook.

Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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