
BB201M

**Build in Biasing Circuit MOS FET IC
UHF RF Amplifier**

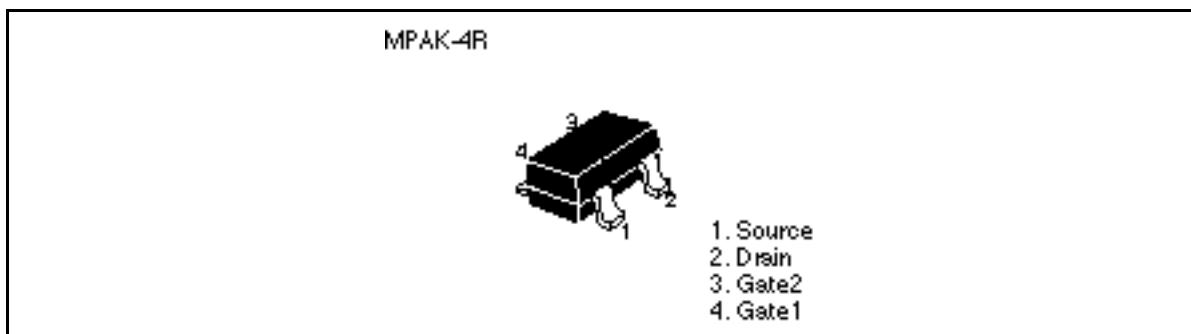
HITACHI

ADE-208-713A (Z)
2nd. Edition
Dec. 1998

Features

- Build in Biasing Circuit; To reduce using parts cost & PC board space.
- Low noise characteristics;
($NF = 2.0 \text{ dB typ. at } f = 900 \text{ MHz}$)
- Withstanding to ESD;
Build in ESD absorbing diode. Withstand up to 200V at $C=200\text{pF}$, $R_s=0$ conditins.
- Provide mini mold packages; MPAK-4R(SOT-143 var.)

Outline



Notes: 1. Marking is "AV-".
2. BB201M is individual type number of HITACHI BBFET.

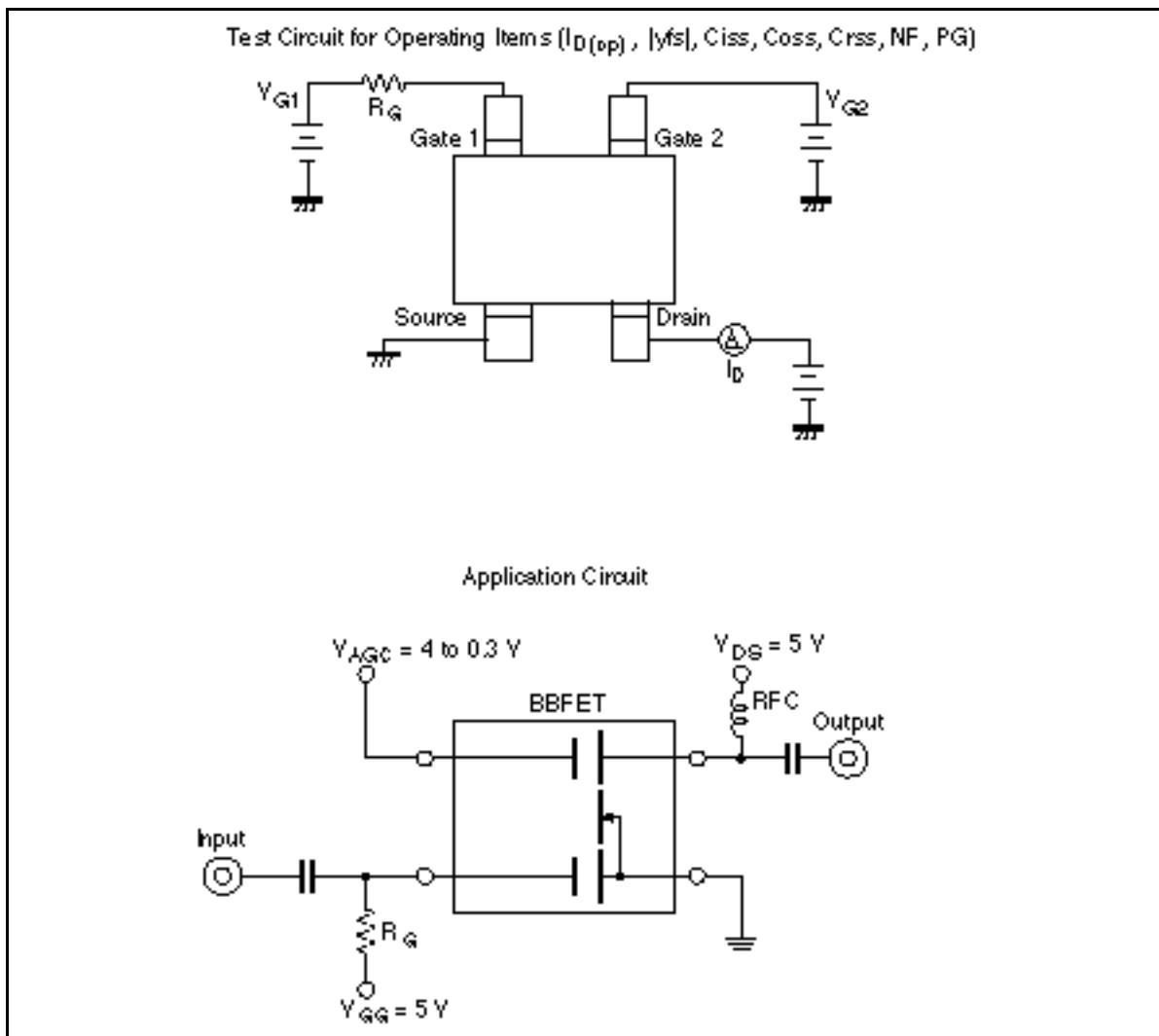
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Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DS}	6	V
Gate1 to source voltage	V _{G1S}	+6 -0	V
Gate 2 to source voltage	V _{G2S}	±6	V
Drain current	I _D	25	mA
Channel power dissipation	Pch	150	mW
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

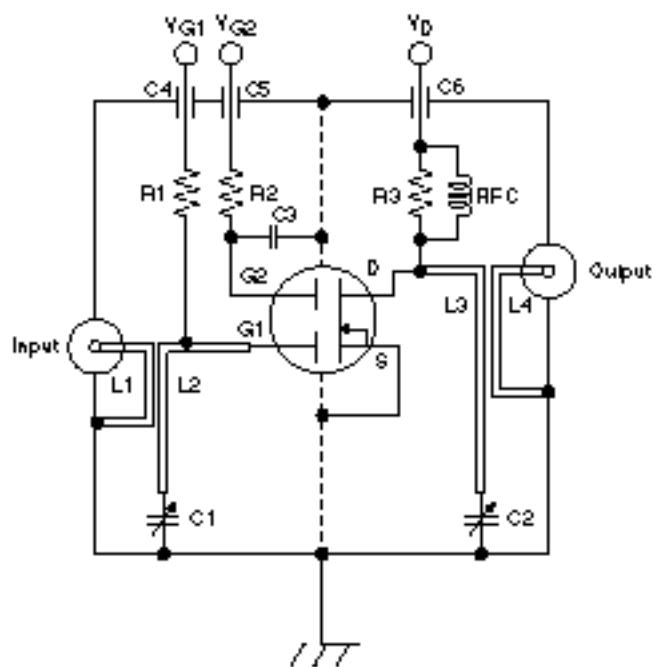
Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	V _{(BR)DSS}	6	—	—	V	I _D = 200µA, V _{G1S} = V _{G2S} = 0
Gate1 to source breakdown voltage	V _{(BR)G1SS}	+6	—	—	V	I _{G1} = +10µA, V _{G2S} = V _{DS} = 0
Gate2 to source breakdown voltage	V _{(BR)G2SS}	±6	—	—	V	I _{G2} = ±10µA, V _{G1S} = V _{DS} = 0
Gate1 to cutoff current	I _{G1SS}	—	—	+100	nA	V _{G1S} = +5V, V _{G2S} = V _{DS} = 0
Gate2 to cutoff current	I _{G2SS}	—	—	±100	nA	V _{G2S} = ±5V, V _{G1S} = V _{DS} = 0
Gate1 to source cutoff voltage	V _{G1S(off)}	0.2	0.45	0.8	V	V _{DS} = 5V, V _{G2S} = 4V I _D = 100µA
Gate2 to source cutoff voltage	V _{G2S(off)}	0.4	0.7	1.0	V	V _{DS} = 5V, V _{G1S} = 5V I _D = 100µA
Drain current	I _{D(op)}	10	15	20	mA	V _{DS} = 5V, V _{G1} = 5V, V _{G2S} = 4V R _G = 220k
Forward transfer admittance	y _{fs}	16	22	—	mS	V _{DS} = 5V, V _{G1} = 5V, V _{G2S} = 4V R _G = 220k, f = 1kHz
Input capacitance	C _{iss}	1.2	1.7	2.2	pF	V _{DS} = 5V, V _{G1} = 5V
Output capacitance	C _{oss}	0.7	1.1	1.5	pF	V _{G2S} = 4V, R _G = 220k
Reverse capacitance	C _{rss}	—	0.012	0.03	pF	f = 1MHz
Power gain	PG	16	20	—	dB	V _{DS} = 5V, V _{G1} = 5V, V _{G2S} = 4V
Noise figure	NF	—	2.0	3.0	dB	R _G = 220k, f = 900MHz

Main Characteristics

BB201M

900MHz Power Gain, Noise Test Circuit



C1, C2 : Variable Capacitor (10pF MAX)

C3 : Disk Capacitor (1000pF)

C4 to C6 : Air Capacitor (1000pF)

R1 : 220 k Ω

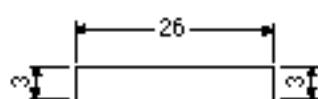
R2 : 47 k Ω

R3 : 4.7 k Ω

L1 :

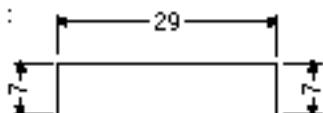


L2 :

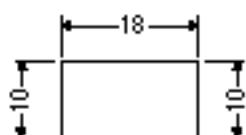


(ϕ 1mm Copper wire)
Unit : mm

L3 :

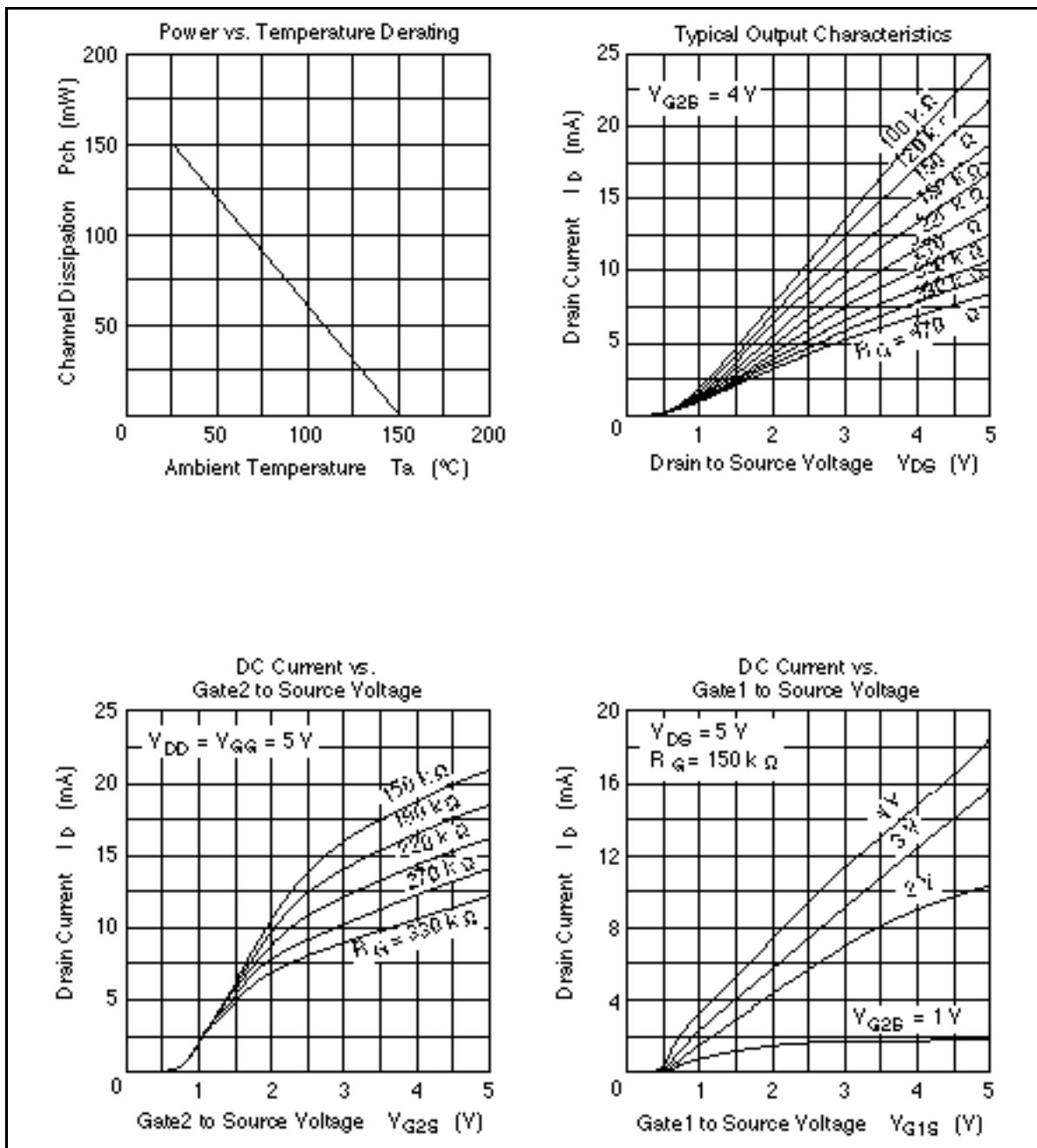


L4 :

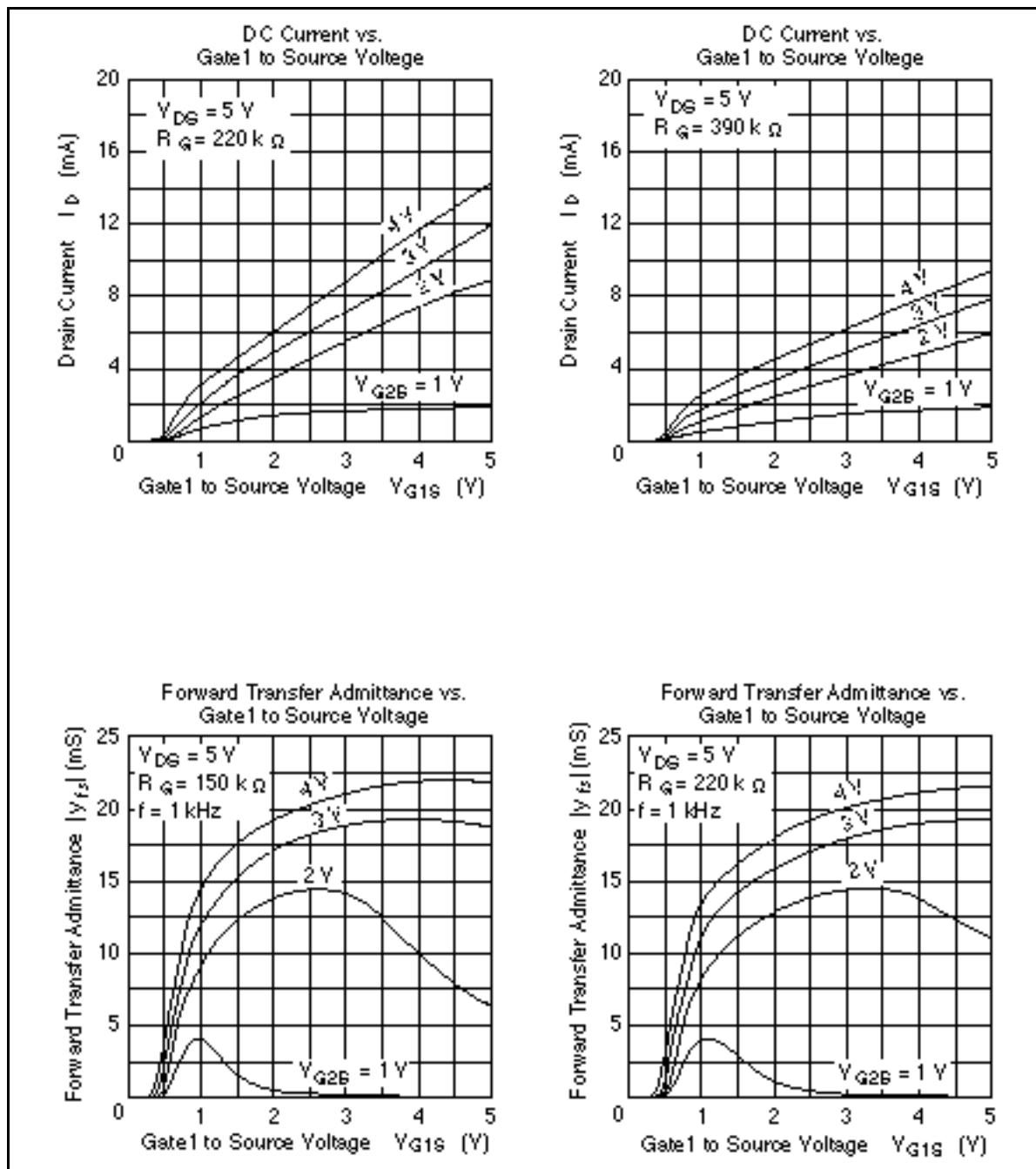


RFc : ϕ 1mm Copper wire with enamel 4turns inside dia. 6mm

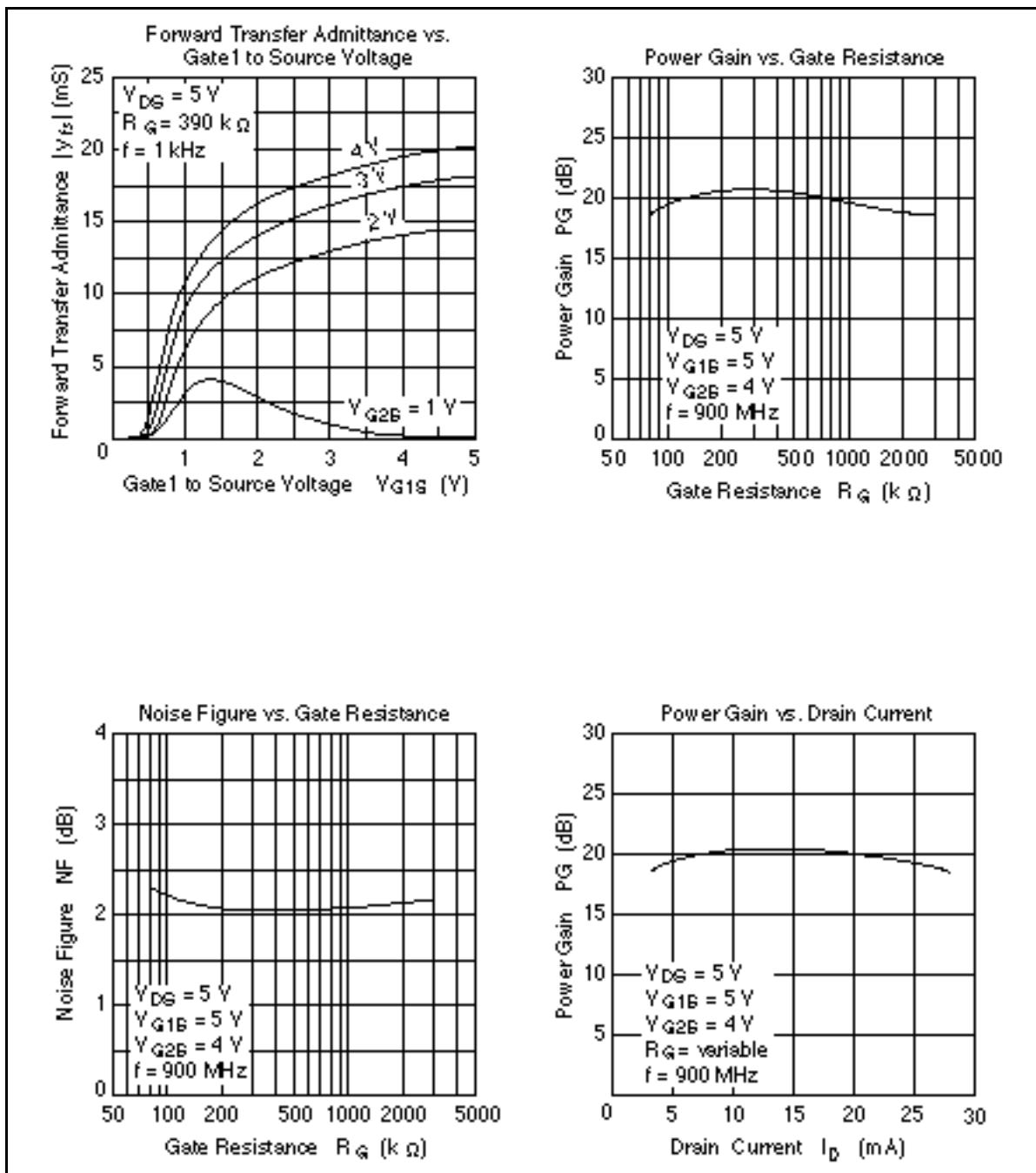
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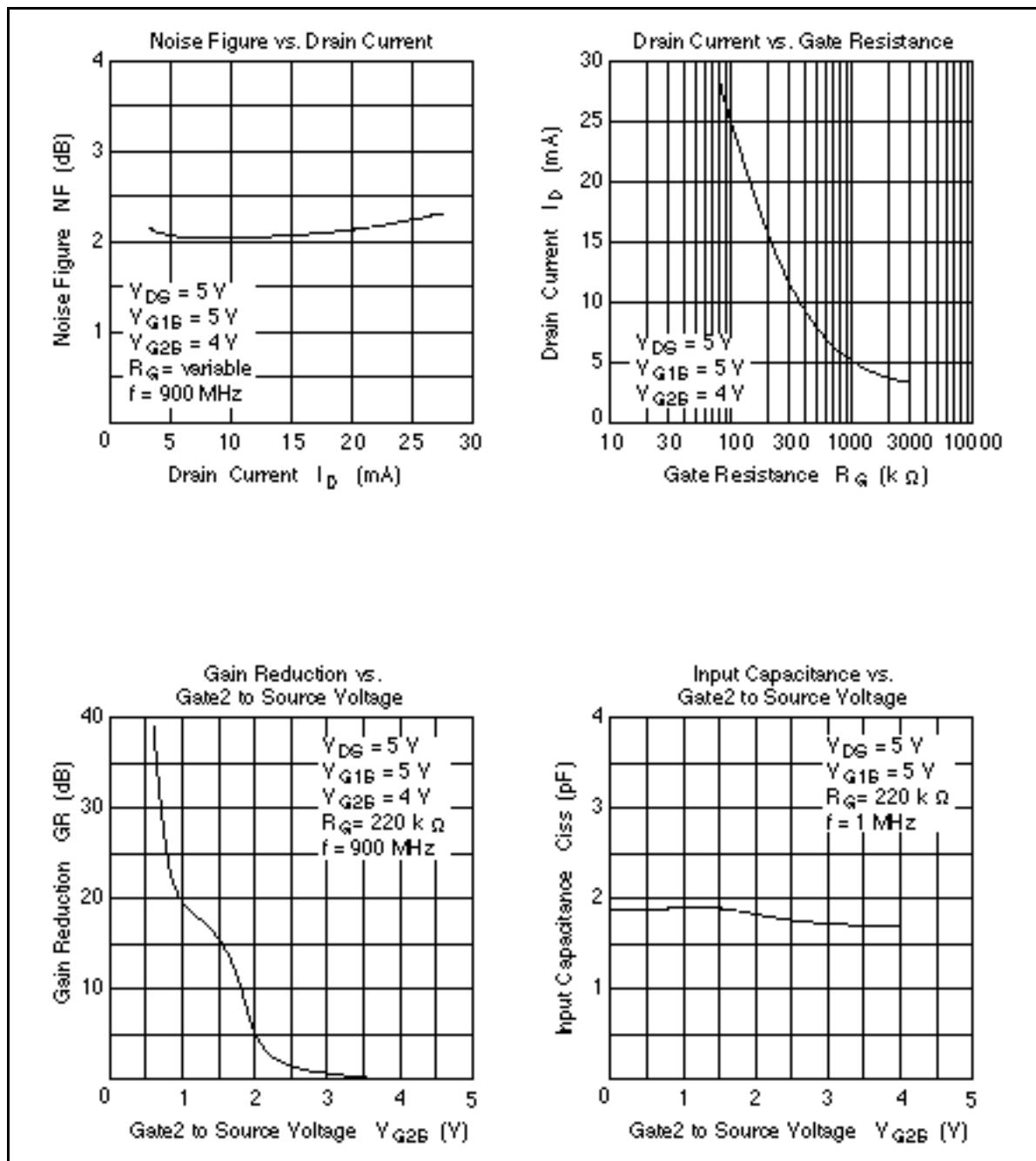
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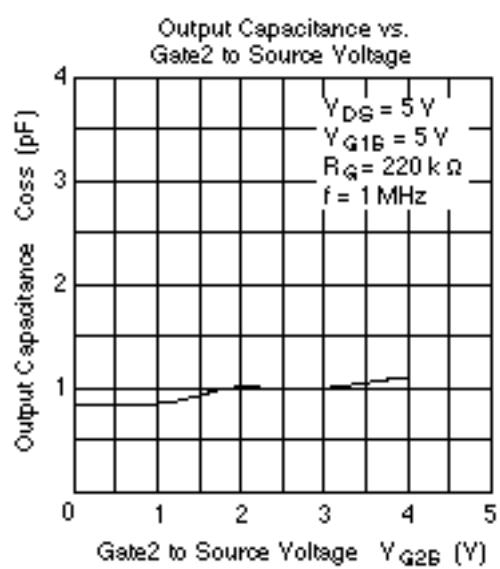
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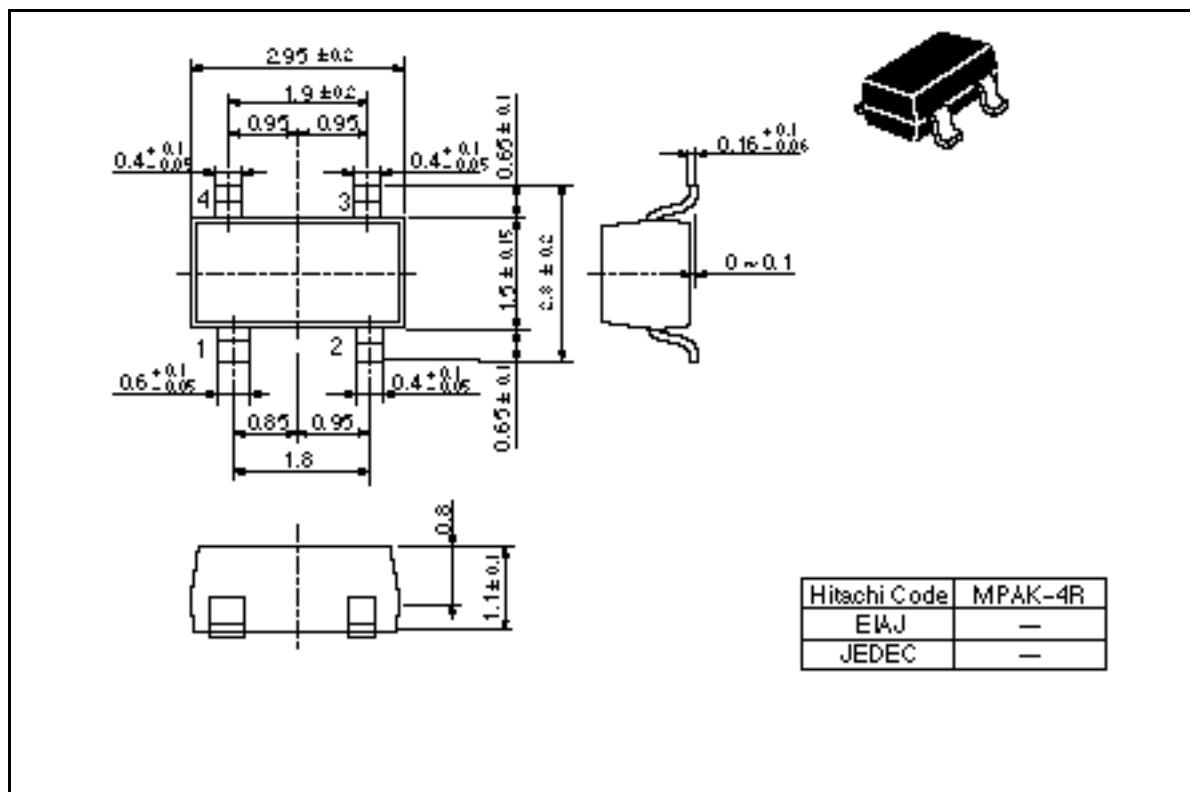
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Package Dimensions

Unit: mm



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