

2SK1103

Silicon N-Channel Junction FET

For switching

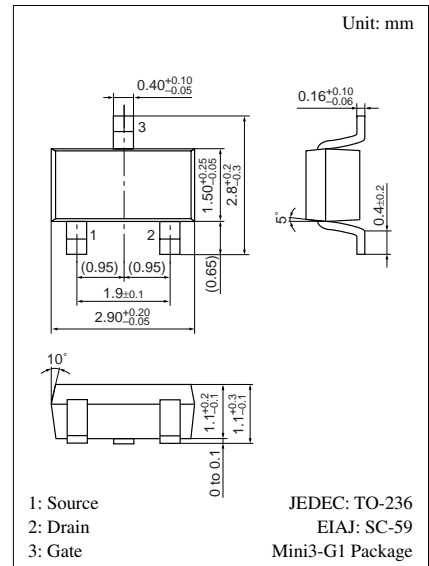
Complementary to 2SJ0163 (2SJ163)

■ Features

- Low ON-resistance
- Low-noise characteristics

■ Absolute Maximum Ratings (T_a = 25°C)

Parameter	Symbol	Ratings	Unit
Gate to Drain voltage	V _{GDS}	-65	V
Drain current	I _D	20	mA
Gate current	I _G	10	mA
Allowable power dissipation	P _D	150	mW
Channel temperature	T _{ch}	150	°C
Storage temperature	T _{stg}	-55 to +150	°C



Marking Symbol (Example): 4L

■ Electrical Characteristics (T_a = 25°C)

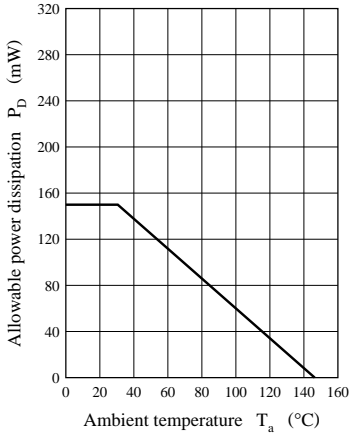
Parameter	Symbol	Conditions	min	typ	max	Unit
Drain to Source cut-off current	I _{DSS} *	V _{DS} = 10 V, V _{GS} = 0	0.2		6	mA
Gate to Source leakage current	I _{GSS}	V _{GS} = -30 V, V _{DS} = 0			-10	nA
Gate to Drain voltage	V _{GDS}	I _G = -10 μA, V _{DS} = 0	-65			V
Gate to Source cut-off voltage	V _{GSC}	V _{DS} = 10 V, I _D = 10 μA		-1.5	-3.5	V
Forward transfer admittance	Y _{fs}	V _{DS} = 10 V, I _D = 1 mA, f = 1 kHz	1.8	2.5		mS
Drain to Source ON-resistance	R _{DS(on)}	V _{DS} = 10 mV, V _{GS} = 0		300		Ω
Input capacitance (Common Source)	C _{iss}	V _{DS} = 10 V, V _{GS} = 0, f = 1 MHz		7		pF
Reverse transfer capacitance (Common Source)	C _{rss}			1.5		pF

* I_{DSS} rank classification

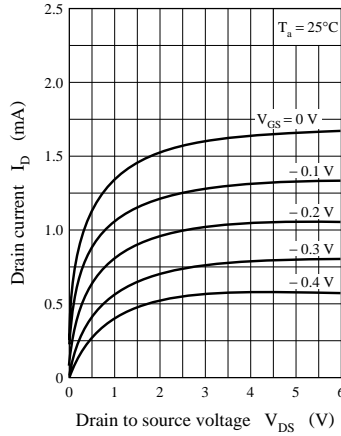
Runk	O	P	Q	R
I _{DSS} (mA)	0.2 to 1	0.6 to 1.5	1 to 3	2.5 to 6
Marking Symbol	4LO	4LP	4LQ	4LR

(Note) The part number in the parenthesis shows conventional part number.

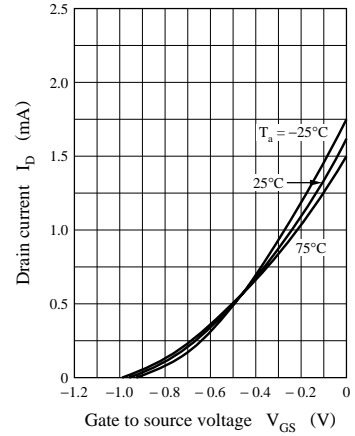
$P_D - T_a$



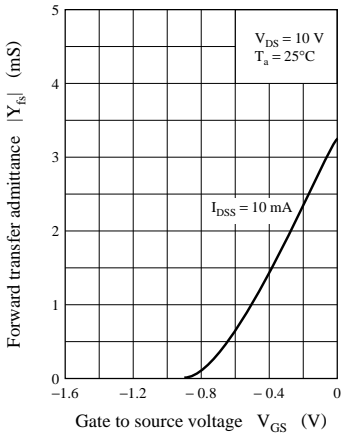
$I_D - V_{DS}$



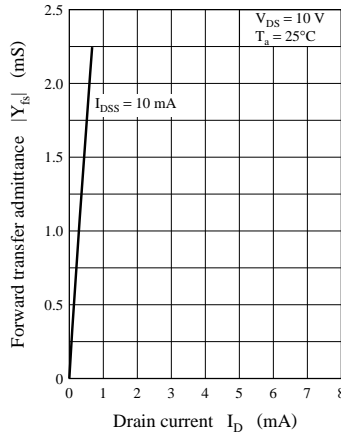
$I_D - V_{GS}$



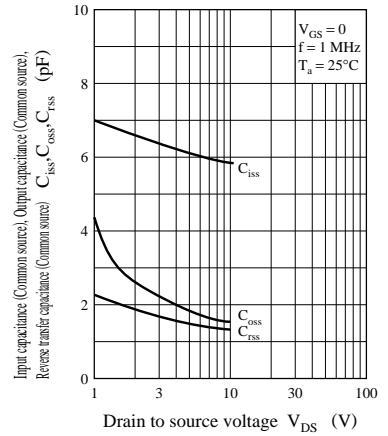
$|Y_{fs}| - V_{GS}$



$|Y_{fs}| - I_D$



$C_{iss}, C_{oss}, C_{rss} - V_{DS}$



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