

Power Module for 1 hp Motor Drives

- 1 hp (0.75kW) power output
Industrial rating at 150% overload for 1 minute
- 180 - 240V AC input, 50/60Hz
- 3-phase rectifier bridge
- 3-phase ultrafast IGBT inverter
- HEXFRED ultrafast soft recovery freewheeling diodes
- Low inductance (current sense)
shunts in positive and negative DC rail
- NTC temperature sensor
- Pin-to-base plate isolation 2500V rms
- Easy-to-mount two-screw package
- Case temperature range -25°C to 125°C operational

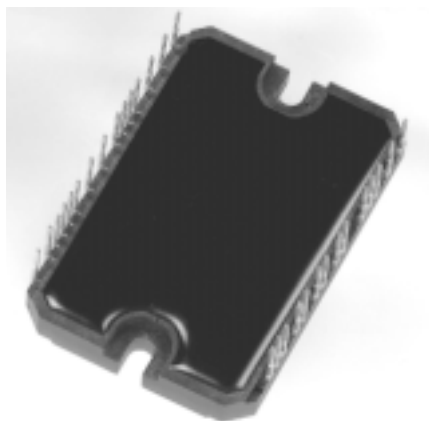


Figure 1. IRPT1053A Power Module

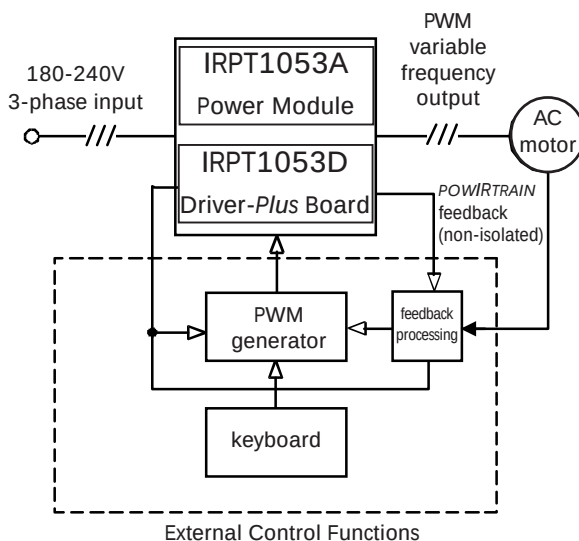


Figure 2. The IRPT1053C POWIRTRAIN within a motor control system

The IRPT1053A Power Module

The IRPT1053A Power Module, shown in figure 1, is a chip and wire epoxy encapsulated module. It houses input rectifiers output inverter, current sense shunts and NTC thermistor. The 3-phase input bridge rectifiers are rated at 800V. The inverter section employs 600V, short circuit rated, ultrafast IGBTs and ultrafast freewheeling diodes. Current sensing is achieved through 45mΩ low inductance shunts provided in the positive and negative DC bus rail. The NTC thermistor provides temperature sensing capability. The lead spacing on the power module meets UL840 pollution level 3 requirements.

The power circuit and layout within the module are carefully designed to minimize inductance in the power path, to reduce noise during inverter operation and to improve the inverter efficiency. The Driver-Plus Board required to run the inverter can be soldered to the power module pins, thus minimizing assembly and alignment. The power module is designed to be mounted to a heat sink with two screw mount positions, in order to insure good thermal contact between the module substrate and the heat sink.

PowIRtrain and Design Kit

The IRPT1053A *PowIRtrain* (Figure 3) provides the complete power conversion function for a 1hp (0.75kW) variable voltage, variable frequency AC motor controller. The *PowIRtrain* combines the Power Module (IRPT1053A) with a Driver-Plus Board (IRPT1053D). The *PowIRtrain* Design Kit, IRPT1053E includes the following:

- Complete *PowIRtrain* integrated power stage
- Specification and operating instructions
- Bill of materials
- Electrical schematic
- Mechanical layout for Driver-Plus Board
- Software transferrable files for easy design integration
- Application information and layout considerations

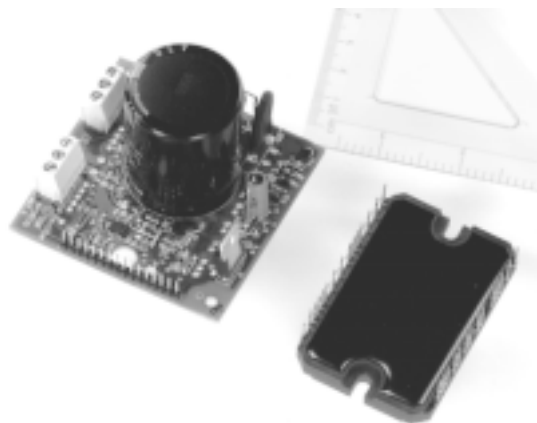


Figure 3. IRPT1053C *PowIRtrain*

Specifications

PARAMETERS	VALUES	CONDITIONS
Input Power		
Voltage	220V, -15%, +10%, 3-phase	
Frequency	50 - 60Hz	
Current	6.2 Arms @ nominal output	$T_A = 40^{\circ}\text{C}$, $R_{thSA} = 1.07^{\circ}\text{C/W}$
I_{FSM}	175 A	10ms half-cycle, non-repetitive surge
Output Power		
Voltage	0 - 230V rms	defined by external PWM control
Nominal motor hp (kW)	1hp (0.75kW) nominal full load power 150% overload for 1 minute	$V_{in} = 220\text{V}$, $f_{pwm} = 4\text{kHz}$, $f_o = 60\text{Hz}$ $T_A = 40^{\circ}\text{C}$, $R_{thSA} = 1.07^{\circ}\text{C/W}$
Nominal motor current	4.4A nominal full load current 6.6A 150% overload for 1 minute	
DC Link		
DC link voltage	425V maximum	
Sensor		
Temp. sense resistance	50kOhms $\pm$ 5% 3.1kOhms $\pm$ 10%	@ $T_{NTC} = 25^{\circ}\text{C}$ @ $T_{NTC} = 100^{\circ}\text{C}$
Current sense	45mOhms $\pm$ 5%	@ $T_{SHUNT} = 25^{\circ}\text{C}$
Protection		
IGBT short circuit time	10 μ s	DC bus = 425V, $V_{GE} = 15\text{V}$, line to line short
Recommended short circuit-shutdown current	30A peak	
Gate Drive		
Q_G	39 nC (typical)	$V_{GE} = 15\text{V}$ refer Figure 5b
Recommended gate driver	IR2132J (see figure 10)	refer design kit IRPT1053E
Module		
Isolation voltage	2500V rms	pin to base plate, 60Hz, 1 minute
Operating case temperature	-25°C to 125°C	95%RH max. (non-condensing)
Mounting torque	1 Nm	M4 screw type
Storage temp. range	-40°C to 125°C	
Soldering temperature for 10 sec.	260°C maximum	at the pins (.06" from case)

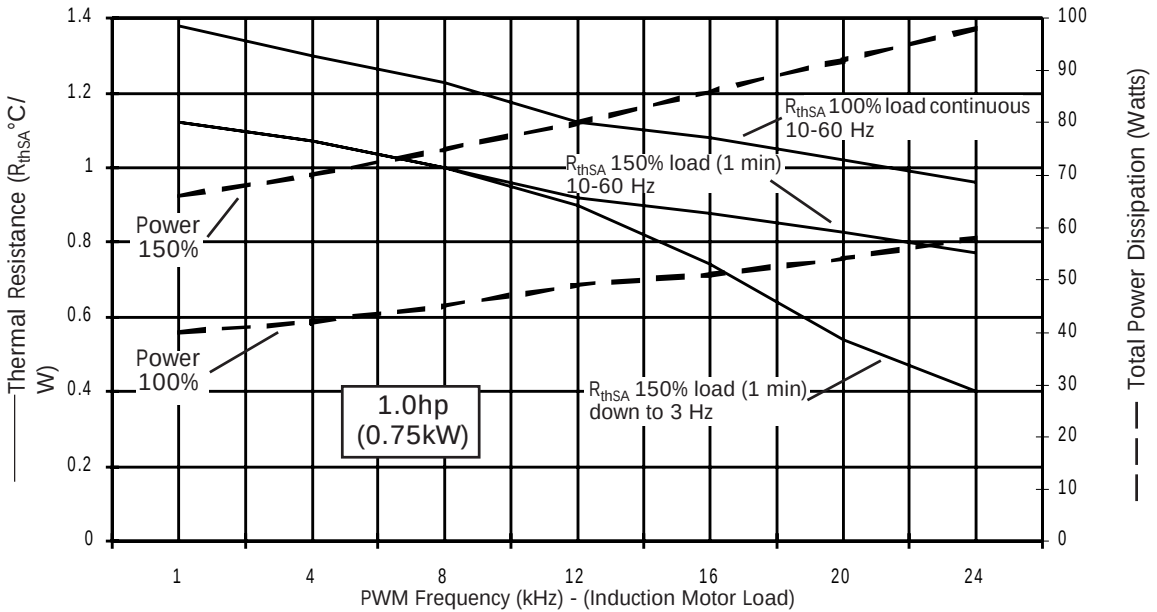


Figure 4a. 1hp/4.4A output Heat sink Thermal Resistance and Power Dissipation vs. PWM Frequency

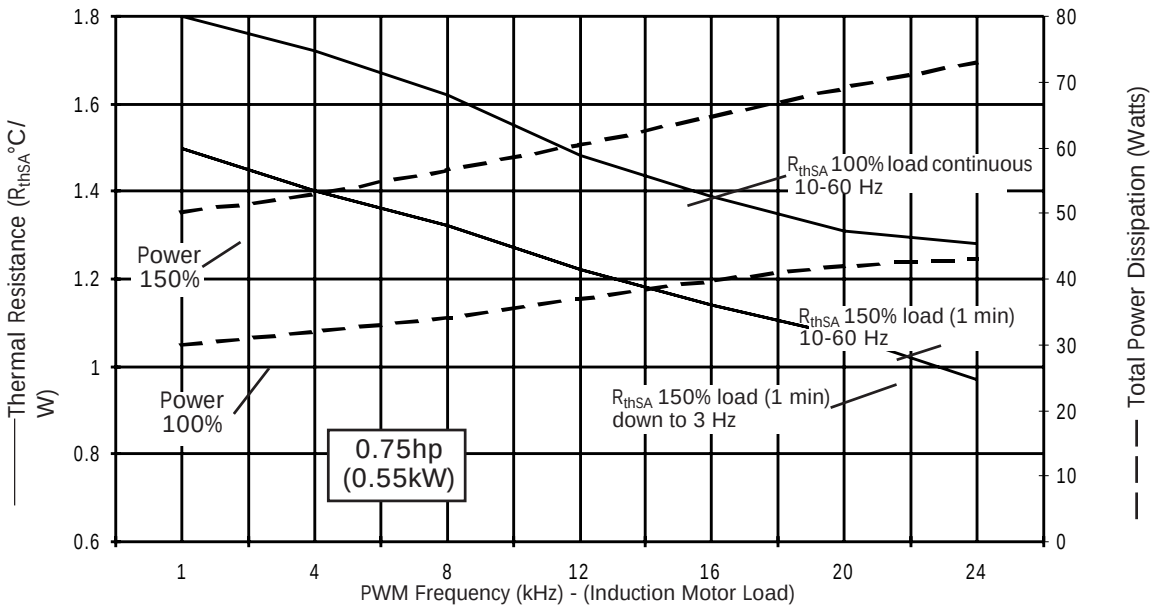


Figure 4b. 0.75hp/3.5A output Heat sink Thermal Resistance and Power Dissipation vs. PWM Frequency

NOTE: For figures 4a and 4b – Operating Conditions: V_{in} = 230 Vrms, MI = 1.15, P.F. = 0.8, T_A = 40°C. Z_{thSA} limits ΔT_c rise during 1 minute overload to 10°C.

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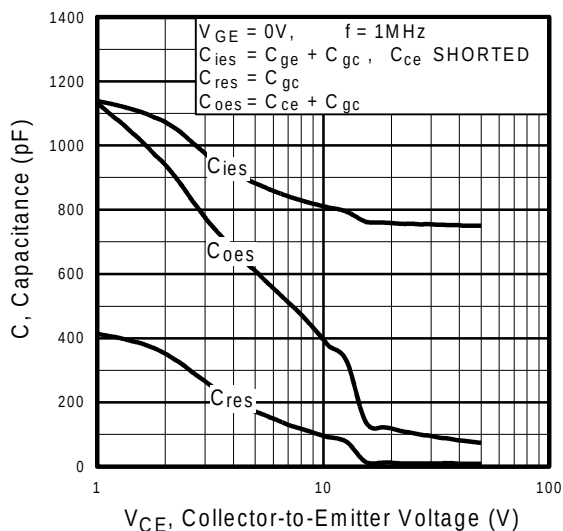


Figure 5a - Typical Capacitance vs. Collector-to-Emitter Voltage

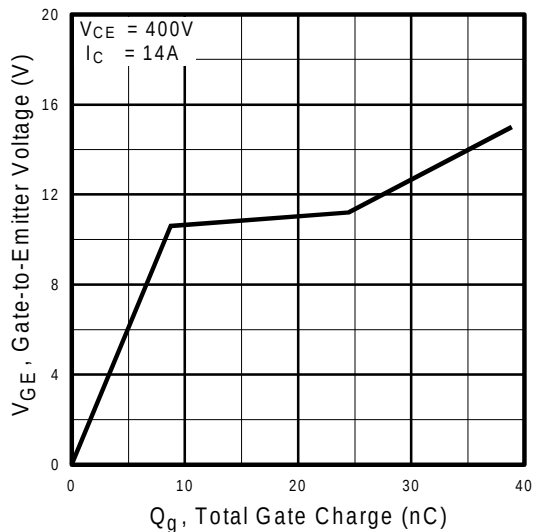


Figure 5b - Typical Gate Charge vs. Gate-to-Emitter Voltage

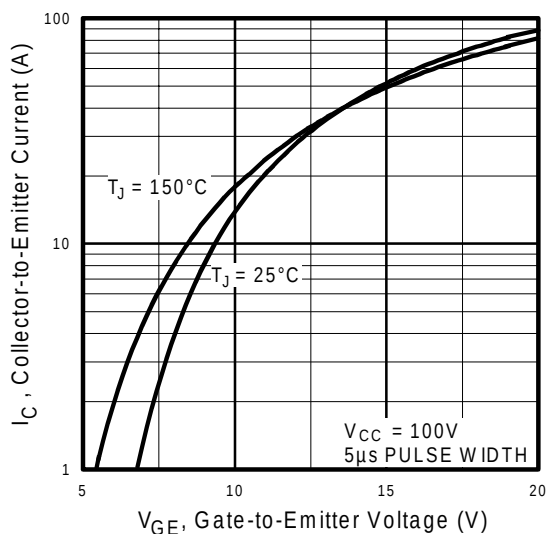


Figure 5c - Typical Transfer Characteristics

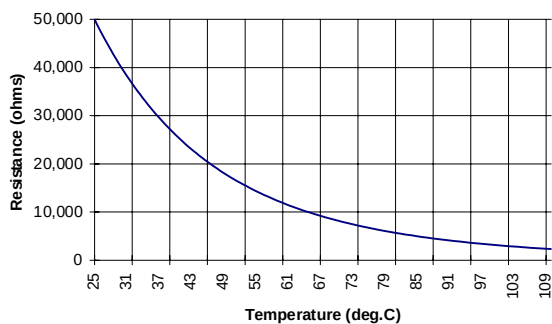


Figure 6 - Normal R-T Characteristics of the NTC Thermistor

Mounting Procedure

Mounting

1. Connect the driver board and the IRPT1053A power module.
2. Remove all particles and grit from the heat sink and power substrate.
3. Spread a .004" to .005" layer of silicone grease on the heat sink, covering the entire area that the power substrate will occupy. Recommended heat sink flatness is .001 inch/ inch and Total Indicator Readout (TIR) of .003 inch below substrate
4. Place the power substrate onto the heat sink with the mounting holes aligned and press it firmly into the silicone grease.
5. Place the 2 M4 mounting screws through the PCB and power module and into the heat sink and tighten the screws to 1 Nm torque.

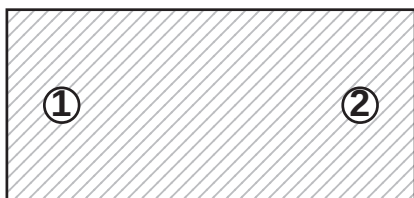


Figure 6. Power Module Mounting Screw Sequence

Power Connections

The power module pin designation, function and other details can be obtained from the package outline in Figure 8 and circuit diagram in Figure 9. Three phase input connections made to pins R, S and T and inverter output connections are made to pins U, V and W. Positive rectifier output and positive inverter bus are brought out to pins RP and P respectively in order to provide DC bus capacitor soft charging implementation option. The current shunt terminals are connected to pins ISI, IS2 and IS3, IS4 on the positive and negative DC rail respectively.

Functional Information

Heat Sink Requirements

Figures 4a-4b show the thermal resistance of the heat sink required for various output power levels and Pulse-Width-Modulated (PWM) switching frequencies. Maximum total losses of the unit are also shown. This data is based on the following key operating conditions:

- The maximum continuous combined losses of the rectifier and inverter occur at full pulse-width-modulation. These maximum losses set the maximum continuous operating temperature of the heat sink.
- The maximum combined losses of the rectifier and inverter at full pulse-width-modulation under overload set the incremental temperature rise of the heat sink during overload.
- The minimum output frequency at which full load current is to be delivered sets the peak IGBT junction temperature.
- At low output frequency, IGBT junction temperature tends to follow the instantaneous fluctuations of the output current. Thus, peak junction temperature rise increases as output frequency decreases.

Over Temperature Protection

Over temperature can be detected using the NTC thermistor included in the power module for thermal sensing. Protection circuit that initiates a shutdown if the temperature of the IMS substrate exceeds a set level can be implemented. The nominal resistance vs. temperature characteristic of the thermistor is given in Figure 6.

Voltage Rise During Braking

The motor will feed energy back to the DC link during regenerative braking, forcing the DC bus voltage to rise above the level defined by input line voltage. Deceleration of the motor must be controlled by appropriate PWM control to keep the DC bus voltage within the rated maximum value.

IRPT1053A Mechanical Specifications

NOTE: Dimensions are in inches [millimeters]

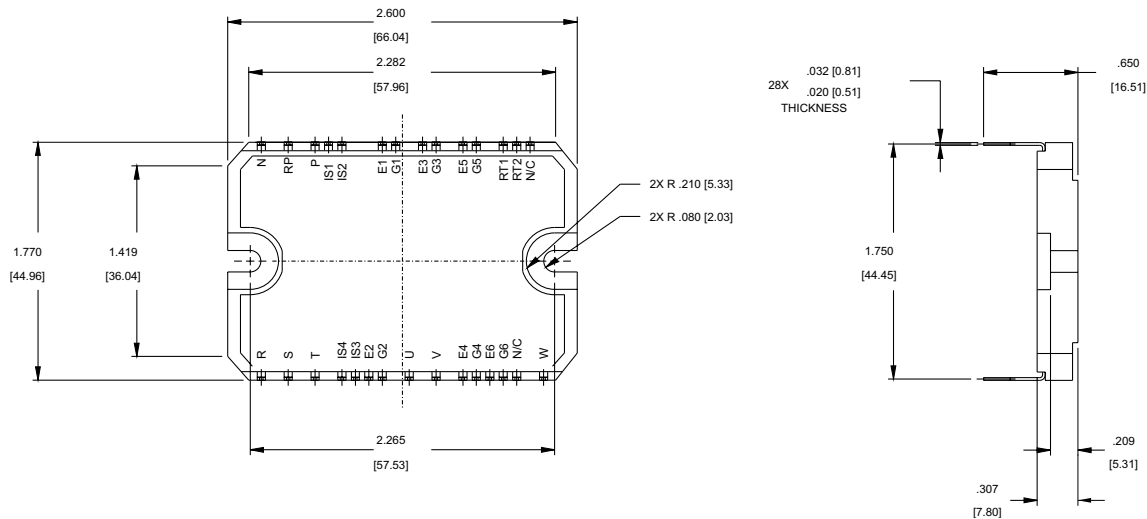


Figure 7.

IRPT1053A Mechanical Specifications

NOTE: Dimensions are in inches [millimeters]

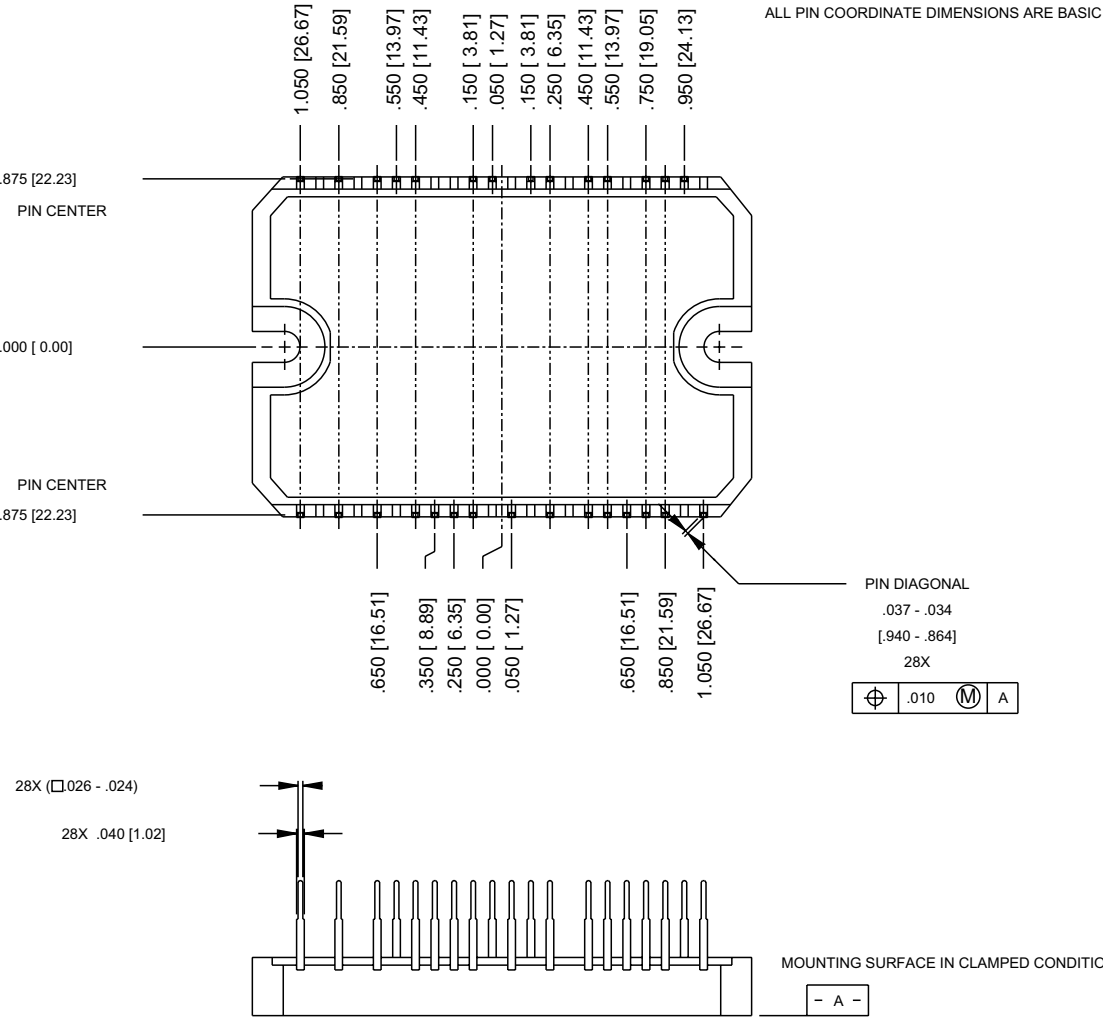


Figure 8.

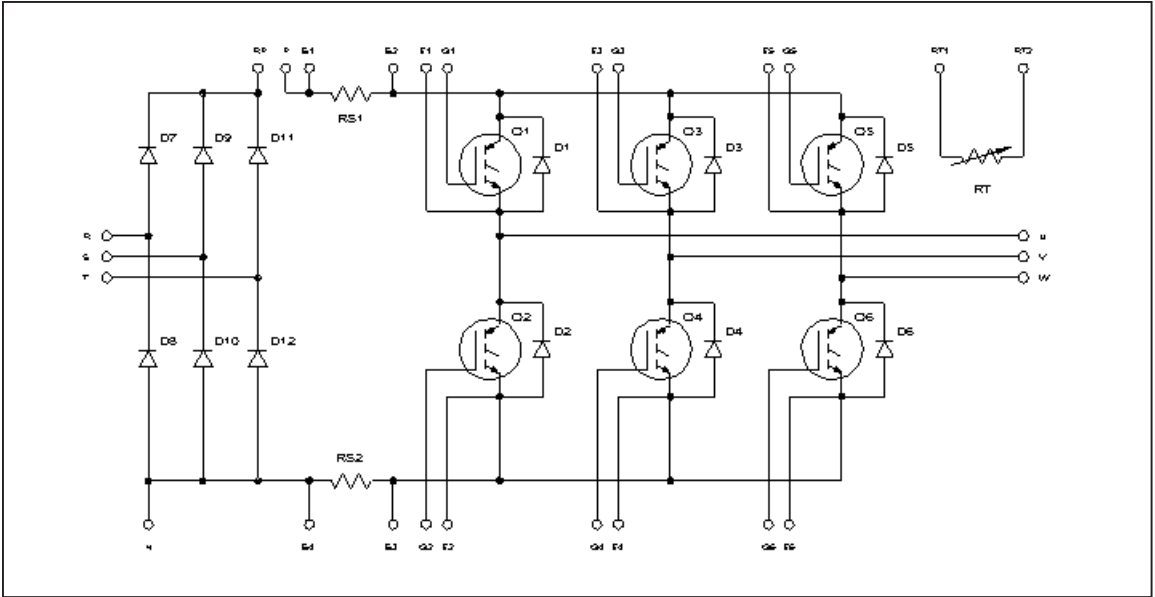


Figure 9. Power Module Circuit Diagram

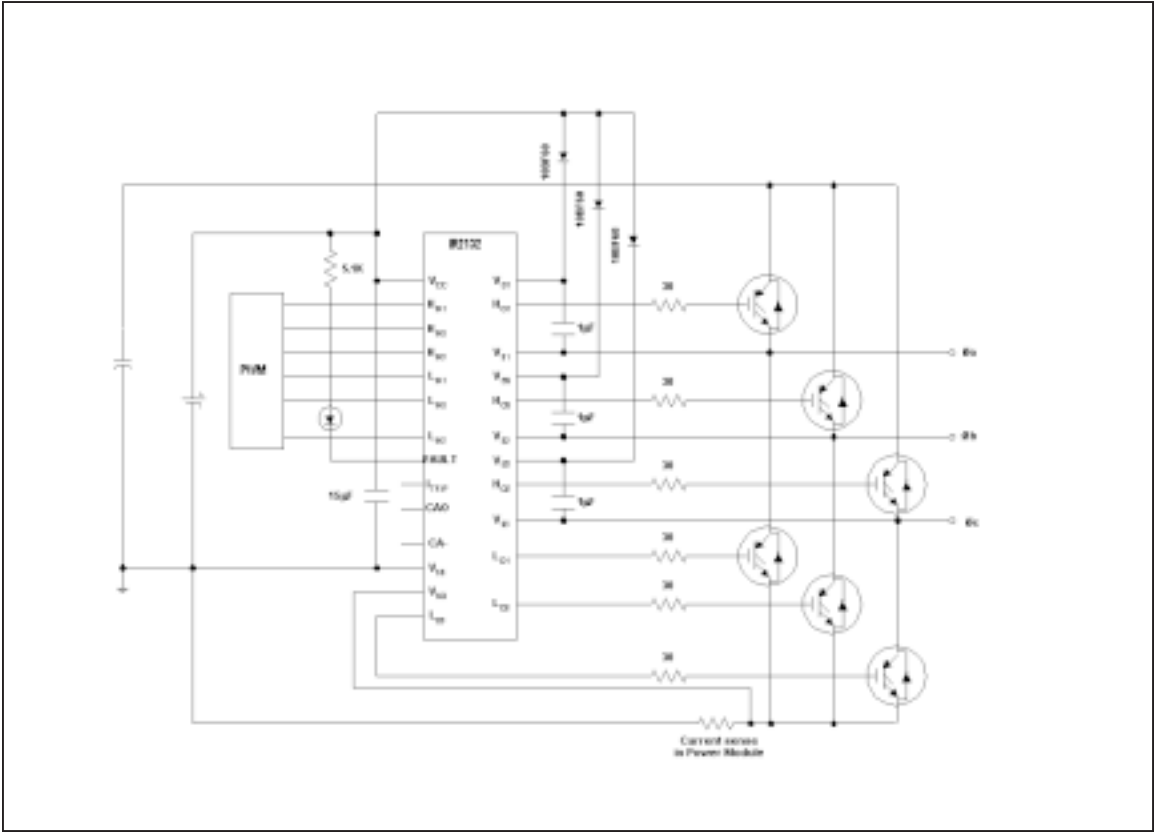


Figure 10. Recommended Gate Drive Circuit

Part Number Identification and Ordering Instructions

IRPT1053A Power Module

Chip and wire epoxy encapsulated module with 800V rectifiers, 600V short-circuit rated, ultra-fast IGBT inverter with ultra-fast freewheeling diodes, temperature sensing NTC thermistor and current-sensing low-inductance shunts.

IRPT1053C *POWIRTRAIN*

Integrated Power Module (IRPT1053A) and Driver-Plus Board (IRPT1053D) pre-assembled and tested to meet all system specifications.

IRPT1053D Driver-Plus Board

Printed Circuit board assembled with DC link capacitors, NTC in-rush limiting thermistor, high-power terminal blocks, surge suppression MOVs, IGBT gate drivers, protection circuitry and low power supply. The PCB is functionally tested with standard power module to meet all system specifications.

IRPT1053E Design Kit

Complete *POWIRTRAIN* (IRPT1053C) with full set of design documentation including schematic diagram, bill of material, mechanical layout of Driver Board, schematic files, Gerber files and design tips.

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