2SB0819 (2SB819)

Silicon PNP epitaxial planer type

For low-frequency output amplification Complementary to 2SD1051

Features

- High collector to emitter voltage V_{CEO}.
- Large collector power dissipation P_C.
- M type package allowing easy automatic and manual insertion as well as stand-alone fixing to the printed circuit board.

Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Ratings	Unit
Collector to base voltage	V_{CBO}	-50	V
Collector to emitter voltage	V_{CEO}	-40	V
Emitter to base voltage	$V_{\rm EBO}$	-5	V
Peak collector current	I_{CP}	-3	A
Collector current	I_{C}	-1.5	A
Collector power dissipation	P_{C}^{*}	1	W
Junction temperature	T _j	150	°C
Storage temperature	T_{stg}	−55 ~ +150	°C

 $^{^{\}ast}$ Printed circuit board: Copper foil area of 1cm² or more, and the board thickness of 1.7mm for the collector portion

Unit: mm 6.9±0.1 1.5 1.5 R0.9 0.85 0.85 1.Base 2:Collector EIAJ:SC-71 3:Emitter M Type Mold Package

Electrical Characteristics (Ta=25°C)

Parameter	Symbol	Conditions	min	typ	max	Unit
C. 11	I _{CBO}	$V_{CB} = -20V, I_{E} = 0$			-1	μΑ
Collector cutoff current	I _{CEO}	$V_{CE} = -10V, I_{B} = 0$			-100	μΑ
Emitter cutoff current	I _{EBO}	$V_{EB} = -5V, I_C = 0$			-10	μA
Collector to base voltage	V _{CBO}	$I_{\rm C} = -1 \text{mA}, I_{\rm E} = 0$	-50			V
Collector to emitter voltage	V _{CEO}	$I_{\rm C} = -2mA, I_{\rm B} = 0$	-40			V
Forward current transfer ratio	h _{FE} *1	$V_{CE} = -5V, I_C = -1A^{*2}$	80		220	
Collector to emitter saturation voltage	V _{CE(sat)}	$I_C = -1.5A, I_B = -0.15A^{*2}$			-1	V
Base to emitter saturation voltage	V _{BE(sat)}	$I_C = -2A$, $I_B = -0.2A^{*2}$			-1.5	V
Transition frequency	f_T	$V_{CB} = -5V$, $I_E = 0.5A$, $f = 200MHz$		150		MHz
Collector output capacitance	C _{ob}	$V_{CB} = -20V, I_{E} = 0, f = 1MHz$		45		pF

^{*2} Pulse measurement

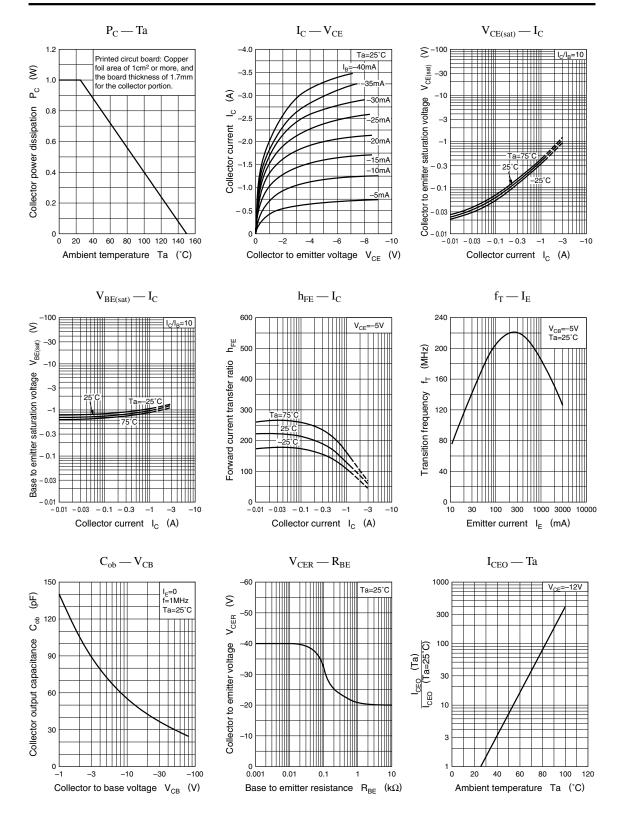
^{*1}hFE Rank classification

Rank	Q	R
h _{FE}	80 ~ 160	120 ~ 220

Note.) The Part number in the Parenthesis shows conventional part number.

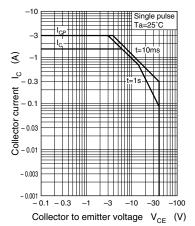
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Transistor 2SB0819



Transistor 2SB0819

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