

HMC336MS8G

GaAs MMIC SPDT NON-REFLECTIVE POSITIVE CONTROL SWITCH, DC* - 6.0 GHz

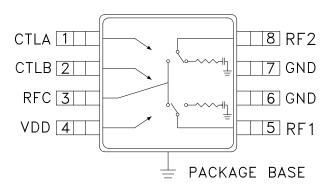
Typical Applications

This switch is suitable for usage in DC - 6.0 GHz 50-Ohm or 75-Ohm systems:

v01.1202

- Broadband
- Fiber Optics
- Switched Filter Banks
- Wireless below 6.0 GHz

Functional Diagram



Features

Broadband Performance: DC - 6.0 GHz High Isolation: 42 dB@ 6 GHz Low Insertion Loss: 1.6 dB@ 6 GHz MSOP8G SMT Package

General Description

The HMC336MS8G is a broadband non-reflective GaAs MESFET SPDT switch in a low cost 8 lead MSOP8G surface mount package with an exposed ground paddle. Covering DC to 6.0 GHz, this switch offers high isolation and low insertion loss. The switch operates using a positive control voltage of 0/+5 Volts, and requires a fixed bias of +5V. This switch is suitable for usage in 50-Ohm or 75-Ohm systems.

Electrical Specifications, T_A = +25° C, With 0/+5V Control, 50 Ohm System

Parameter		Frequency	Min.	Тур.	Max.	Units
Insertion Loss		DC - 2.0 GHz DC - 4.0 GHz DC - 6.0 GHz		1.2 1.4 1.6	1.6 1.8 2.0	dB dB dB
Isolation		DC - 2.0 GHz DC - 4.0 GHz DC - 6.0 GHz	42 39 37	47 44 42		dB dB dB
Return Loss	"On State"	DC - 2.0 GHz DC - 6.0 GHz	9 6	12 9		dB dB
Return Loss (RF1, RF2)	"Off State"	2.0 - 6.0 GHz	13	18		dB
Input Power for 1 dB Compression		0.5 - 6.0 GHz	20	25		dBm
Input Third Order Intercept (Two-Tone Input Power = +7 dBm Each Tone, 1 MHz Tone Spacing)		0.5 - 6.0 GHz	37	42		dBm
Switching Characteristics tRISE, tFALL (10/90% RF) tON, tOFF (50% CTL to 10/90% RF)		DC - 6.0 GHz		8 20		ns ns

* DC blocking capacitors are required at ports RFC, RF1 and RF2. Their value will determine the lowest transmission frequency.

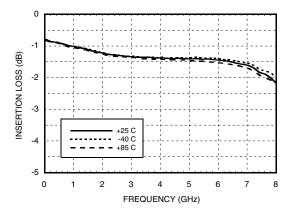


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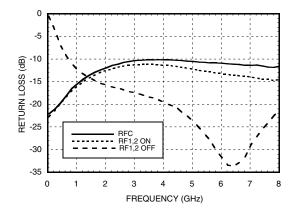
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Isolation

Insertion Loss vs. Temperature

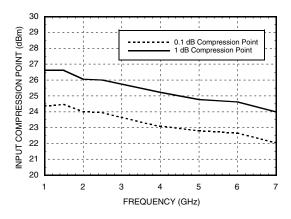


Return Loss

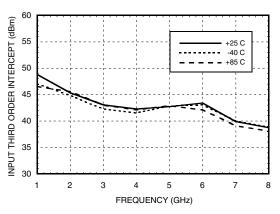


0 -10 RF1 RF2 -20 **ISOLATION (dB)** -30 -40 -50 -60 -70 0 2 3 6 7 1 4 5 8 FREQUENCY (GHz)

0.1 and 1 dB Input Compression Point







For price, delivery, and to place orders, please contact Hittite Microwave Corporation: 12 Elizabeth Drive, Chelmsford, MA 01824 Phone: 978-250-3343 Fax: 978-250-3373 Order Online at www.hittite.com



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Absolute Maximum Ratings

Bias Voltage Range (Vdd)	+7.0 Vdc
Control Voltage Range (A & B)	-0.5V to Vdd +1.0 Vdc
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C
Maximum Input Power	+28 dBm

v01.1202

Note:

DC blocking capacitors are required at ports RFC and RF1, 2. Their value will determine the lowest transmission frequency.

Bias Voltage & Current

Vdd	ldd (Typ.)	ldd (Max.)
(Vdc)	(μΑ)	(μΑ)
+5.0	35	100

Truth Table

Control Input		Signal Path State	
А	В	RFCOM to:	
Low	High	RF1	
High	Low	RF2	

Control Voltages

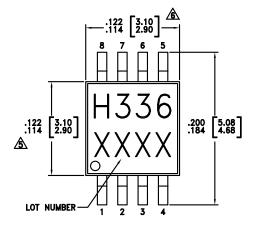
State	Bias Condition
Low	0 to 0.2 Vdc @ 35 µA Typical
High	+5 Vdc @ 10 μA Typical

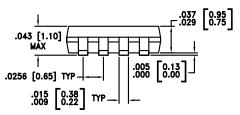


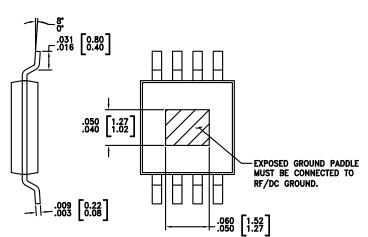
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Outline Drawing







NOTES:

1. PACKAGE BODY MATERIAL: LOW STRESS INJECTION MOLDED PLASTIC SILICA AND SILICON IMPREGNATED.

2. LEADFRAME MATERIAL: COPPER ALLOY

3. LEADFRAME PLATING: Sn/Pb SOLDER

4. DIMENSIONS ARE IN INCHES [MILLIMETERS].

DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.15mm PER SIDE.

A DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.25mm PER SIDE.

7. ALL GROUND LEADS AND GROUND PADDLE MUST BE SOLDERED TO PCB RF GROUND.

Pin Descriptions

Pin Number	Function	Description	Interface Schematic
1	CTLA	See truth table and control voltage table.	CR
2	CTLB	See truth table and control voltage table.	
3, 5, 8	RFC, RF1, RF2	This pin is DC coupled and matched to 50 Ohm. Blocking capacitors are required.	
4	VDD	Supply Voltage. This pin may be left floating with degradation of power performance by approximately 1.5 dB.	
6, 7	GND	Package bottom has exposed metal paddle that must also be connected to PCB RF ground.	

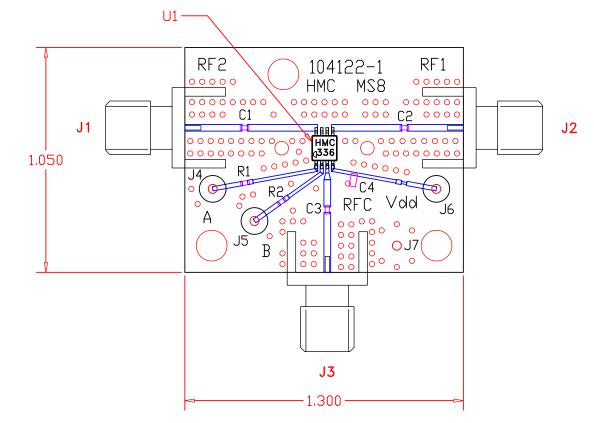
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Evaluation PCB



List of Material

Item	Description	
J1 - J3	PC Mount SMA RF Connector	
J4 - J7	DC Pin	
C1 - C3	100 pF Capacitor, 0402 Pkg.	
C4	10k pF Capacitor, 0603 Pkg.	
R1 - R2	100 Ohm Resistor, 0402 Pkg.	
U1	HMC336MS8G SPDT Switch	
PCB*	104122 Evaluation PCB 1.05"x1.30"	
* Circuit Board Material: Rogers 4350		

The circuit board used in the final application should be generated with proper RF circuit design techniques. Signal lines at the RF port should have 50 ohm impedance and the package ground leads and backside ground slug should be connected directly to the ground plane similar to that shown above. The evaluation circuit board shown above is available from Hittite Microwave Corporation upon request.

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Notes:

SWITCHES - SMT