## The RF Line <br> UHF Linear Amplifier

Designed for linear amplifier applications in 50 Ohm systems requiring wide bandwidth, low noise, and low distortion. Internal DC blocking on RF ports reduces external component count and related circuit area. This hybrid utilizes push-pull circuit design.

- Supply Voltage: 15 Vdc (MHL8115)

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28 \text { Vdc (MHL8118) }
$$

- Third Order Intercept: 41.5 dBm Typ
- Power Gain: 17.5 dB Typ (@ 900 MHz )
- Excellent Phase Linearity and Group Delay Characteristics
- 50 Ohm Input/Output Impedances


## MHL8115 MHL8118

$1 \mathrm{~W}, 17.5 \mathrm{~dB}$
$50-1000 \mathrm{MHz}$ LINEAR AMPLIFIERS


CASE 448-02
MHL8115, STYLE 2 MHL8118, STYLE 1

ABSOLUTE MAXIMUM RATINGS $\left(\mathrm{T}_{\mathrm{C}}=25^{\circ} \mathrm{C}\right.$ unless otherwise noted)

| Rating | Symbol | Value | Unit |
| :--- | :---: | :---: | :---: |
| DC Supply Voltage | MHL8115 <br>  <br>  <br>  <br> MHL8118 | $\mathrm{V}_{\mathrm{CC}}$ | 18 |
| 32 | Vdc |  |  |
| RF Input Power | $\mathrm{P}_{\text {in }}$ | +20 | dBm |
| Storage Temperature Range | $\mathrm{T}_{\text {stg }}$ | -40 to +100 | ${ }^{\circ} \mathrm{C}$ |
| Operating Case Temperature Range | $\mathrm{T}_{\mathrm{C}}$ | -20 to +100 | ${ }^{\circ} \mathrm{C}$ |

ELECTRICAL CHARACTERISTICS $\left(T_{C}=+25^{\circ} \mathrm{C} ; \mathrm{V}_{\mathrm{CC}}=15 \mathrm{Vdc}\right.$ (MHL8115), 28 Vdc (MHL8118); $50 \Omega$ System)

| Characteristic |  | Symbol | Min | Typ | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Supply Current | $\begin{aligned} & \hline \text { MHL8115 } \\ & \text { MHL8118 } \end{aligned}$ | IDC | - | $\begin{aligned} & 700 \\ & 400 \end{aligned}$ | $\begin{aligned} & 760 \\ & 440 \end{aligned}$ | mA |
| Power Gain | ( $\mathrm{f}=900 \mathrm{MHz}$ ) | PG | 16.5 | 17.5 | - | dB |
| Gain Flatness | (f = 50-1000 MHz) | FL | - | 1.0 | 2.0 | dB |
| Power Output @ 1 dB Comp. | (f = 900 MHz ) | Pout 1 dB | 29 | 30 | - | dBm |
| Third Order Intercept (f1 = 879 MHz, f2 = 884 MHz) |  | ITO | 40.5 | 41.5 | - | dBm |
| Input/Output VSWR | $\begin{aligned} & (\mathrm{f}=50-900 \mathrm{MHz}) \\ & (\mathrm{f}=900-1000 \mathrm{MHz}) \end{aligned}$ | VSWR | - | - | $\begin{aligned} & \hline 2.0: 1 \\ & 2.6: 1 \end{aligned}$ |  |
| Noise Figure, Broadband | $\begin{aligned} & (\mathrm{f}=500 \mathrm{MHz}) \\ & (\mathrm{f}=1000 \mathrm{MHz}) \end{aligned}$ | NF |  | $\begin{aligned} & \hline 7.5 \\ & 8.5 \end{aligned}$ | $\begin{aligned} & \hline 8.5 \\ & 9.5 \end{aligned}$ | dB |
| Second Harmonic Distortion ( $\mathrm{P}_{\mathrm{O}}=100 \mathrm{~mW}, \mathrm{f}_{2} \mathrm{H}=1000 \mathrm{MHz}$ ) |  | dso | - | -55 | -45 | dB |
| Second Order Intermodulation Distortion ( $\mathrm{P}_{\mathrm{o}}=2.75 \mathrm{dBm}, \mathrm{f}_{1}=373 \mathrm{MHz}, \mathrm{f}_{2}=450 \mathrm{MHz}$ ) |  | IM2 | - | -65 | -60 | dB |
| Intermodulation Distortion, 3 Tone ( $f=860 \mathrm{MHz}, \mathrm{P}_{\text {sync }}=200 \mathrm{~mW}$ ) |  | IM3 | - | -60 | - | dB |



Figure 1. MHL8115 External Connections
(Case 448-02, Style 2)


C1, C2 $\geq 0.01 \mu \mathrm{~F}$ (CHIP)
R1 $=90 \Omega, 3$ WATTS

Figure 1. MHL8118 External Connections
(Case 448-02, Style 1)


NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.

|  | INCHES |  | MILLIMETERS |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| DIM | MIN | MAX | MIN | MAX |  |  |
| A | 1.740 | 1.760 | 44.20 | 44.70 |  |  |
| B | 0.550 | 0.570 | 13.97 | 14.49 |  |  |
| C | 0.405 | 0.445 | 10.29 | 11.30 |  |  |
| D | 0.018 | 0.022 | 0.46 | 0.55 |  |  |
| E | 0.085 | 0.095 | 2.16 | 2.41 |  |  |
| G | 0.200 |  | BSC | 5.08 |  | BSC |
| H | 0.120 BSC |  | 3.05 BSC |  |  |  |
| J | 0.009 | 0.011 | 0.23 | 0.28 |  |  |
| K | 0.180 | 0.220 | 4.57 | 5.59 |  |  |
| N | 1.045 | 1.075 | 26.54 | 27.30 |  |  |
| Q | 0.145 | 0.155 | 3.68 | 3.94 |  |  |
| R | 0.455 | 0.465 | 11.56 | 11.81 |  |  |
| U | 1.490 | 1.510 | 37.85 | 38.35 |  |  |



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STYLE 1:
    2. GROUND
        3. RESISTOR-GROUND
        RESIS
    4. VCC
    5. RF OUTPUT
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CASE 448-02
ISSUE A

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