

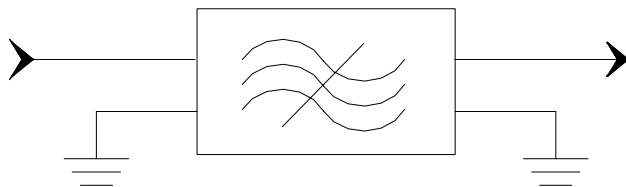
### Specifications

| Parameter                                   | Unit                   | Minimum                                 | Typical | Maximum |
|---|------------------------|---|---------|---------|
| Center Frequency                            | MHz                    | 69.95                                   | 70      | 70.05   |
| Insertion Loss                              | dB                     | -                                       | 21.5    | 24      |
| 3 dB Bandwidth                              | MHz                    | 7.75                                    | 7.8     | 7.86    |
| Selectivity                                 | $F0 \pm 4.3\text{MHz}$ | dBc                                     | 44      | -       |
|   | $F0 \pm 4.5\text{MHz}$ | dBc                                     | 58      | -       |
|   | $F0 \pm 4.9\text{MHz}$ | dBc                                     | 68      | -       |
|   | $F0 \pm 8.9\text{MHz}$ | dBc                                     | 60      | -       |
| Phase Linearity                             | deg                    | -                                       | 4.1     | -       |
| Passband Variation                          | dB                     | -                                       | 0.6     | 1.2     |
| Ultimate Rejection( $f0 \pm 15\text{MHz}$ ) | dB                     | 55                                      | 60      | -       |
| Absolute delay                              | usec                   | -                                       | 3.75    | -       |
| Substrate Material                          |                        | YZ-LiNbO <sub>3</sub>                   |         |         |
| Ambient Temperature                         | °C                     | 25                                      |         |         |
| Package Size                                |                        | DIP3512 (35.2x12.7x5.2mm <sup>3</sup> ) |         |         |

#### Notes:


1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance shown

### Matching Configuration



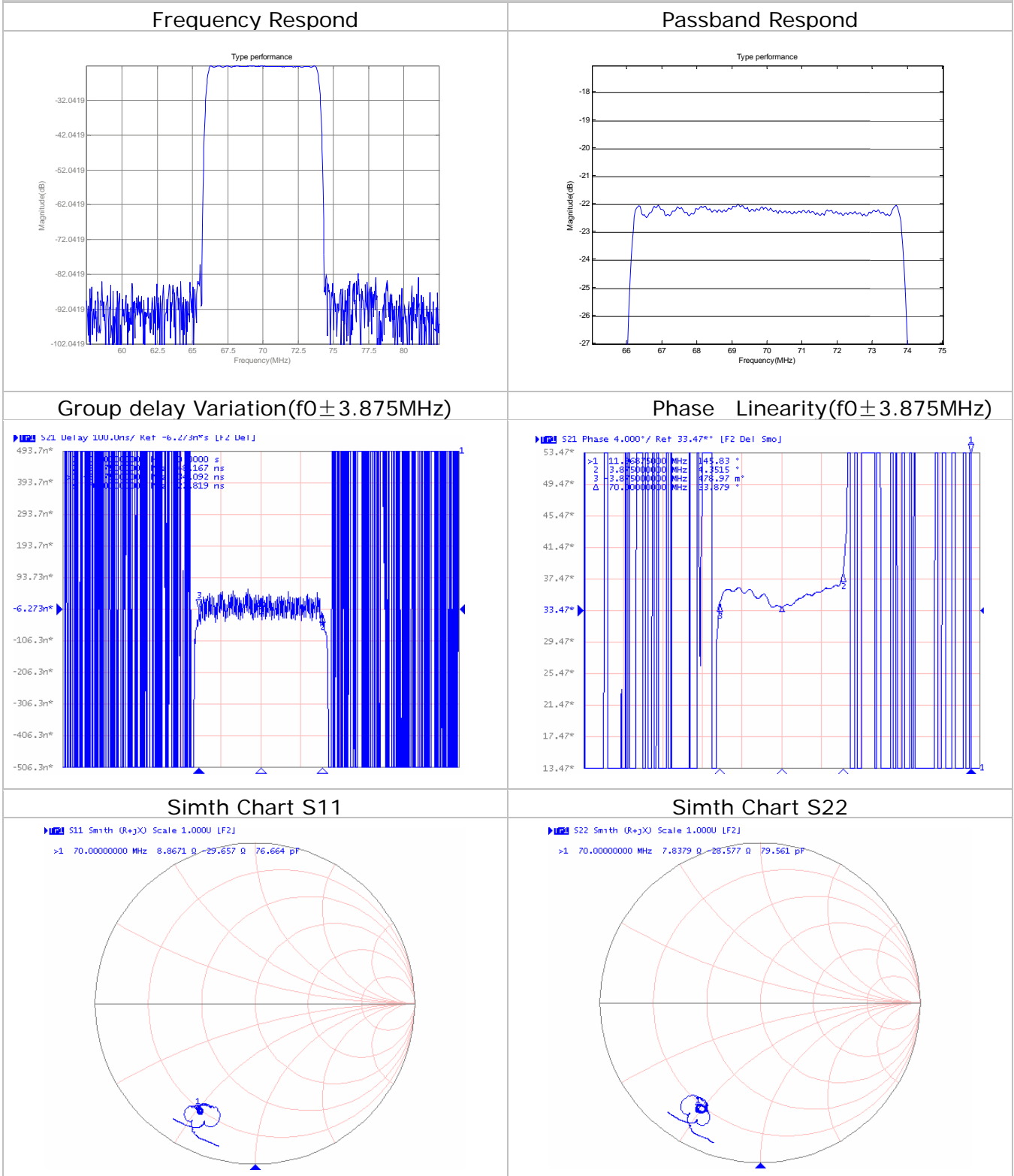
**Source/Load Impedance=50 ohm**

Notes - Component values may change depending on board layout.

|  |   |             |            |      |
|--|---|-------------|------------|------|
|  | <b>SIPAT Co., Ltd.</b><br>( CETC No. 26 Research Institute )<br>Nanping Huayuan Road No. 14<br>Chongqing, China, 400060 | Part Number | LBN07087   |      |
|  |   | Rev. Date   | 2004-12-10 |      |
|  |   | Rev.        | 1.0        | Page |



*Typical Performance*



|  |   |                    |                 |  |
|--|---|--------------------|-----------------|--|
|  | <p><b>SIPAT Co., Ltd.</b><br/>( CETC No. 26 Research Institute )<br/>Nanping Huayuan Road No. 14<br/>Chongqing, China, 400060</p> | <p>Part Number</p> | <p>LBN07087</p> |  |
|  | <p>Rev. Date</p>  | <p>2004-12-10</p>  |                 |  |
|  | <p>Rev.</p>   | <p>1.0</p>         | <p>Page 3/3</p> |  |