

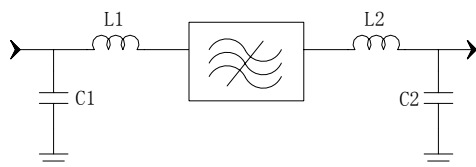
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	139.96	140	140.04
Insertion Loss	dB	-	10.2	12
1 dB Bandwidth	MHz	0.48	0.55	-
3 dB Bandwidth	MHz	-	0.86	-
35 dB Bandwidth	MHz	-	1.98	2.10
Passband Variation	dB	-	0.3	0.6
Absolute Delay	usec	-	1.88	-
Phase Linearity (f0±250KHz)	deg	-	0.8	-
Group Delay Variation (f0±250KHz)	nsec	-	80	200
Ultimate Rejection f0±2.5MHz	dB	37	41	-
f0±20MHz	dB	40	45	-
Substrate Material		Quartz		
Ambient Temperature	°C	25		
Package Size		SF-26		

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

Matching Configuration



L1=82nH L2=100nH

C1=56pF C2=47pF

Source/Load Impedance=50 ohm

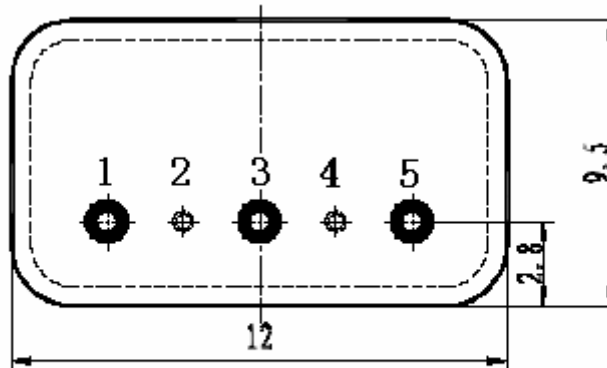
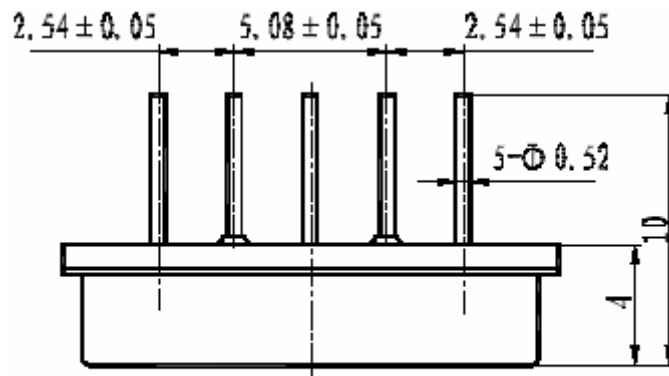
Notes - Component values may change depending on board layout.



SIPAT Co., Ltd.
(CETC No. 26 Research Institute)
Nanping Huayuan Road No. 14
Chongqing, China, 400060

Part Number	LB140DS22	
Rev. Date	2005-4-26	
Rev.	1.0	Page 1/3

Package Dimension



Pin 1:input
pin 5:Output

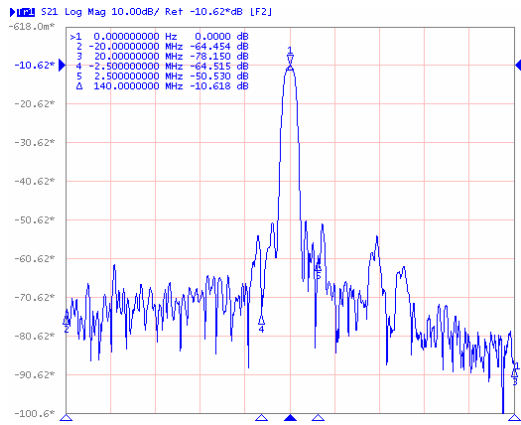


SIPAT Co., Ltd.
(CETC No. 26 Research Institute)
Nanping Huayuan Road No. 14
Chongqing, China, 400060

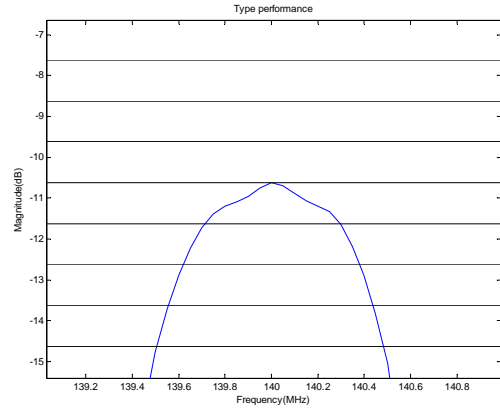
Part Number	LB140DS22	
Rev. Date	2005-4-26	
Rev.	1.0	Page 2/3

Typical Performance

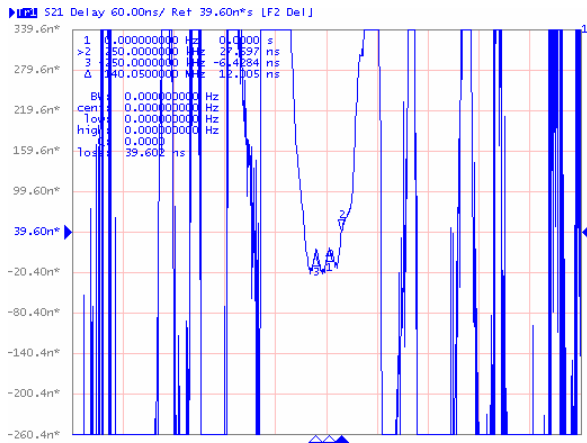
Frequency Respond



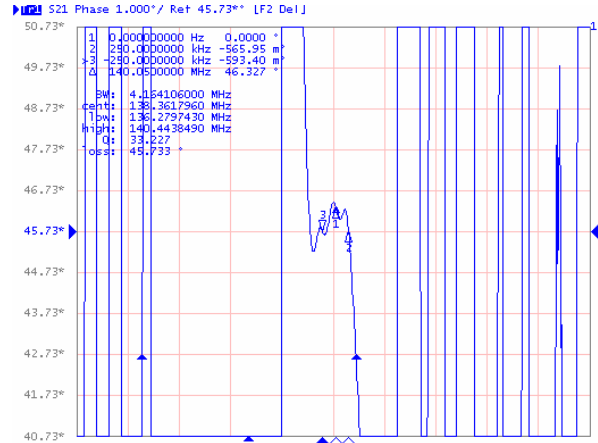
Passband Respond



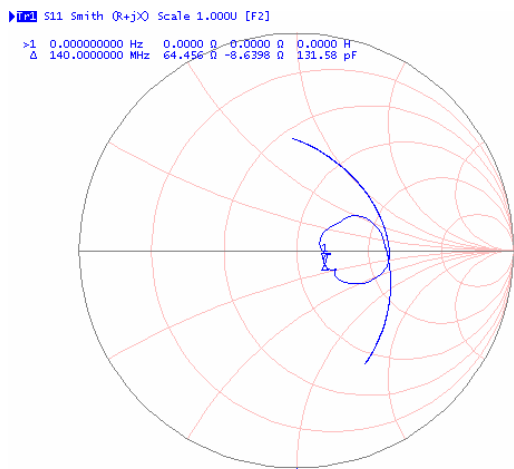
Group Delay Variation($f_0 \pm 250\text{kHz}$)



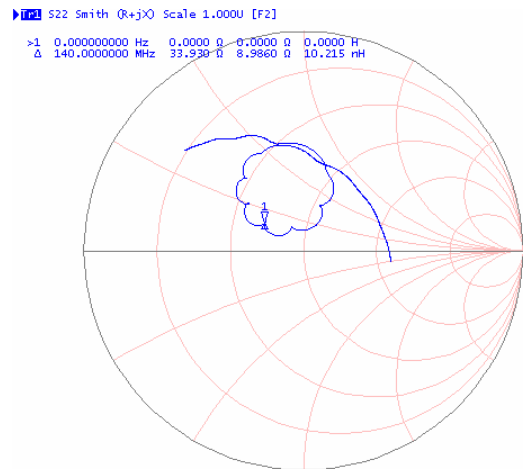
Phase Linearity($f_0 \pm 250\text{kHz}$)



Smith Chart S11



Smith Chart S22



SIPAT Co., Ltd.
(CETC No. 26 Research Institute)
Nanning Huayuan Road No. 14
Chongqing, China, 400060

Part Number

LB140DS22

Rev. Date

2005-4-26

Rev.

1.0

Page 3/3