



Features

- Suitable for low voltage large current drivers
- High DC current gain and large current capability
- Complementary pair with SPS8050

Ordering Information

Type NO.	Marking	Package Code	
SPS8550	SPS8550	TO-92	

Outline Dimensions unit: mm 4.40~4.80 0.50 Max. 1.27 Typ. 1.27 Typ. 2 1 **PIN Connections** 1. Emitter 2. Base 3. Collector

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Absolute maximum ratings

(Ta=25°C)

Characteristic	Symbol	Ratings	Unit
Collector-Base voltage	V_{CBO}	-15	V
Collector-Emitter voltage	V_{CEO}	-12	V
Emitter-Base voltage	V_{EBO}	-6.5	V
Collector current	I_{C}	-1.5	Α
Collector dissipation	P _C	625	mW
Junction temperature	$T_{\mathtt{J}}$	150	°C
Storage temperature	T_{stg}	-55~150	°C

Electrical Characteristics

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Collector-Base breakdown voltage	BV _{CBO}	I_{C} =-50 μ A, I_{E} =0	-15	-	-	V
Collector-Emitter breakdown voltage	BV_{CEO}	$I_C=-1$ mA, $I_B=0$	-12	-	-	V
Emitter-Base breakdown voltage	BV _{EBO}	$I_E = -50 \mu A, I_C = 0$	-6.5	-	-	V
Collector cut-off current	I_{CBO}	V_{CB} =-15V, I_E =0	-	-	-0.1	μА
Emitter cut-off current	I_{EBO}	V_{EB} =-6V, I_C =0	-	-	-0.1	μΑ
DC current gain	h _{FE}	V _{CE} =-1V, I _C =-100mA	200	-	450	-
Collector-Emitter saturation voltage	$V_{CE(sat)}$	I _C =-800mA, I _B =-80mA	-	-	-0.5	V
Transistor frequency	f _T	V_{CE} =-5V, I_{C} =-50mA	-	260	-	MHz
Collector output capacitance	C _{ob}	V_{CB} =-10V, I_E =0, f=1MHz	_	5	-	pF

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Fig. 1 P_C-T_a

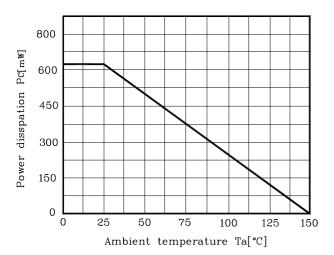


Fig. 3 h_{FE} I_C

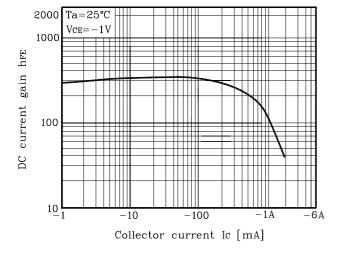


Fig. 2 I_{C} - V_{BE}

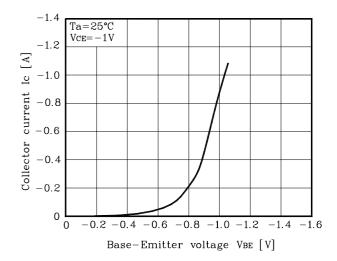
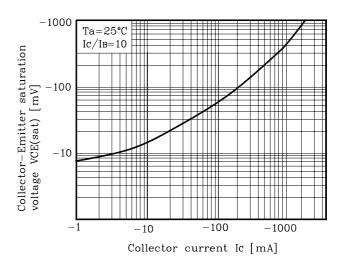


Fig. 4 $V_{CE(sat)}$. I_C



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