

## MicroPower Microprocessor Reset Circuit with Manual Reset

### Features

- 1.2V to 6V Input Voltage Range
- Low Quiescent Current : less Than 1.5  $\mu$ A
- High accuracy detection threshold :  $\pm 1.5\%$
- Fixed trimmed reset thresholds for 1.5V, 1.75V, 2.32V, 2.63V, 2.93V, 3.08V, 3.9V, 4.38V.
- Manual Reset Input
- Reset Timeout Period 250ms
- Available output configurations
  - Open-drain output
  - CMOS active high output
  - CMOS active low output
- SOT-23-5 package
- No external components

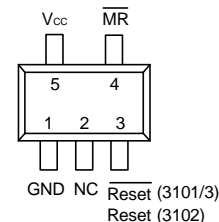
### Applications

- Computers
- Cell Phones
- Portable Electronics
- $\mu$ P Power Supply Monitoring

### General Description

The APR3101/2/3 are designed to monitor voltage supplies in  $\mu$ P and digital systems. The quiescent current is extremely low, typically 1.5  $\mu$ A, making it ideal for portable battery operated equipment. The APR3101/2/3 operate by monitoring the system power supply voltage. When the input voltage drops below a fixed threshold or whenever manual reset is asserted, the device asserts a reset signal for a fixed time period after Vcc rises above the fixed threshold or manual reset is deasserted. The APR3101/2/3 series is available with three output stage versions: APR3101 push-pull active low output, APR3102 push-pull active high output and APR3103 open drain active low output. The APR3101/2/3 also have a debounced manual reset input that is provided to override the reset monitor. They are also designed to reject fast line transient glitches on Vcc. The APR3101/2/3 are come in a miniature SOT-23-5 package.

### Pin Configuration

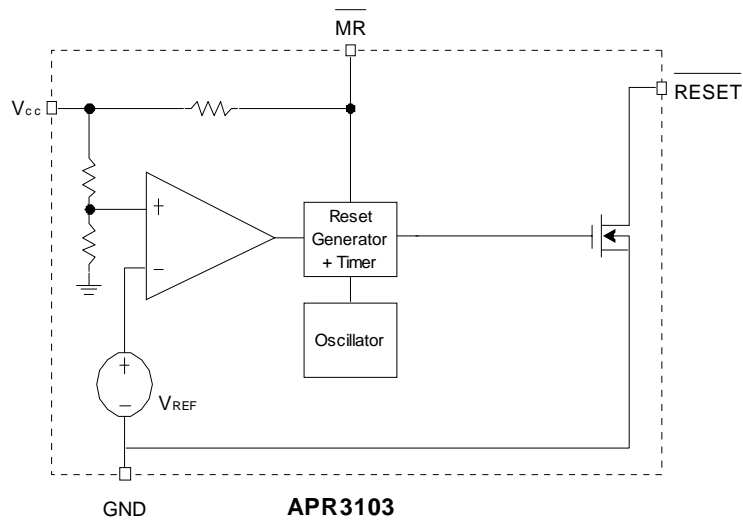
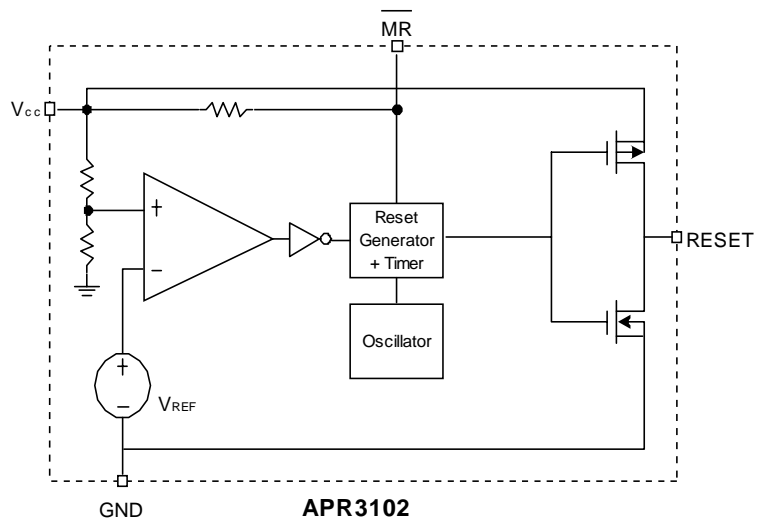
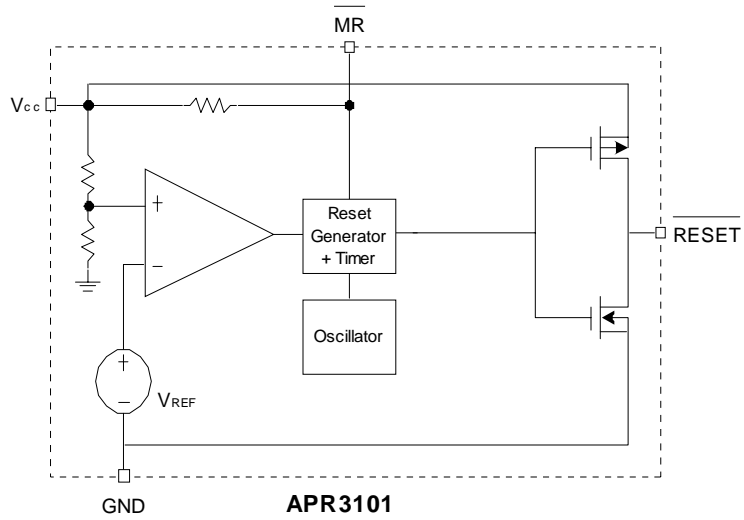


SOT-23-5 (Top View)

ANPEC reserves the right to make changes to improve reliability or manufacturability without notice, and advise customers to obtain the latest version of relevant information to verify before placing orders.



Block Diagram



## Part Number Description

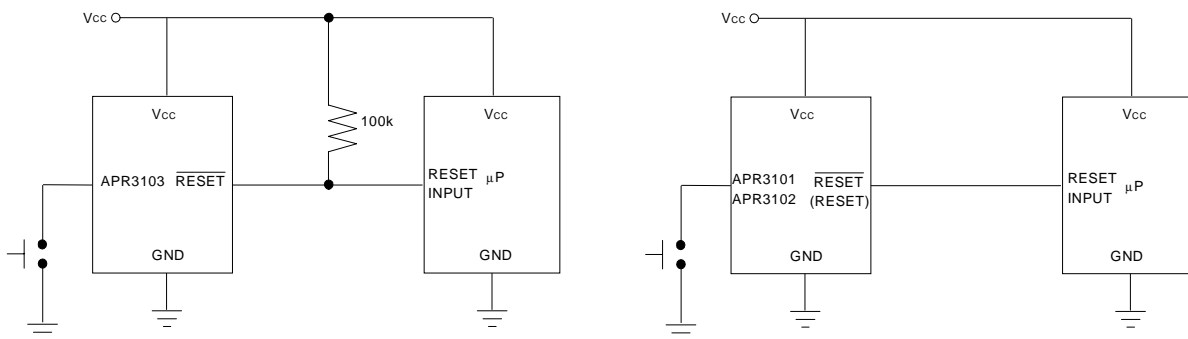
Part Number	Part Description
APR3101	Reset Output Push Pull Active Low with Delay
APR3102	Reset Output Push Pull Active High with Delay
APR3103	Reset Output Open Drain Active Low with Delay

## Electrical Characteristics

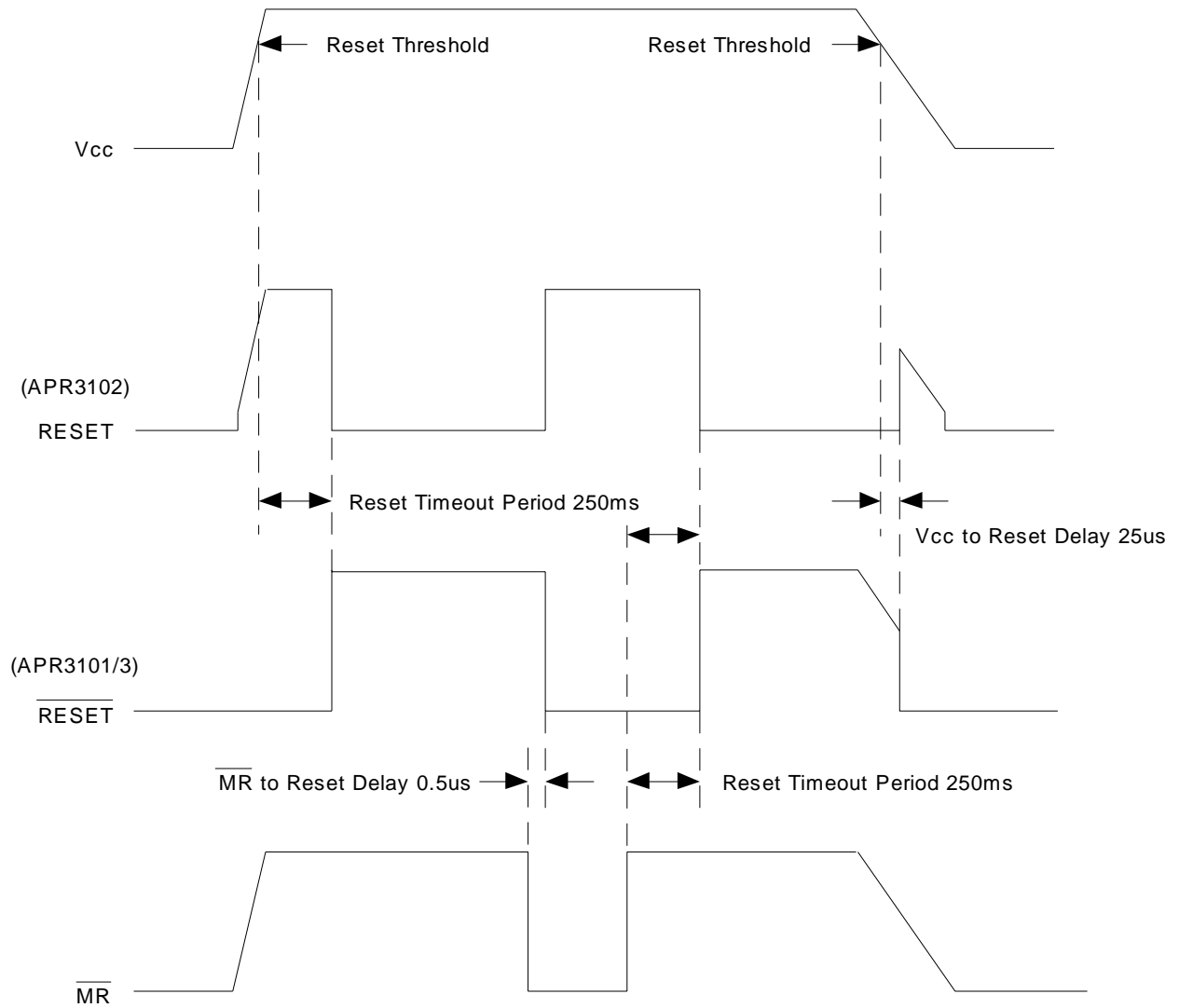
$V_{IN} = 5V$ ,  $T_A = -40$  to  $85^\circ C$  unless otherwise noted. Typical values are at  $T_A = 25^\circ C$ ,  $V_{CC} = 5V$  for 4.38V versions,  $V_{CC} = 3.3V$  for 3.08/2.93V versions,  $V_{CC} = 3.0V$  for 2.63V. versions and  $V_{CC} = 2.5V$  for 2.32V versions,  $V_{CC} = 2.0V$  for 1.75V versions,  $V_{CC} = 1.8V$  for 1.5V versions.

Symbol	Parameter	Test Conditions	APR3101/2/3			Unit
			Min.	Typ.	Max.	
$V_{CC}$	Supply Voltage		1.2		6	V
$I_{CC}$	Supply Current	$V_{CC} = 1.5V \sim 6V$		1.5	4	$\mu A$
		$T_A = -40$ to $85^\circ C$		3	5	
$V_{TH}$	Reset Threshold	$T_A = 25^\circ C$		$\pm 1.5\%$		
		$T_A = -40$ to $85^\circ C$		$\pm 2\%$		
$V_{HYST}$	Hysteresis Range			20		mV
$T_{RTP}$	Reset Timeout Period		150	250	350	ms
$V_{OL}$	Reset Output Low	$I_{SINK} = 1.2mA$			0.5	V
$V_{OH}$	Reset Output High	$I_{SOURCE} = 0.6mA$	$0.8 * V_{CC}$			
$T_D$	$V_{CC}$ to Reset Delay	$V_{CC} = V_{TH}$ to $(V_{TH} - 100mV)$		25		$\mu S$
$I_{LE}$	Reset Output Leakage				0.5	$\mu A$
$T_{MPW}$	$\overline{MR}$ Minimum Pulse Width		10			$\mu S$
$T_{MRD}$	$\overline{MR}$ to Reset Delay			0.5		$\mu S$
$V_{MIL}$	$\overline{MR}$ Input Low Threshold				$0.4 * V_{CC}$	V
$V_{MIH}$	$\overline{MR}$ Input High Threshold		$0.5 * V_{CC}$			
$R_{MR}$	$\overline{MR}$ Pull-Up resistance		30	47	70	K $\Omega$

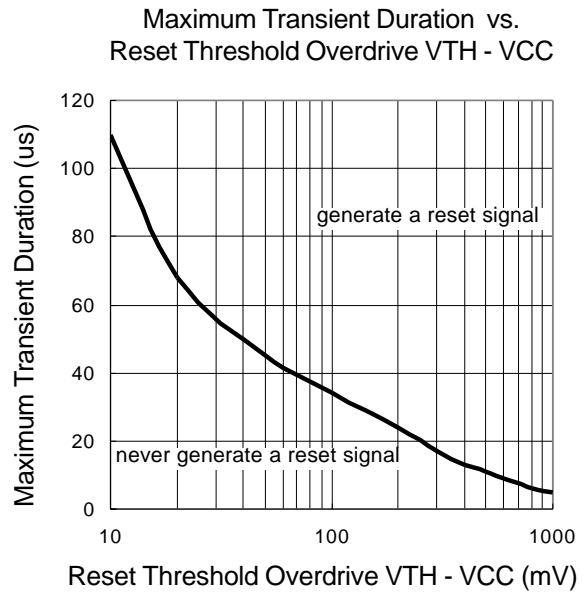
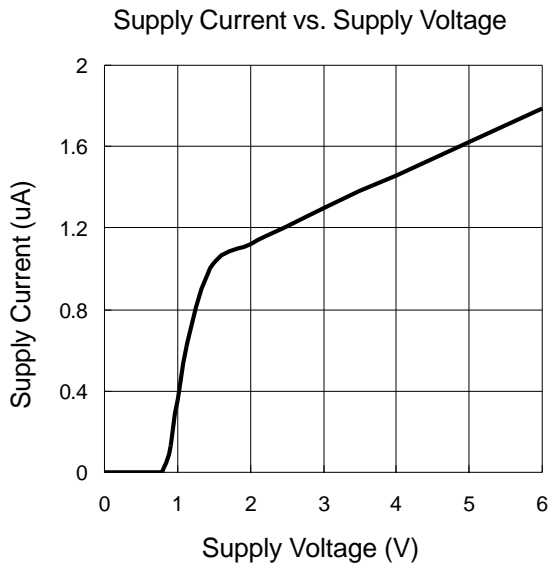
## Application Circuit



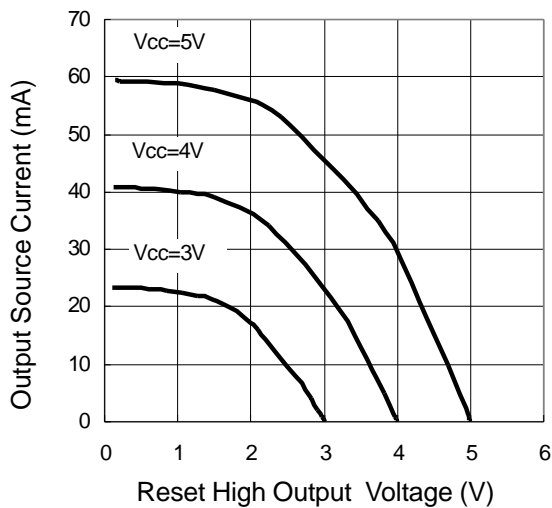
# Timing Chart



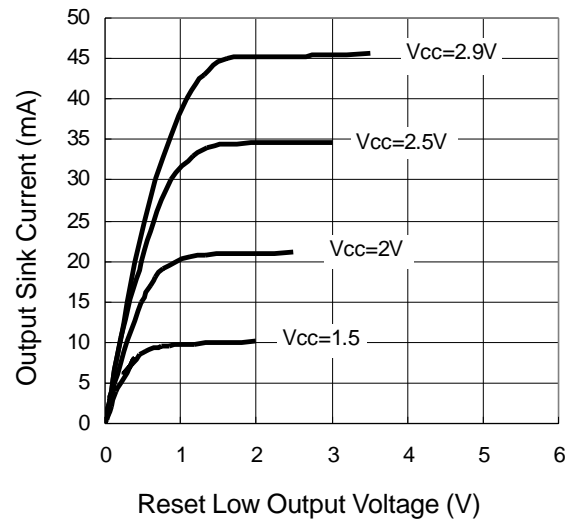
## Typical Characteristics



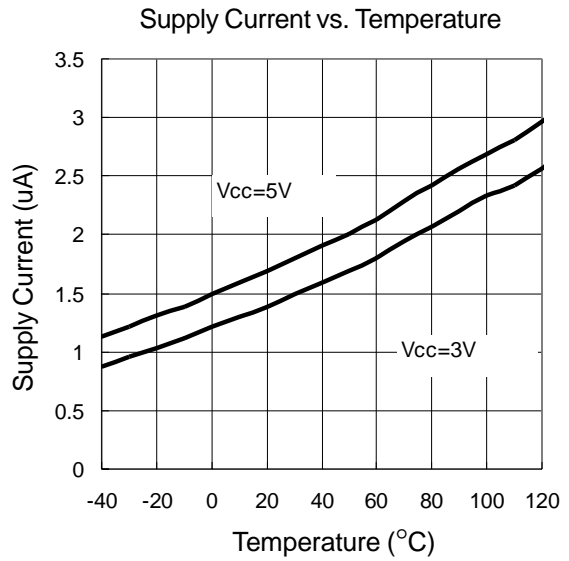
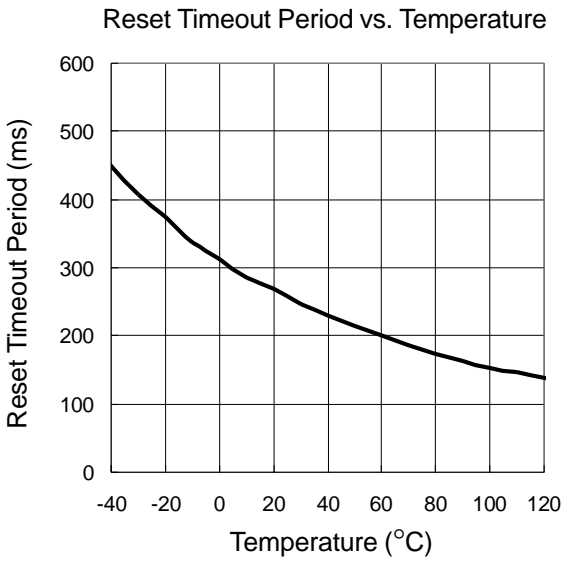
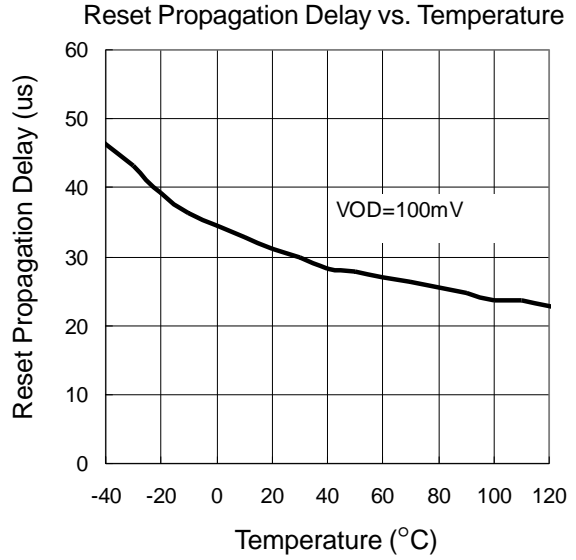
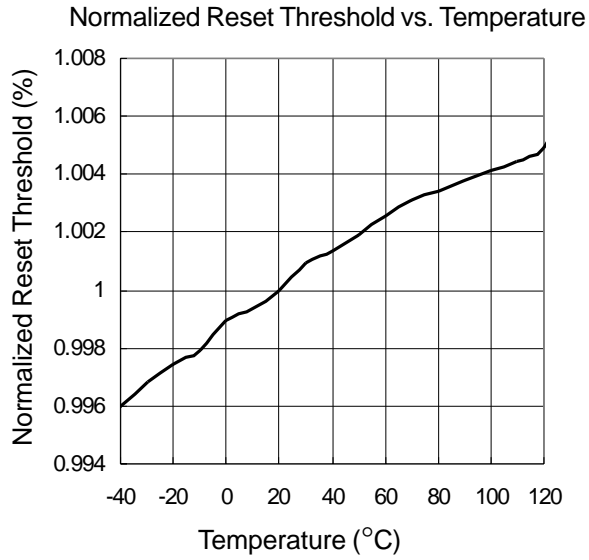
Output Source Current vs. Reset High Output Voltage



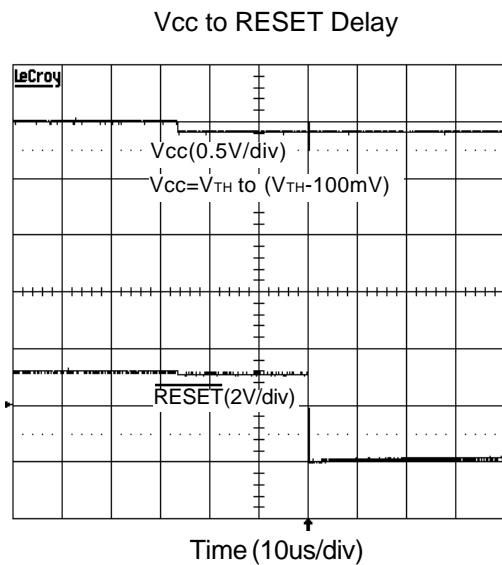
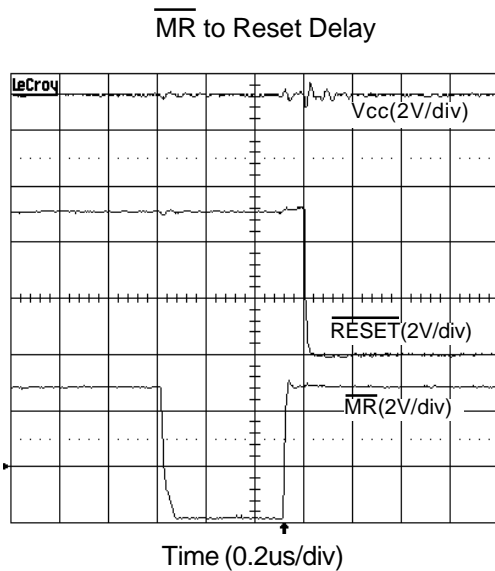
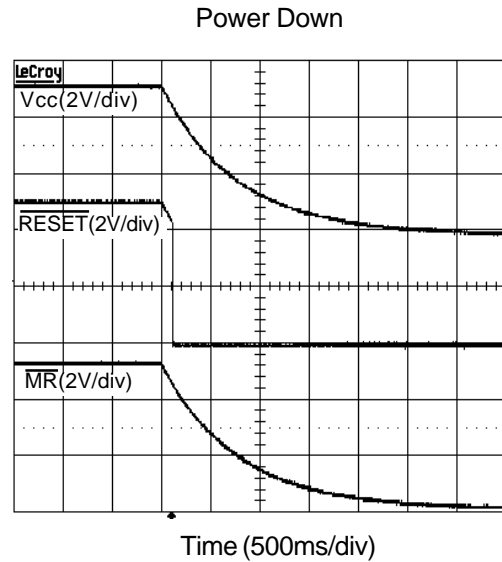
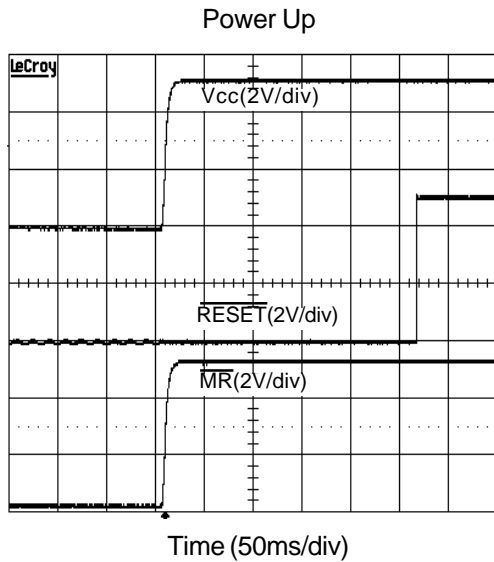
Output Sink Current vs. Reset Low Output Voltage



## Typical Characteristics

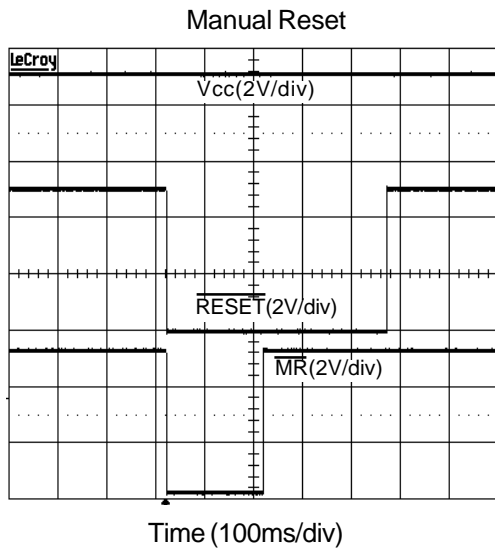


## Typical Characteristics





## Typical Characteristics



## Application Information

### V<sub>CC</sub> Transient Rejection

The APR3101/2/3 have the function to reject the transient glitches from the power line. The Maximum Transient Duration vs. Reset Threshold Overdrive shows at Typical Characteristics. The transient voltage with the duration under the curve will not generate a reset signal, e.g. a transient of 100mV below the reset threshold voltage have the duration more than 35us, it will generate a reset signal. Connect a 0.1uF bypass capacitor to the V<sub>CC</sub> pin can improve the transient immunity.

### Manual Reset Input

Force the  $\overline{MR}$  low asserts the reset signal, asserted reset continues as long as  $\overline{MR}$  is low and after the  $\overline{MR}$  goes high the reset signal is maintained for a fixed timeout period. The  $\overline{MR}$  is internally connected a 47k $\Omega$  resistor to V<sub>CC</sub>, so it can be floating if  $\overline{MR}$  is not used. The MR input also has a debounce time 500ns to avoid the glitches. It allows use of a mechanical switch or a TTL, and CMOS logic signal.

### Reset Output

The APR3101/2/3 have 3 output stage versions: APR3101 is an active low push-pull output, when the V<sub>CC</sub> drops below the reset threshold or  $\overline{MR}$  goes low, the RESET output generates a low signal. APR3102 is an active high push-pull output, when the V<sub>CC</sub> drops below the reset threshold or  $\overline{MR}$  goes low; the RESET output generates a high signal (see Timing Chart). APR3103 is an active low open drain output, the RESET output must be connected a pull-up resistor to a supply voltage that is lower than 6V, it suits to use in multiple voltage systems (see Figure 2). The APR3101 RESET output is valid until the V<sub>CC</sub>=1.2V, below 1.2V

the IC is shutdown, and the output becomes a floating state. If it is a trouble, a resistor should be connected from reset output to ground to keep the reset output low (see Figure 1). For The APR3102, a pull-up resistor to V<sub>CC</sub> is required to keep the valid reset output for V<sub>CC</sub> below 1.2V. ¶@

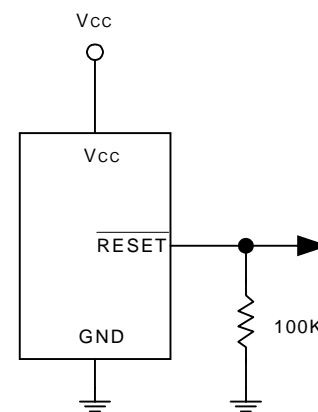


Figure 1. Ensuring  $\overline{RESET}$  Valid to V<sub>CC</sub> = 0 V

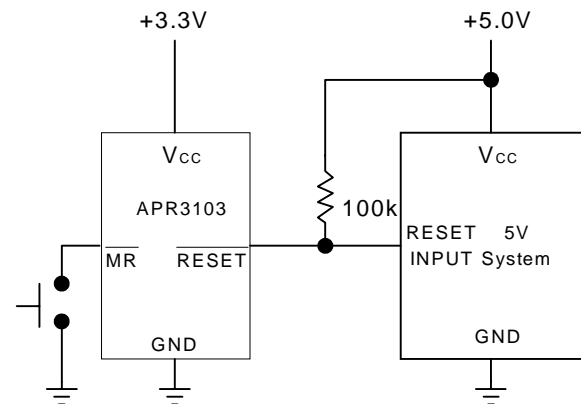
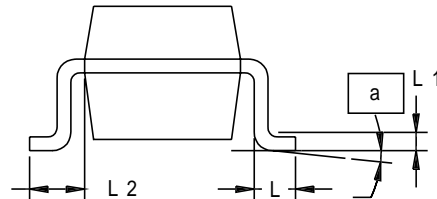
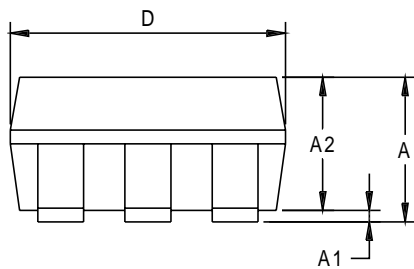
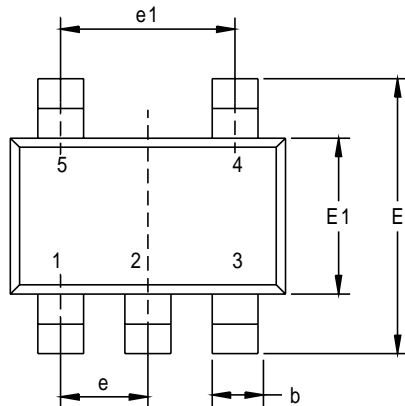


Figure 2. APR3103 Open Drain Output with Multiple Supplies

Packaging Information

SOT-23-5

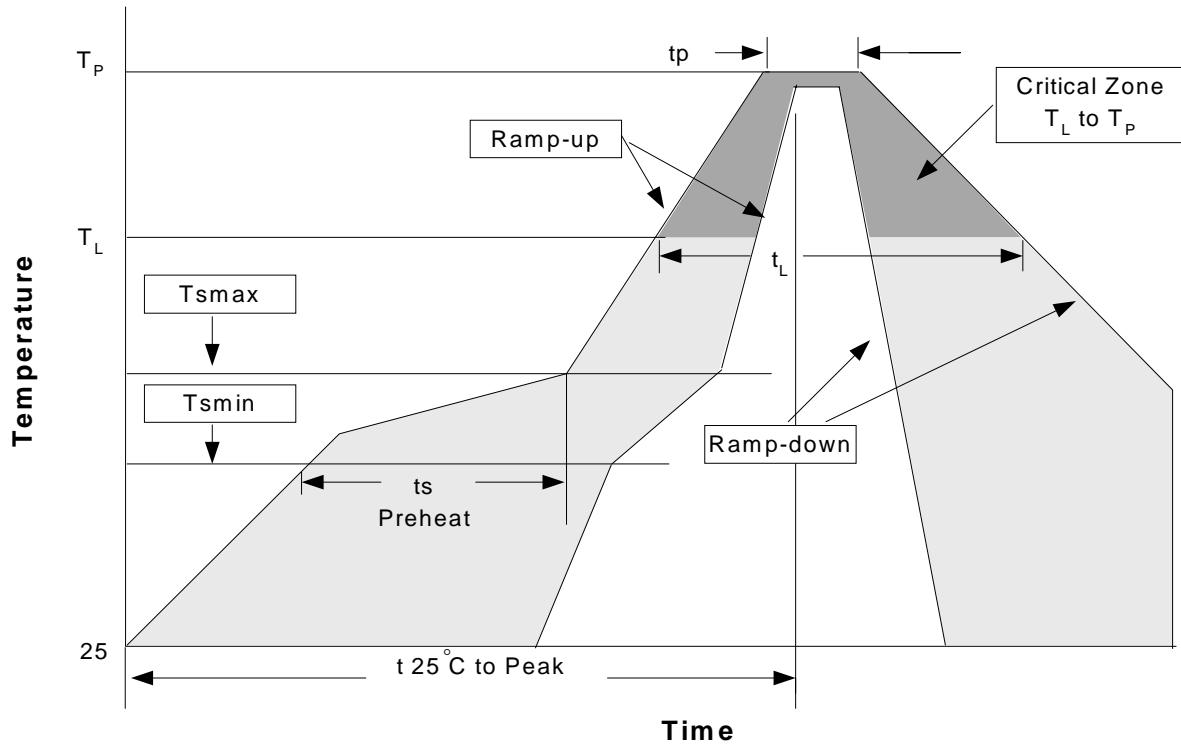


Dim	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	0.95	1.45	0.037	0.057
A1	0.05	0.15	0.002	0.006
A2	0.90	1.30	0.035	0.051
b	0.30	0.50	0.011	0.019
D	2.8	3.00	0.110	0.118
E	2.6	3.00	0.102	0.118
E1	1.5	1.70	0.059	0.067
e	0.95BSC		0.037BSC	
e1	1.90BSC		0.074BSC	
L	0.35	0.55	0.014	0.022
L1	0.20 BSC		0.008 BSC	
L2	0.5	0.7	0.020	0.028
N	5		5	
$\alpha$	0°	10°	0°	10°

## Physical Specifications

Terminal Material	Solder-Plated Copper (Solder Material : 90/10 or 63/37 SnPb), 100%Sn
Lead Solderability	Meets EIA Specification RSI86-91, ANSI/J-STD-002 Category 3.

### Reflow Condition (IR/Convection or VPR Reflow)



### Classification Reflow Profiles

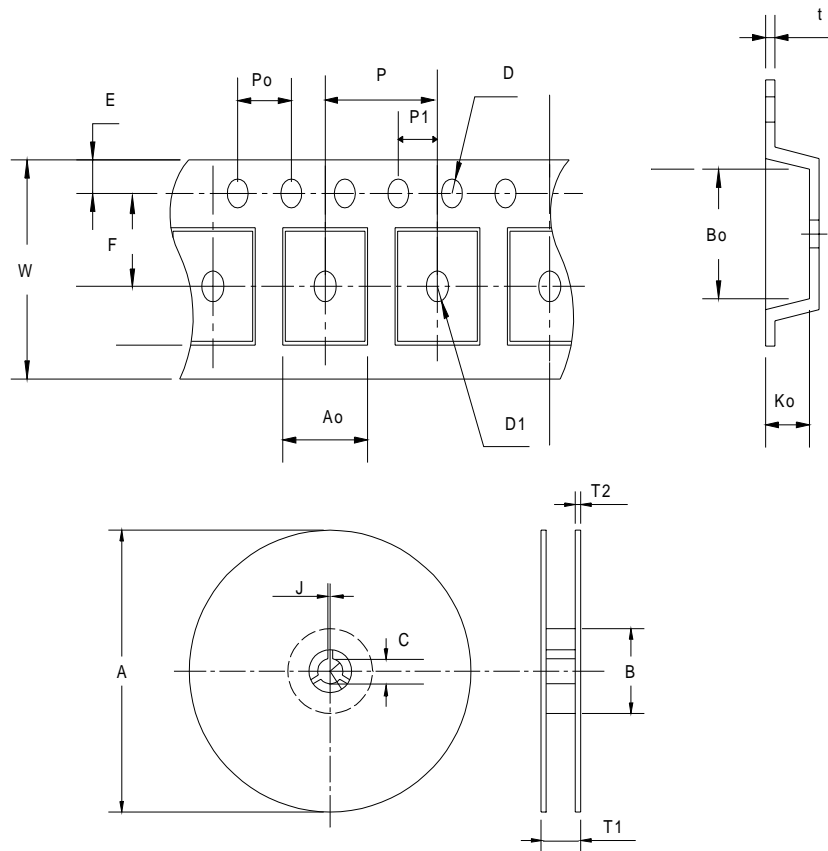
Profile Feature	Sn-Pb Eutectic Assembly		Pb-Free Assembly	
	Large Body	Small Body	Large Body	Small Body
Average ramp-up rate (T <sub>L</sub> to T <sub>P</sub> )	3°C/second max.		3°C/second max.	
Preheat	100°C		150°C	
- Temperature Min (T <sub>smin</sub> )	150°C		200°C	
- Temperature Mix (T <sub>smax</sub> )	60-120 seconds		60-180 seconds	
- Time (min to max)(t <sub>s</sub> )				
T <sub>smax</sub> to T <sub>L</sub>			3°C/second max	
- Temperature(T <sub>L</sub> )				
- Time (t <sub>L</sub> )				
Peak Temperature(T <sub>p</sub> )	183°C		217°C	
	60-150 seconds		60-150 seconds	
Time within 5°C of actual Peak Temperature(t <sub>p</sub> )	225 +0/-5°C	240 +0/-5°C	245 +0/-5°C	250 +0/-5°C
Ramp-down Rate	10-30 seconds	10-30 seconds	10-30 seconds	20-40 seconds
Time 25°C to Peak Temperature	6°C/second max.		6°C/second max.	
	6 minutes max.		8 minutes max.	

Note: All temperatures refer to topside of the package. Measured on the body surface.

### Reliability test program

Test item	Method	Description
SOLDERABILITY	MIL-STD-883D-2003	245°C , 5 SEC
HOLT	MIL-STD-883D-1005.7	1000 Hrs Bias @ 125 °C
PCT	JESD-22-B, A102	168 Hrs, 100 % RH , 121°C
TST	MIL-STD-883D-1011.9	-65°C ~ 150°C, 200 Cycles
ESD	MIL-STD-883D-3015.7	VHBM > 2KV, VMM > 200V
Latch-Up	JESD 78	10ms , I <sub>tr</sub> > 100mA

### Carrier Tape



Application	A	B	C	J	T1	T2	W	P	E
SOT-23-5	178±1	72 ± 1.0	13.0 + 0.2	2.5 ± 0.15	8.4 ± 2	1.5± 0.3	8.0 <sup>+ 0.3</sup> <sub>- 0.3</sub>	4 ± 0.1	1.75± 0.1
	F	D	D1	Po	P1	Ao	Bo	Ko	t
	3.5 ± 0.05	1.5 + 0.1	1.5 + 0.1	4.0 ± 0.1	2.0 ± 0.1	3.15 ± 0.1	3.2± 0.1	1.4± 0.1	0.2±0.03

(mm)

**Cover Tape Dimensions**

Application	Carrier Width	Cover Tape Width	Devices Per Reel
SOT- 23	8	5.3	3000

**Customer Service****Anpec Electronics Corp.**

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