

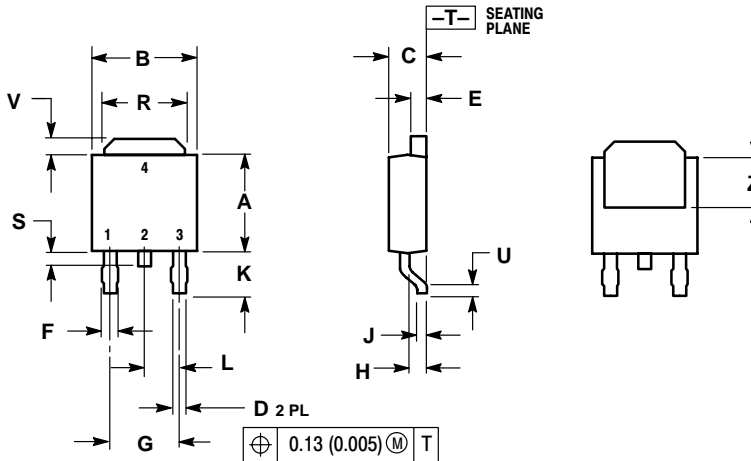


**DPAK**  
**CASE 369A-13**  
**ISSUE AB**

**DATE 07/27/2001**



SCALE 1:1

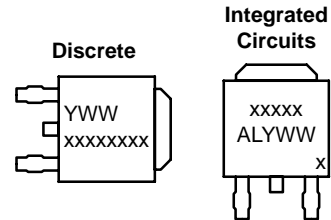


- NOTES:  
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.  
 2. CONTROLLING DIMENSION: INCH.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.235	0.250	5.97	6.35
B	0.250	0.265	6.35	6.73
C	0.086	0.094	2.19	2.38
D	0.027	0.035	0.69	0.88
E	0.033	0.040	0.84	1.01
F	0.037	0.047	0.94	1.19
G	0.180 BSC		4.58 BSC	
H	0.034	0.040	0.87	1.01
J	0.018	0.023	0.46	0.58
K	0.102	0.114	2.60	2.89
L	0.090 BSC		2.29 BSC	
R	0.175	0.215	4.45	5.46
S	0.020	0.050	0.51	1.27
U	0.020	---	0.51	---
V	0.030	0.050	0.77	1.27
Z	0.138	---	3.51	---

**MARKING**  
**DIAGRAMS**

- |   |  |   |   |
|---|--|---|---|
| <p><b>STYLE 1:</b><br/>                 PIN 1. BASE<br/>                 2. COLLECTOR<br/>                 3. EMITTER<br/>                 4. COLLECTOR</p> | <p><b>STYLE 2:</b><br/>                 PIN 1. GATE<br/>                 2. DRAIN<br/>                 3. SOURCE<br/>                 4. DRAIN</p> | <p><b>STYLE 3:</b><br/>                 PIN 1. ANODE<br/>                 2. CATHODE<br/>                 3. ANODE<br/>                 4. CATHODE</p>      | <p><b>STYLE 4:</b><br/>                 PIN 1. CATHODE<br/>                 2. ANODE<br/>                 3. GATE<br/>                 4. ANODE</p> |
| <p><b>STYLE 5:</b><br/>                 PIN 1. GATE<br/>                 2. ANODE<br/>                 3. CATHODE<br/>                 4. ANODE</p>         | <p><b>STYLE 6:</b><br/>                 PIN 1. MT1<br/>                 2. MT2<br/>                 3. GATE<br/>                 4. MT2</p>        | <p><b>STYLE 7:</b><br/>                 PIN 1. GATE<br/>                 2. COLLECTOR<br/>                 3. EMITTER<br/>                 4. COLLECTOR</p> |   |



- XXXXXXXXXX = Device Code  
 A = Assembly Location  
 IL = Wafer Lot  
 Y = Year  
 WW = Work Week

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