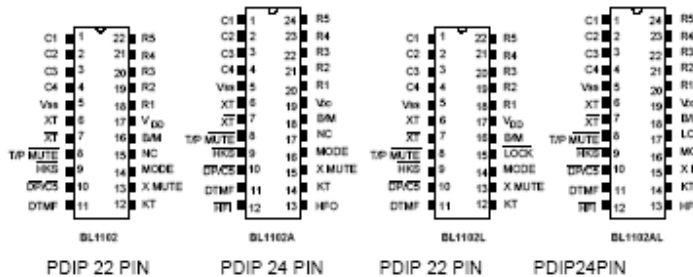


Description

BL1102 Series is a single chip dialer IC using Si-gate CMOS process, it can provide signal for either Pulse or double Tone with multi-frequency dialing. It features key tone and hands-free dialing, "0,"9" dialing latch with first number, 2array 32-digit number memory re-dialing, and 13-array 16-digit number memory etc.



Features

Operating voltage: 2.0 ~ 5.5V

Double Tone/Pulse selection with switch, pulse can be switched to double tone by pressing*/T key.

3.579545MHz crystal or ceramic resonator is used

Power on reset on chip

Minimum tone output duration: 100 ms

2 arrays of 32-digit number memory used for re-dialing and memory dialing respectively

3 arrays of 16-digit number single key dialing memory

10 arrays of 32-digit number double key dialing memory

Pulse break/make ratio can be 3/2 or 2/1, both alternative, and pulse speed is 10PPS

Flash Time: 98ms, 305ms, 6000ms

Minimum tone output width: 93ms, minimum tone interval: 93ms

Key tone output used for confirming the key pressed is right

Pulse, P→T (Pulse to Tone) can be stored as a digit in memory

Easy operation with one-key redial, memory, pause and etc.

Standard 5×5 keyboard used

Key "Mute" used for mute talking

The dial-out starting with number "0" or "9" can be locked and chosen by pin.

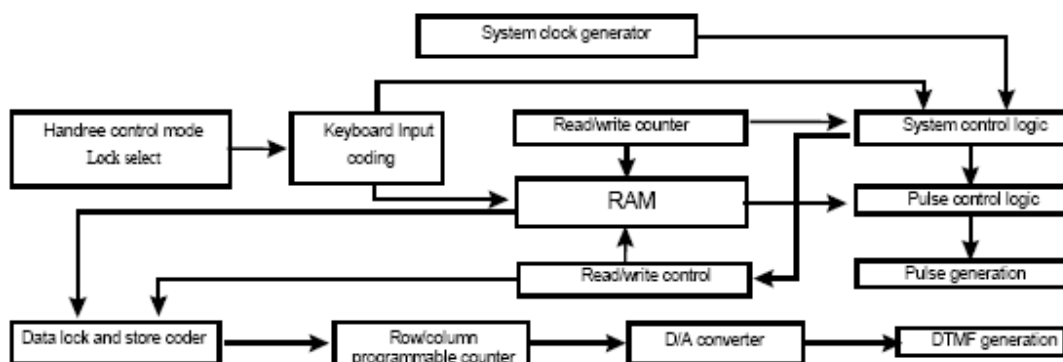
CD-operating key

20 and 24 PDIP plastic Package

Application

Used as dialing circuit in telephone, complete relevant functions.

Block Diagram



Known from the above block diagram, it mainly made up of system clock generator, keyboard interface, data code, random storing, read/write counter, address lock and storing, system control logic, pulse generator, data/mode converter and double tone generator and other circuits. .

Pin Description

Symbol	22 Pin	24 Pin	I/O	Function	
Row/column	1-4,18-	1-4,20-	I	A valid key entry is defined by a single row being	
n input	22	24		Connected to a single column	
X T,X T	6,7	6,7	I/O	Oscillation I/O	
T/P MUTE	8	8	O	The T/P MUTE is a conventional CMOS N-channel open drain output. When dialing and flash, the output is and will turn down the talking network.	
MODE	1 4	1 6	I	Dialing mode selection.. When pin is connected to VSS, it is double tone multi-frequency mode when it is connected to VDD; it is pulse mode (10ppS).	
HKS	9	9	I	The pin is the hook switch input HKS =1, on HOOK state, chip in sleeping mode, no operation. HKS =0, on HOOK state, enable chip on normal operation.	
B/M	1 6	1 8	I	Break/make ratio, pin selection. B/M=1, ratio is 60/40; B/M=0, ratio is 66/33.	
VDD, VSS	1 7, 5	19, 5	I	Power and power ground input	
X MUTE	1 3	1 5	O	Inverter output. Dialing under double tone mode, pin is high voltage; if dialing under pulse mode, the pin is low voltage.	
HFI, HFO	--	12, 13	I/O	Hands-free control details can be seen in table2.	
KT	12	14	O	Key tone signal output. The frequency is1.2KHz.	
LOCK	15 (BL1102L)	17 (BL1102AL)	I	Once the pin is locked, dialing starting with "0" or "9" and all the key input afterwards will become invalid. It will recover the original state after on hook	
				LOCK Pin	Function
				VDD	"0"和"9"dialing lim it
				Floating	Normal
				VSS	"0"dialing lim it
NC	15 (BL1102)	17 (BL1102A)	I	Not available	
DP / C5	10	10	O	The DP/C5 is a conventional CMOS N-channel open drain output. Under tone state. Output will keep high; Under pulse state, output dialing pulse.	
DTMF	11	11	O	Double tone multifrequency signal output	

Table1. Circuit Function

Type	Pulse (PPS)	Flash (ms)	Break/Make Ratio (B/M)	Hands-free	Lock Control	Assembly (PDIP)
BL1102	10	98 / 305 / 600	Pin Selection	N	N	22
BL1102A	10	98 / 305 / 600	Pin Selection	Y	N	24
BL1102L	10	98 / 305 / 600	Pin Selection	N	Y	22
BL1102AL	10	98 / 305 / 600	Pin Selection	Y	Y	24

Table 2

Current State		Next State		
Hook switch	HFO	input	Hands-free output (HFO)	dialing
—	Low	HFI ↓	High	Y
On hook (HKS=1)	High	HFI ↓	Low	—
Off hook (HKS=0)	High	HFI ↓	Low	Y
On hook (HKS=1)	—	Off hook	Low	Y
off hook (HKS=0)	Low	on hook	Low	—
off hook (HKS=0)	High	On hook	High	Y

Function Description

Keyboard Operation

	C1	C2	C3	C4	DP/C5	MUTE: mute on/off	A: 自动拨号键
R1	1	2	3	EM1	MUTE	P: pause	* /T: *&P→T key
R2	4	5	6	EM2	CD	CD: call disconnect	EM1-EM3: 16 digit emergency number store disconnect
R3	7	8	9	EM3	F1	RD: one-key redial	SAVE: 32 位备忘存储键
R4	*/T	0	#	SAVE	F2	S: double-key store	F1, F2, F3: flash time 98/305/600ms store
R5	RD	S	A	P	F3	#: Invalid under pulse state while refer to double tone multifrequency of the corresponding row and column	

- General Dialing

(or and HFI↓), D1 , D2 , ... Dn

a.. D1 , D2 , ... Dn will be dialed out.

b . Dialing length is unlimited, but the re-dial is inhibited if it oversteps 32 digits

- Re-dialing

a . on hook and re-dial : $\overline{\text{off-hook}}$, D_1 , D_2 , ... , D_n , busy, then $\overline{\text{on-hook}}$, $\overline{\text{off-hook}}$ (or $\overline{\text{on-hook}}$ and $\overline{\text{HF1}}\downarrow$) $\overline{\text{RD}}$

or ($\overline{\text{on-hook}}$ and $\overline{\text{HF1}}\downarrow$) , D_1 , D_2 , ... , D_n , busy, then, $\overline{\text{HF1}}\downarrow$, $\overline{\text{RD}}$ $\overline{\text{off-hook}}$ (or $\overline{\text{on-hook}}$ and $\overline{\text{HF1}}\downarrow$) , D_1 , D_2 , ... , D_n , busy, $\overline{\text{RD}}$

b . direct re-dial : $\overline{\text{off-hook}}$ or ($\overline{\text{on-hook}}$ and $\overline{\text{HF1}}\downarrow$) , D_1 , D_2 , ... , D_n , busy , $\overline{\text{on-hook}}$, $\overline{\text{RD}}$. If the dialing from D_1 to D_n is finished, press $\overline{\text{RD}}$, the pulse output pin will become low voltage for 2 . 2 minutes , and automatically insert 600ms for pause.. If press $\overline{\text{RD}}$ before finish dialing from D_1 to D_n , there will be no pulse output.

- “Pause” key operation

$\overline{\text{off-hook}}$ (or $\overline{\text{on-hook}}$ and $\overline{\text{HF1}}\downarrow$) , D_1 , D_2 , P , D_3 , ... , D_n

a . Pause function can be stored in memory storage.

b . Pause function can be performed when general dialing or re-dial or memory dialing.

- pulse \rightarrow tone mode convert (* / T)

$\overline{\text{off-hook}}$ (or $\overline{\text{on-hook}}$ and $\overline{\text{HF1}}\downarrow$) , D_1 , D_2 , ... , D_n , * / T , D_1' , D_2' , ... , D_n'

a . If mode switch is set as pulse, output signal will be :

D_1 , D_2 , ... , D_n , pause (3.1 minutes) , D_1' , D_2' , ... , D_n'
 (pulse) (tone)

b . If the mode switch is set as tone, output signal will be :

D_1 , D_2 , ... , D_n , * / T , D_1' , D_2' , ... , D_n'
 (tone) (tone) (tone)

c . If the tone mode remains since the number is dialed out, pulse mode can be recovered as long as do on-hook operation.

- FLASH

$\overline{\text{off-hook}}$ (or $\overline{\text{on-hook}}$ and $\overline{\text{HF1}}\downarrow$) , F

a . “flash” key can not be stored in the memory , but it enjoys the most high priority in all keyboard functions.

b . after pressing “ flash” key, dialer will recover to the original status.

- Number Storage

$\overline{\text{off-hook}}$ (or $\overline{\text{on-hook}}$ and $\overline{\text{HF1}}\downarrow$) , D_1 , D_2 , ... , D_n , S , S M_n or L_n , $\overline{\text{on-hook}}$, (or $\overline{\text{on-hook}}$ and $\overline{\text{HF1}}\downarrow$) .

a . Dialing out D_1 , D_2 , ... D_n first, then press the storage key S

b . D_1 , D_2 , ... , D_n numbers are stored in the position “ M_n or L_n ” and dialed out.

c . $M_n = M_1 \sim M_3$; $L_n = 0 \sim 9$; * , # , pause key (P)

$\overline{\text{off-hook}}$ (or $\overline{\text{on-hook}}$ and $\overline{\text{HF1}}\downarrow$) , S , D_1 , D_2 , ... , D_n , S , M_n or L_n , $\overline{\text{on-hook}}$, (or $\overline{\text{on-hook}}$ and $\overline{\text{HF1}}\downarrow$) .

a . D_1 , D_2 , ... , D_n are stored in M_n or L_n but will not be dialed out.

b . P and * / T key can be stored in the memory as one digit , P key indicates “pause”.

c. Once the storage function is finished or the state of hook switch changes, the storage mode will be released.

Memory dialing

a. One-key dial:

(or and),

b. Two-key dial:

(or and), ,

● “Save” Key

(or and), , , ... , ,

a. If dialing from to is over, press , the numbers from to will be stored in the memory.

b. then , , press , then to will be dialed out.

● CD

Press , one pulse output will make call disconnected , and make the system recover to the original status.

● MUTE

Press “MUTE”, mute output will be generated.

● Mixed dialing

(or and)

a. + +

b. + +

c. + +

Dialing with “Save” key is valid only being first pressed after off hook, and the second serial numbers can be dialed after the number is sent out.

Absolute Maximum Rating

Parameter	Symbol	Rating	Unit
DC Supply Voltage	$V_{DD} \sim V_{SS}$	-0.3 ~ +7.0	V
Input / output Voltage	V_{IL}	$V_{SS}-0.3$	V
	V_{IH}	$V_{DD}+0.3$	V
	V_{DL}	$V_{SS}-0.3$	V
	V_{DH}	$V_{DD}-0.3$	V
Power-Dissipation	P_D	120	mW
Operating Temperature	T_{OPR}	-20 ~+70	°C
Storage Temperature	T_{STG}	-55 ~+150	°C

DC Characteristic

 ($V_{DD}-V_{SS}=2.5V$, $F_{osc}=3.58MHz$, $T_a=25^{\circ}C$, all output unloaded except for being indicated.)

Parameter	Symbol	Test Condition	Min.	Typical	Max.	Unit
Operating voltage	V_{DD}	tone	2.0		5.5	V
		pulse	2.0		5.5	
		memory	1.0		5.5	
Operating current	I_{OPT}	tone		0.6	2.0	mA
	I_{OPP}	pulse		0.2	0.5	
Memory retention current	I_{MR}	HKS=1, $T_a=450^{\circ}C$ $V_{DD}=1.0V$		0.1	0.2	μA
Standby current	I_{SB}	HKS=0, N loaded and N		0.1	5	μA
		input				
DTMF output voltage	V_{to}	Row group, $R_L=5K\Omega$	130	150	170	mVrms
Pre-emphasis		Column/row group $V_{DD}=2.0\sim 5.5V$	1	2	3	dB
DTMF distortion	T_{Hd}	$R_L=5K\Omega$		-30	-23	dB
tone output outer loading resistance	R_L	$T_{MD}(-23dB)$	5			$K\Omega$
tone output DC voltage	V_{DC}	$V_{DD}=2.5\sim 5.5V$	1.1		2.8	V
tone output sink current	I_{TC}	$V_{TO}=0.5V$	0.2			mA
pulse output drive current	I_{PC}	$V_{PO}=0.5V$	0.5			mA
T/P mute output drive	I_{MH}	$V_{MO}=2.0V$	-0.5			mA
/sink current	I_{ML}	$V_{MO}=0.5V$	0.5			
mute output drive / sink current	I_{KH}	$V_{KO}=2.5V$	-0.5			mA
	I_{KL}	$V_{KO}=0.5v$	0.5			
XMT T/P mute drive/sink	I_{LH}	$V_{LO}=2.0V$	-0.5			mA
current	I_{LL}	$V_{LO}=0.5V$	0.5			
voltage input low level	V_{IL}		GND		$0.3V_{DD}$	V
voltage input high level	V_{IH}		$0.7V_{DD}$		V_{DD}	
key input drive/sink current	I_{KD}	$V_I=0V$	10	30	80	μA
	I_{KS}	$V_I=0V$	200	400		
Pin control input	I_{IS}	HKS, MODE, M/B			± 2.5	μA

AC characteristic

Parameter	Symbol	Test Condition	Min.	Typical	Max.	Unit
Time of keyin debounce	T _{KID}		--	20	--	ms
Time of key release debounce	T _{KRD}		--	20	--	ms
Time of key tone defer	T _{KD}		--	20	--	ms
Time of One-key re-dial "pause"	T _{RB}		--	2.2	--	s
Time of pulse T/P mute defer	T _{MD}	B/M=1	--	40	--	ms
		B/M=0	--	33.3	--	
Pre-digit pause	T _{PDP}	B/M=1	--	40	--	ms
		B/M=0	--	33.3	--	
pulse speed	F _{PR}		--	10	--	pps
Time in data pace (auto-dial)	T _{TD}		--	800	--	ms
Break/make ratio	B/M	B/M=1	--	60:40	--	%
		B/M=0	--	66.6:33.3	--	
Tone width	T _{TD}	Automatic dialing	--	93	--	ms
Inter tone pace	T _{TD}	Automatic dialing	--	93	--	ms
Time of flash pause	T _{FB}		--	98	--	ms
			--	305	--	
			--	600	--	
Time of Pause	T _P		--	3.1	--	s
Row –group frequency	F1	row1		699		Hz
	F2	row2		766		
	F3	row3		848		
	F4	row4		948		
Column-group frequency	F5	column1		1216		Hz
	F6	column2		1332		
	F7	column3		1472		
Key tone frequency	F _{kt}		--	1.2	--	kHz
Time of one-key re-dial pause	T _{PR}		--	600	--	ms

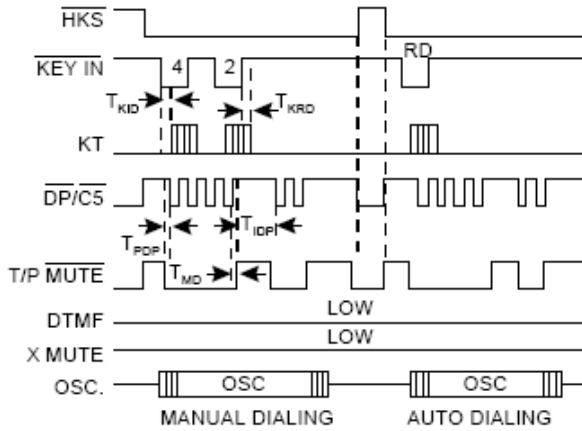
NB:

1 . If it is operated on the following normal situation, the oscillation parameters are recommended as :
 R_s < 100Ω , L_m: 96mH , C_m :0.02pF, C_n:5pF, C₁:18pF
 OSC : 3.57945 Mhz ±0.02 %

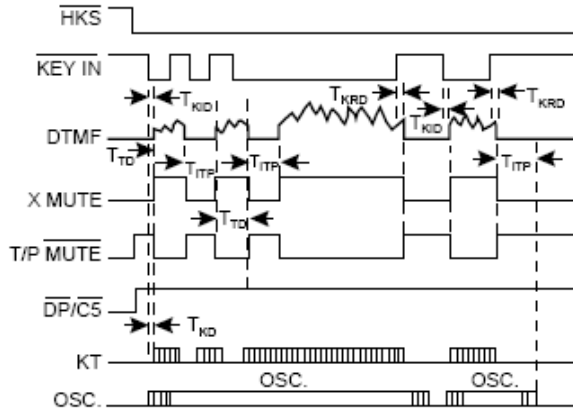
2 . the accuracy of oscillator frequency will effect the above –mentioned times.

Timing Diagram

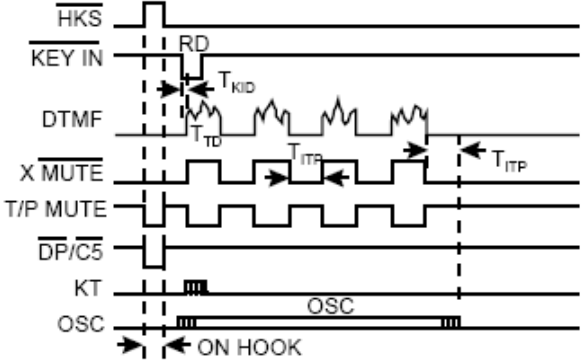
Pulse mode normal dialing timing diagram



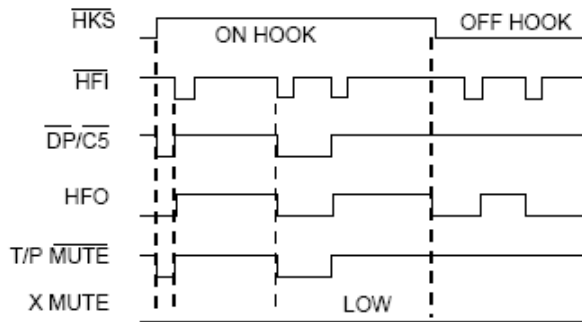
Tone mode normal timing diagram



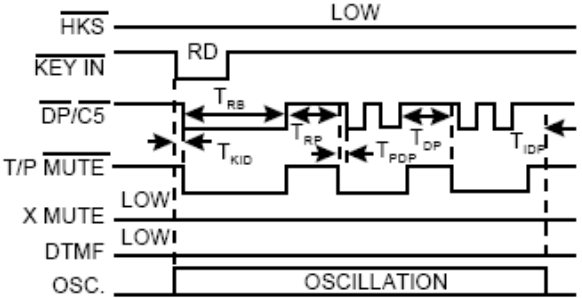
Tone mode redial timing diagram



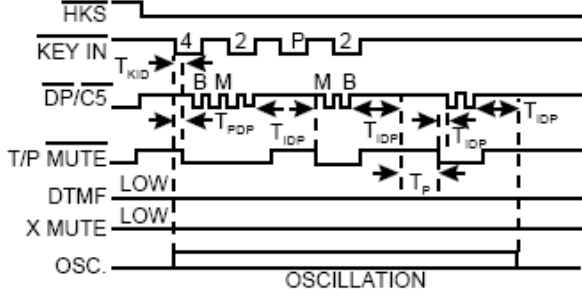
hands-free control function timing



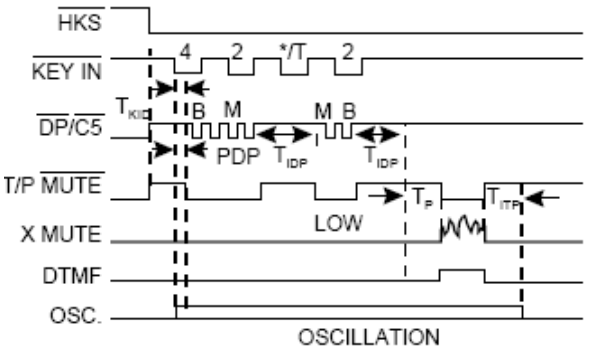
Pulse mode timing diagram



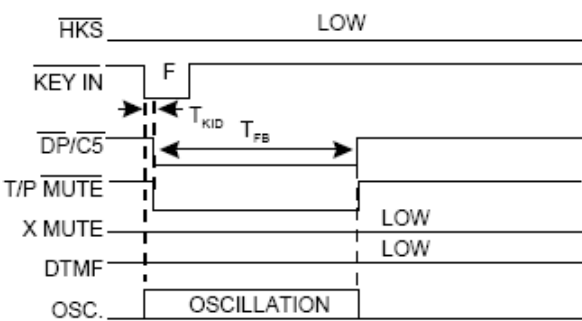
Pause function timing diagram



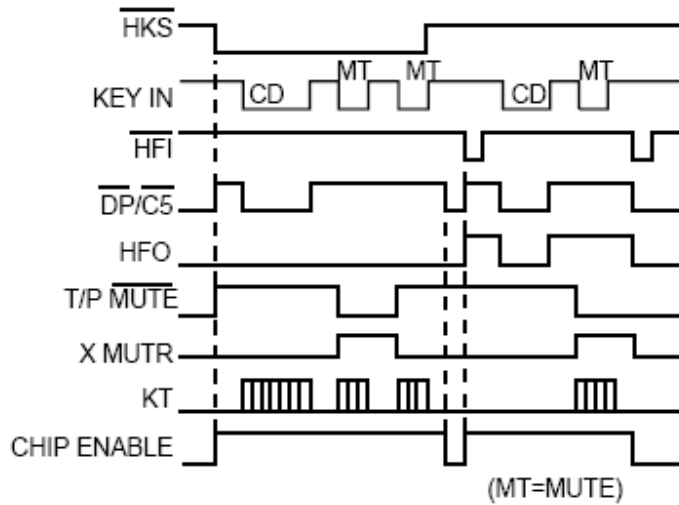
General dial: pulse→ tone (P→T) transfer timing diagram



Flash function operation timing diagram



"Mute" key function operation timing diagram



Application Circuit

