

**HMC**

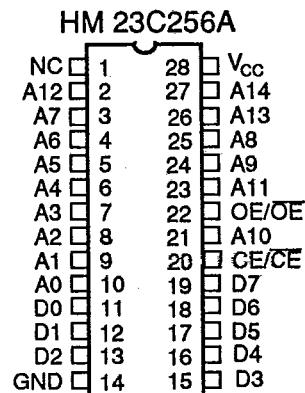
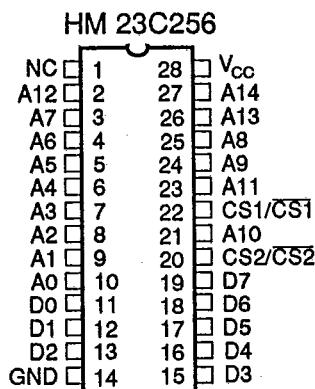
# **HM23C256/A**

## **32K × 8 MASK ROM**

### **Features**

- EPROM pin compatible.
- 32,768 × 8 bit organization.
- Single +5 Volt Supply.
- Access Time – 200/300ns (Max.)
- Totally static operation.
- Completely TTL compatible.
- Low power dissipation.
- Three-state outputs.
- 23C256
  - Non power down version.
  - Two programmable chip selects ( $\overline{CS}$ ).
- 23C256A
  - Automatic power down ( $\overline{CE}$ ).
  - Output enable function ( $\overline{OE}$ ).

### **Pin Assignment**



### **Bonding Pad Diagram**

