## PWM driver for CD

## BH6573FV

BH6573FV is a 4-channel PWM driver developed for driving stepping motor, and DC motor of DSC, and also developed for driving DC motor, and actuator of CD / MD.
This IC has achieved lower power consumption of the set by using power MOSFET in output.

## - Applications

CD, MD, DSC, DVC

## -Features

1) Four channels of power MOS-H bridges are contained.
2) Available for PWM input.
3) Applicable for stepping-motor drive.
4) Separating VM into $\mathrm{CH} 1, \mathrm{CH} 2$ and $\mathrm{CH} 3 / 4$.
5) Low on-resistance $1.3 \Omega$ (typ.)
6) Low power consumption.
7) SSOP-B24 package.

- Absolute maximum ratings ( $\mathrm{Ta}=25^{\circ} \mathrm{C}$ )

| Parameter | Symbol | Limits | Unit |
| :--- | :---: | :---: | :---: |
| H-bridge power supply voltage | $\mathrm{V}_{\mathrm{M}}$ | 6 | V |
| Control circuit power supply voltage | VDD | 6 | V |
| Driver output current | lo | $1000{ }^{* 1}$ | mA |
| Power dissipation | Pd | $1025^{* 2}$ | mW |
| Operating temperature range | Topr | $-40 \sim+85$ | ${ }^{\circ} \mathrm{C}$ |
| Storage temperature range | Tstg | $-5 \sim_{\sim}+150$ | ${ }^{\circ} \mathrm{C}$ |

*1 The current is guaranteed 1.0 A is case of the current is turned on/off in a duty-ratio of less than $1 / 10$ with a maximum on-time of 5 msec .
*2 When a $70 \mathrm{~mm} \times 70 \mathrm{~mm}, 1.6 \mathrm{~mm}$ thick glass epoxy substrate having a copper foil content of less than $3 \%$ is mounted
When the circuit if used at Ta of $25^{\circ} \mathrm{C}$ or more, subtract 8.2 mW per degree from the rating.

## - Recommended operating conditions

| Parameter | Symbol | Min. | Typ. | Max. | Unit |
| :--- | :---: | :---: | :---: | :---: | :---: |
| H-bridge power supply voltage | $\mathrm{V}_{\mathrm{M}}$ | 1.6 | 5.0 | 5.5 | V |
| Control circuit power supply voltage | $\mathrm{V}_{\mathrm{DD}}$ | 2.0 | 3.0 | 5.5 | V |


$\bullet$ Pin descriptions

| Pin No. | Pin name | Functions |
| :---: | :---: | :--- |
| 1 | GND | GND |
| 2 | IN1R | CH1 Reverse input |
| 3 | IN1F | CH1 Forward input |
| 4 | OUT1F | CH1 Forward output |
| 5 | VM1 | Power supply for CH1 power block |
| 6 | OUT1R | CH1 Reverse output |
| 7 | OUT2R | CH2 Reverse output |
| 8 | VM2 | Power supply for CH2 power block |
| 9 | OUT2F | CH2 Forward output |
| 10 | IN2F | CH2 Forward input |
| 11 | IN2R | CH2 Reverse input |
| 12 | VDD | Power supply for pre block |


| Pin No. | Pin name | Functions |
| :---: | :---: | :--- |
| 13 | GND | GND |
| 14 | IN4R | CH4 Reverse input |
| 15 | IN4F | CH4 Forward input |
| 16 | OUT4F | CH4 Forward output |
| 17 | OUT4R | CH4 Reverse output |
| 18 | VM3 | Power supply for CH3, 4 power block |
| 19 | OUT3R | CH3 Reverse output |
| 20 | OUT3F | CH3 Forward output |
| 21 | IN3F | CH3 Forward input |
| 22 | IN3R | CH3 Reverse input |
| 23 | PSB2 | CH3, 4 power save control pin |
| 24 | PSB1 | CH1, 2 power save control pin |



## Optical disc ICs

- Electrical characteristics (unless otherwise noted, $\mathrm{Ta}=25^{\circ} \mathrm{C}, \mathrm{Vm}=5 \mathrm{~V}, \mathrm{VdD}=3 \mathrm{~V}$, $\mathrm{fi}=176 \mathrm{kHz}, \mathrm{RL}=8 \Omega-47 \mathrm{uH}$ )

| Parameter | Symbol | Min. | Typ. | Max. | Unit | Conditions | $\underset{\text { circuit }}{\text { Measuring }}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| <H-bridge power supply voltage> |  |  |  |  |  |  |  |
| Upon no signal | IMST | - | 200 | 350 | $\mu \mathrm{A}$ |  | Fig. 1 |
| <Control circuit power supply voltage> |  |  |  |  |  |  |  |
| Upon no signal | IDD1 | - | 0 | 1 | $\mu \mathrm{A}$ |  | Fig. 1 |
| Upon operation | IDD2 | - | 6 | 70 | $\mu \mathrm{A}$ | Drivimg 4channels | Fig. 1 |
| <Logic input character> |  |  |  |  |  |  |  |
| "H" level input voltage | VIH | VDD -0.4 | - | - | V |  | Fig. 1 |
| "L" level input voltage | VIL | - | - | 0.3 | V |  | Fig. 1 |
| "H" level input current | IIH | - | - | 1 | $\mu \mathrm{A}$ |  | Fig. 1 |
| "L" level input current | IIL | -1 | - | - | $\mu \mathrm{A}$ |  | Fig. 1 |
| Output in-resistance 1 | Ron | - | 1.3 | 2.0 | $\Omega$ |  | Fig. 1 |
| Output in-resistance 2 | Ron | - | 1.8 | 2.6 | $\Omega$ | Sum of on-resistance of top and that of bottom (VM=5V, VDD=3V) | Fig. 1 |
| Propagation delay time | tRISE | - | 0.2 | 1 | usec | Sum of on-resistance of top and that of bottom ( $\mathrm{VM}=2.5 \mathrm{~V}, \mathrm{~V} D \mathrm{D}=3 \mathrm{~V}$ ) | Fig. 1 |
|  | tFALL | - | 0.2 | 1 | usec |  | Fig. 1 |
| Minimum input pulse width | tMIN | 220 | - | - | nsec | Output pulse more than $1 / 2 \mathrm{tMIN}$ | Fig. 1 |

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- Measuring circuit


Fig. 1

## - Circuit operation

© Truth table

| PSB1* | PSB2 $*$ | IN1~4F | IN1~4R | OUT1~4F | OUT1~4R |
| :---: | :---: | :---: | :---: | :---: | :---: |
| $H$ | $H$ | L | L | Hi-Z | Hi-Z |
| $H$ | $H$ | L | $H$ | L | $H$ |
| $H$ | $H$ | $H$ | $L$ | $H$ | L |
| $H$ | $H$ | $H$ | $H$ | L | L |
| L | L | X | X | $H i-Z$ | $H i-Z ~$ |

* PSB mode

| PSB1 | PSB2 | OUT1, 2 | OUT3, 4 |
| :---: | :---: | :---: | :---: |
| H | H | Output is followed by truth table | Output is followed by truth table |
| H | L | Output is followed by truth table | Hi-Z |
| L | H | Hi-Z | Output is followed by truth table |
| L | L | Hi-Z | Hi-Z |

Optical disc ICs
-Application example


Fig. 2

Optical disc ICs


Fig. 3

## -Operation notes

1. Connect a bypass capacitor $(0.1 \mu \mathrm{~F})$ across the supply voltage lines close to the IC pins.
2. Avoid short circuit between each driver output ( $4,6,7,9,16,17,19,20$ pin) and power supply ( $5,8,12$, 18pin) , or GND (1, 13pin). And avoid short circuit between output terminals (4-6, 7-9, 16-17, 19-20).

- External dimensions (Units : mm)



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