



SamHop Microelectronics Corp.

STS 2611

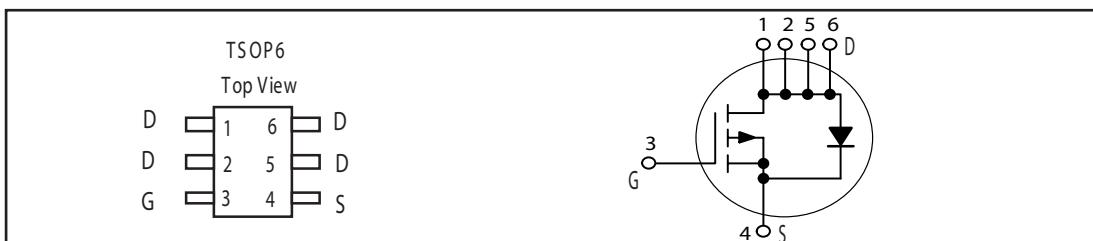
FEB 25 2005

## P-Channel Enhancement Mode Field Effect Transistor

PRODUCT SUMMARY		
V <sub>DSS</sub>	I <sub>D</sub>	R <sub>DS(ON)</sub> (mΩ) Max
-20V	-2.8A	90 @ V <sub>GS</sub> = -4.5V
		150 @ V <sub>GS</sub> = -2.5V

### FEATURES

- Super high dense cell design for low R<sub>DS(ON)</sub>.
- Rugged and reliable.
- SOT-26 package.



### ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	-20	V
Gate-Source Voltage	V <sub>GS</sub>	±12	V
Drain Current-Continuous <sup>a</sup> @ T <sub>J</sub> =25°C -Pulsed <sup>b</sup>	I <sub>D</sub>	-2.8	A
	I <sub>DM</sub>	-11	A
Drain-Source Diode Forward Current <sup>a</sup>	I <sub>S</sub>	-1.25	A
Maximum Power Dissipation <sup>a</sup>	P <sub>D</sub>	1.25	W
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to 150	°C

### THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Ambient <sup>a</sup>	R <sub>thJA</sub>	100	°C/W
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ELECTRICAL CHARACTERISTICS (TA = 25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ <sup>c</sup>	Max	Unit
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250μA	-20			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = -16V, V <sub>GS</sub> = 0V		1		uA
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>GS</sub> = ±12V, V <sub>DS</sub> = 0V		±100		nA
<b>ON CHARACTERISTICS<sup>b</sup></b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250μA	-0.5	-0.8	-1.5	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -2.5A		75	90	m-ohm
		V <sub>GS</sub> = -2.5V, I <sub>D</sub> = -1.0A		125	150	m-ohm
On-State Drain Current	I <sub>D(ON)</sub>	V <sub>DS</sub> = -5V, V <sub>GS</sub> = -4.5V	-7			A
Forward Transconductance	g <sub>Fs</sub>	V <sub>DS</sub> = -5V, I <sub>D</sub> = -2.5A		6		S
<b>DYNAMIC CHARACTERISTICS<sup>c</sup></b>						
Input Capacitance	C <sub>ISS</sub>	V <sub>DS</sub> = -20V, V <sub>GS</sub> = 0V f = 1.0MHz		380		pF
Output Capacitance	C <sub>OSS</sub>			100		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>			60		pF
<b>SWITCHING CHARACTERISTICS<sup>c</sup></b>						
Turn-On Delay Time	t <sub>D(ON)</sub>	V <sub>DD</sub> = -10V, I <sub>D</sub> = -1A, V <sub>GS</sub> = -4.5V, R <sub>GEN</sub> = 6 ohm		7.5		ns
Rise Time	t <sub>r</sub>			10.9		ns
Turn-Off Delay Time	t <sub>D(OFF)</sub>			27.3		ns
Fall Time	t <sub>f</sub>			22.5		ns
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = -10V, I <sub>D</sub> = -2.5A, V <sub>GS</sub> = -4.5V		3.6		nC
Gate-Source Charge	Q <sub>gs</sub>			0.9		nC
Gate-Drain Charge	Q <sub>gd</sub>			0.8		nC

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ELECTRICAL CHARACTERISTICS ( $T_A=25^\circ\text{C}$  unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ <sup>c</sup>	Max	Unit
DRAIN-SOURCE DIODE CHARACTERISTICS <sup>b</sup>						
Diode Forward Voltage	$V_{SD}$	$V_{GS} = 0V, I_S = -1.25A$		-0.81	-1.2	V

Notes

- a. Surface Mounted on FR4 Board,  $t \leq 10\text{sec}$ .
- b. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$ .
- c. Guaranteed by design, not subject to production testing.

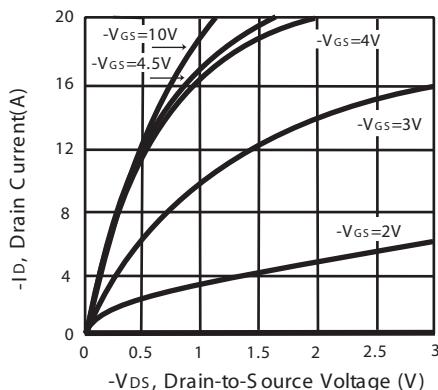


Figure 1. Output Characteristics

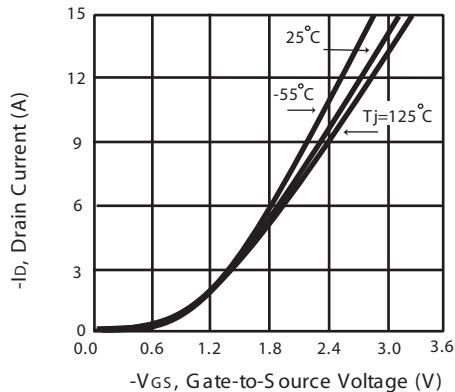


Figure 2. Transfer Characteristics

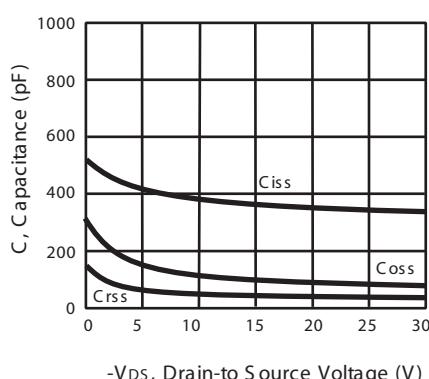


Figure 3. Capacitance

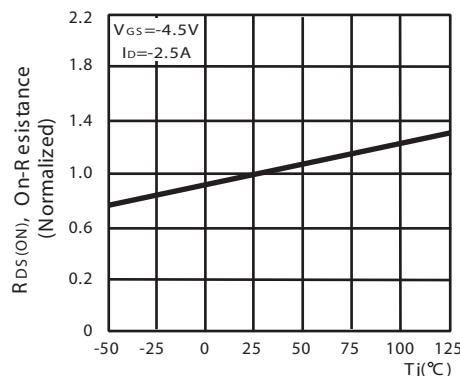


Figure 4. On-Resistance Variation with Temperature

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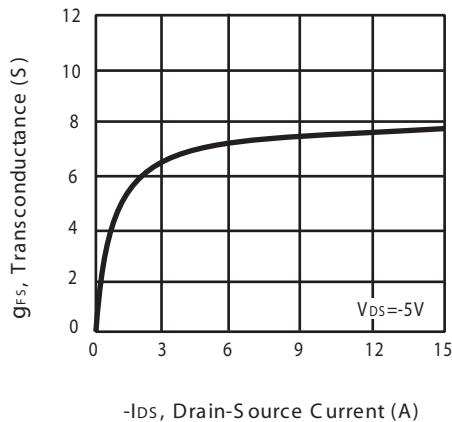
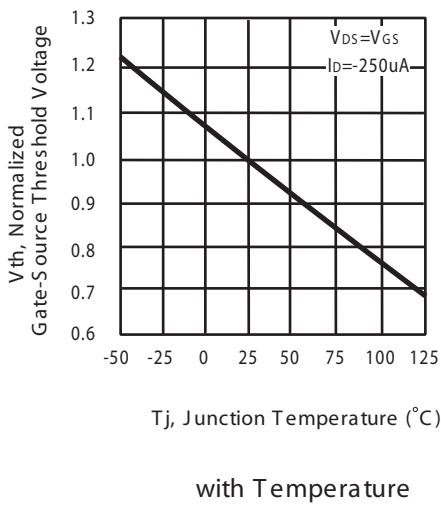


Figure 7. Transconductance Variation with Drain Current

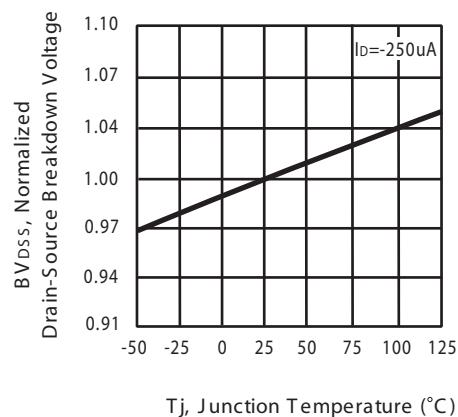


Figure 6. Breakdown Voltage Variation with Temperature

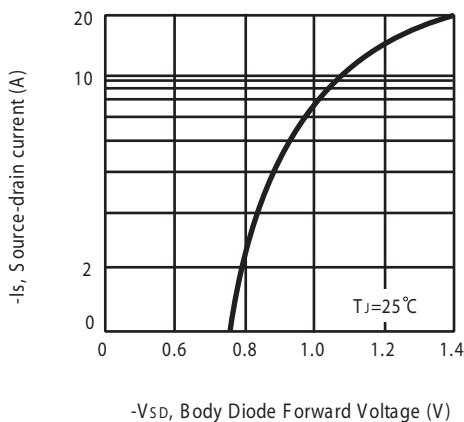


Figure 8. Body Diode Forward Voltage Variation with Source Current

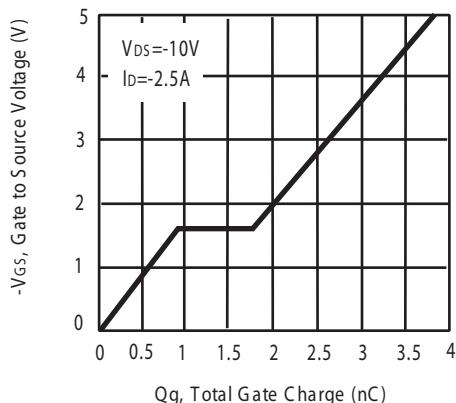


Figure 9. Gate Charge

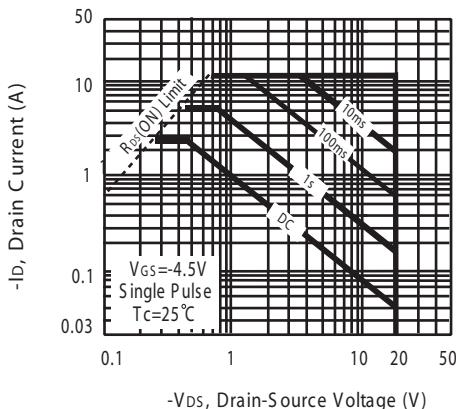


Figure 10. Maximum Safe Operating Area

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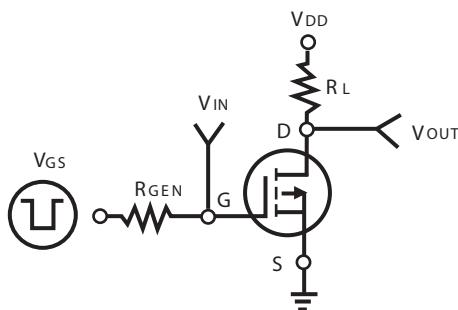


Figure 11. Switching Test Circuit

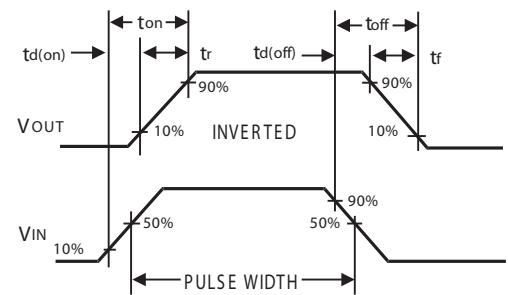
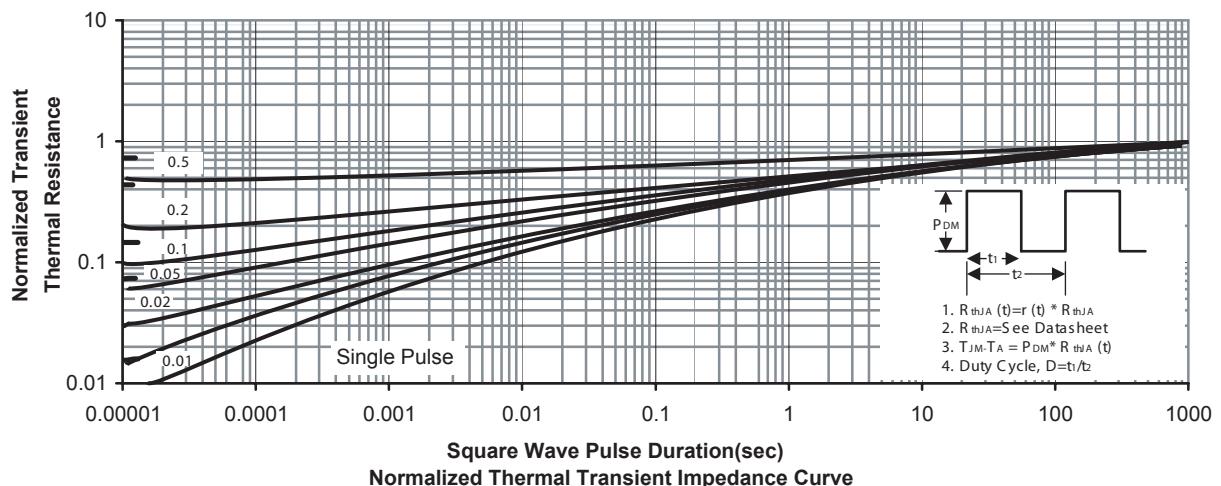


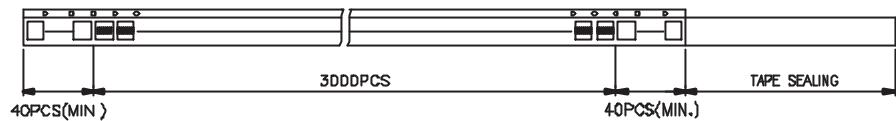
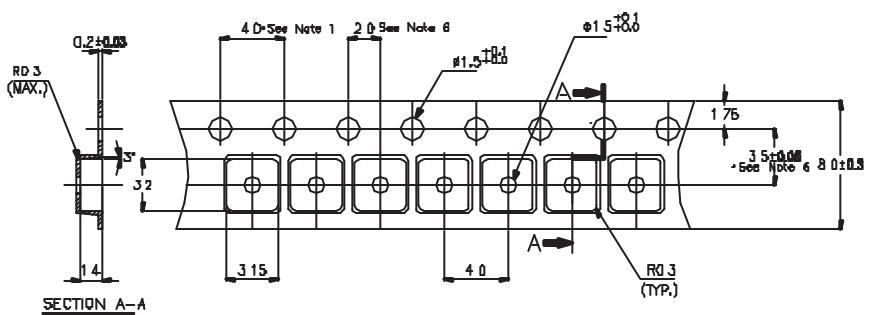
Figure 12. Switching Waveforms



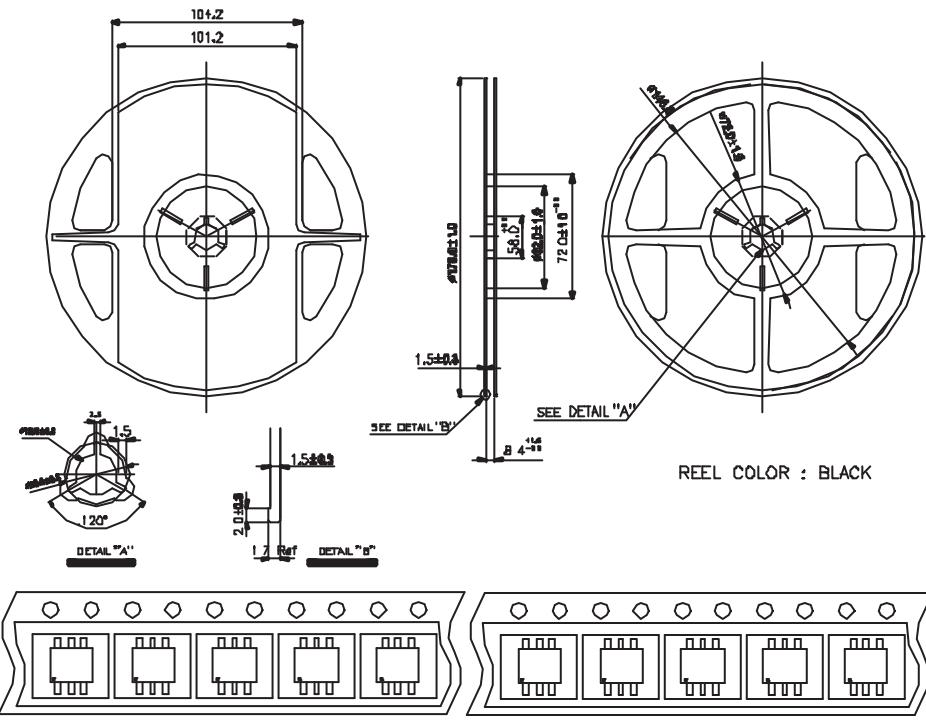
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# TSOP6 Tape and Reel Data

## TSOP6 Carrier Tape



TSOP6 Reel



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## PACKAGE OUTLINE DIMENSIONS

