

Low Saturation Voltage

PNP Silicon Driver Transistors

Part of the GreenLine™ Portfolio of devices with energy–conserving traits.

This PNP Silicon Epitaxial Planar Transistor is designed to conserve energy in general purpose driver applications. This device is housed in the SOT-23 and SC–59 packages which are designed for low power surface mount applications.

 \bullet Low V $_{\text{CE(sat)}}$, < 0.1 V at 50 mA

Applications

- LCD Backlight Driver
- Annunciator Driver
- General Output Device Driver

MAXIMUM RATINGS (T _A = 25°C)

Rating	Symbol	Value	Unit
Collector-Base Voltage	V _{(BR)CBO}	45	Vdc
Collector-Emitter Voltage	V _{(BR)CEO}	15	Vdc
Emitter-Base Voltage	$V_{(BR)EBO}$	5.0	Vdc
Collector Current — Continuous	Ιc	100	mAdc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Power Dissipation	P _D (1)	250	mW
T _A =25 °C			
Derate above 25°C		1.8	mW/°C
Thermal Resistance, Junction to Ambient	R _{θJA}	556	°C/W
Junction Temperature	ΤJ	150	°C
Storage Temperature Range	T _{stg}	-55 —+ 150	°C

DEVICE MARKING

MMBT1010LT1 = GLP; MSD1010T1 = GLP

ELECTRICAL CHARACTERISTICS

Symbol	Condition	Min	Max	Unit
V _{(BR)CEO}	$I_{C} = 10 \text{ mA}, I_{B} = 0$	15	_	Vdc
V _{(BR)EBO}	$I_E = 10 \mu A, I_E = 0$	5.0	_	Vdc
I _{CBO}	$V_{CB} = 20 V, I_E = 0$	_	0.1	μΑ
I _{CEO}	$V_{CE} = 10 \text{ V}, I_B = 0$	_	100	μΑ
h _{FE1} (2)	$V_{CE} = 5 \text{ V,I}_{C} = 100 \text{ mA}$	300	600	_
V _{CE(sat)} (2)	$I_{C} = 10 \text{ mA}, I_{B} = 1.0 \text{ mA}$	_	0.1	Vdc
	$I_{C} = 50 \text{ mA}, I_{B} = 5.0 \text{ mA}$	_	0.1	
	$I_C = 100 \text{ mA}, I_B = 10 \text{ mA}$		0.19	
V _{BE(sat)} (2)	I _C = 100 mA, I _B = 10 mA	_	1.1	Vdc
	V (BR)CEO V (BR)EBO I CBO I CEO h FE1 (2) V CE(sat)(2)	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$

- (1) Device mounted on a FR-4 glass epoxy printed circuit board using the minimum recommended footprint.
- (2) Pulse Test: Pulse Width \leq 300 μ s, D.C \leq 2%.

MMBT1010LT1 MSD1010T1

PNP GENERAL PURPOSE DRIVER TRANSISTORS SURFACE MOUNT





