

July 1994

Features

- This Circuit is Processed in Accordance to MIL-STD-883 and is Fully Conformant Under the Provisions of Paragraph 1.2.1.
- Low Supply Current. 8.0mA (Max)
- Low Offset Voltage 2.0mV (Max)
- High Slew Rate. 340V/ μ s (Typ)
- Open Loop Gain. 20kV/V (Min)
- Wide Gain-Bandwidth ($A_V \geq 10$) 470MHz (Typ)
- Input Noise Voltage at 1kHz 11nV/ $\sqrt{\text{Hz}}$ (Typ)
- Lower Power Replacement for HA-2540/883, AD840

Applications

- Pulse and Video Amplifiers
- Wideband Amplifiers
- High Speed Sample-Hold Circuits
- Fast, Precise D/A Converters

Description

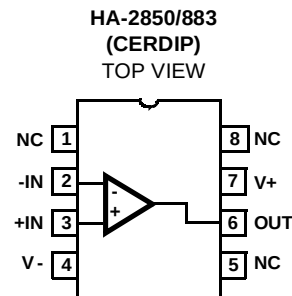
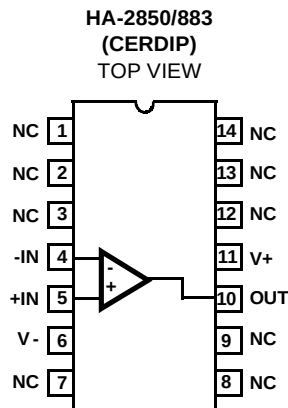
The HA-2850/883 is a wideband, high slew rate, operational amplifier featuring superior speed and bandwidth characteristics. It also features trimmed supply current, which minimizes supply current (and thus AC parameter) variation over process and temperature extremes. For example, the I_{CC} variation over the entire military temperature range is typically less than 0.5mA. Bipolar construction, coupled with dielectric isolation, delivers outstanding performance in circuits with closed loop gains ≥ 10 .

The 340V/ μ s slew rate, and 470MHz gain bandwidth product ensure high performance in video and wideband amplifier designs. Differential gain and phase are a low 0.04% and 0.04 degrees, respectively, making the HA-2850/883 ideal for video applications. A full ± 10 V output swing, high open loop gain, and outstanding AC parameters make the HA-2850/883 an excellent choice for data acquisition systems.

Ordering Information

PART NUMBER	TEMPERATURE RANGE	PACKAGE
HA1-2850/883	-55°C to +125°C	14 Lead CerDIP
HA7-2850/883	-55°C to +125°C	8 Lead CerDIP

Pinouts



Specifications HA-2850/883

Absolute Maximum Ratings

Voltage between V+ and V- Terminals. 35V
 Differential Input Voltage 6V
 Voltage at Either Input Terminal V+ to V-
 Peak Output Current ($\leq 10\%$ Duty Cycle) 50mA
 Junction Temperature (T_J) $+175^\circ\text{C}$
 Storage Temperature Range -65°C to $+150^\circ\text{C}$
 ESD Classification <2000V
 Lead Temperature (Soldering 10s). $+300^\circ\text{C}$

Thermal Information

Thermal Resistance θ_{JA} θ
 14 Lead CerDIP Package 81°C/W 26°C/W
 8 Lead CerDIP Package 115°C/W 30°C/W
 Package Power Dissipation Limit at $+75^\circ\text{C}$ for $T_J \leq +175^\circ\text{C}$
 14 Lead CerDIP Package 1.23W
 8 Lead CerDIP Package 0.87W
 Package Power Dissipation Derating Factor Above $+75^\circ\text{C}$
 14 Lead CerDIP Package $12.3\text{mW}/^\circ\text{C}$
 8 Lead CerDIP Package $8.7\text{mW}/^\circ\text{C}$

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

Operating Conditions

Operating Temperature Range. -55°C to $+125^\circ\text{C}$ $V_{INCM} \leq 1/2 (V+ - V-)$
 Operating Supply Voltage. $\pm 12\text{V}$ to $\pm 15\text{V}$ $R_L \geq 1\text{k}\Omega$

TABLE 1. DC ELECTRICAL PERFORMANCE CHARACTERISTICS

Device Tested at: $V_{SUPPLY} = \pm 15\text{V}$, $R_{SOURCE} = 100\Omega$, $R_{LOAD} = 100\text{k}\Omega$, $V_{OUT} = 0\text{V}$, Unless Otherwise Specified.

PARAMETERS	SYMBOL	CONDITIONS	GROUP A SUBGROUP	TEMPERATURE	LIMITS		UNITS
					MIN	MAX	
Input Offset Voltage	V_{IO}	$V_{CM} = 0\text{V}$	1	$+25^\circ\text{C}$	-2	2	mV
			2, 3	$+125^\circ\text{C}$, -55°C	-6	6	mV
Input Bias Current	$+I_B$	$V_{CM} = 0\text{V}$, $+R_S = 1.1\text{k}\Omega$ $-R_S = 100\Omega$	1	$+25^\circ\text{C}$	-14.5	14.5	μA
			2, 3	$+125^\circ\text{C}$, -55°C	-20	20	μA
	$-I_B$	$V_{CM} = 0\text{V}$, $+R_S = 100\Omega$ $-R_S = 1.1\text{k}\Omega$	1	$+25^\circ\text{C}$	-14.5	14.5	μA
			2, 3	$+125^\circ\text{C}$, -55°C	-20	20	μA
Input Offset Current	I_{IO}	$V_{CM} = 0\text{V}$, $+R_S = 1.1\text{k}\Omega$ $-R_S = 1.1\text{k}\Omega$	1	$+25^\circ\text{C}$	-4	4	μA
			2, 3	$+125^\circ\text{C}$, -55°C	-8	8	μA
Common Mode Range	+CMR	$V+ = 5\text{V}$, $V- = -25\text{V}$	1	$+25^\circ\text{C}$	10	-	V
			2, 3	$+125^\circ\text{C}$, -55°C	10	-	V
	-CMR	$V+ = 25\text{V}$, $V- = -5\text{V}$	1	$+25^\circ\text{C}$	-	-10	V
			2, 3	$+125^\circ\text{C}$, -55°C	-	-10	V
Large Signal Voltage Gain	$+A_{VOL}$	$V_{OUT} = 0\text{V}$ and $+10\text{V}$ $R_L = 1\text{k}\Omega$	4	$+25^\circ\text{C}$	20	-	kV/V
			5, 6	$+125^\circ\text{C}$, -55°C	10	-	kV/V
	$-A_{VOL}$	$V_{OUT} = 0\text{V}$ and -10V $R_L = 1\text{k}\Omega$	4	$+25^\circ\text{C}$	20	-	kV/V
			5, 6	$+125^\circ\text{C}$, -55°C	10	-	kV/V
Common Mode Rejection Ratio	+CMRR	$\Delta V_{CM} = 10\text{V}$, $V_{OUT} = -10\text{V}$ $V+ = 5\text{V}$, $V- = -25\text{V}$	1	$+25^\circ\text{C}$	75	-	dB
			2,3	$+125^\circ\text{C}$, -55°C	75	-	dB
	-CMRR	$\Delta V_{CM} = -10\text{V}$, $V_{OUT} = 10\text{V}$ $V+ = 25\text{V}$, $V- = -5\text{V}$	1	$+25^\circ\text{C}$	75	-	dB
			2, 3	$+125^\circ\text{C}$, -55°C	75	-	dB

Specifications HA-2850/883

TABLE 1. DC ELECTRICAL PERFORMANCE CHARACTERISTICS (Continued)

Device Tested at: $V_{SUPPLY} = \pm 15V$, $R_{SOURCE} = 100\Omega$, $R_{LOAD} = 100k\Omega$, $V_{OUT} = 0V$, Unless Otherwise Specified.

PARAMETERS	SYMBOL	CONDITIONS	GROUP A SUBGROUP	TEMPERATURE	LIMITS		UNITS
					MIN	MAX	
Output Voltage Swing	+V _{OUT}	R _L = 1k Ω	1	+25°C	10	-	V
			2, 3	+125°C, -55°C	10	-	V
	-V _{OUT}	R _L = 1k Ω	1	+25°C	-	-10	V
			2, 3	+125°C, -55°C	-	-10	V
Output Current	+I _{OUT}	V _{OUT} = 10V	1	+25°C	10	-	mA
			2, 3	+125°C, -55°C	10	-	mA
	-I _{OUT}	V _{OUT} = -10V	1	+25°C	-	-10	mA
			2, 3	+125°C, -55°C	-	-10	mA
Quiescent Power Supply Current	+I _{CC}	V _{OUT} = 0V, I _{OUT} = 0mA	1	+25°C	-	8	mA
			2, 3	+125°C, -55°C	-	8	mA
	-I _{CC}	V _{OUT} = 0V, I _{OUT} = 0mA	1	+25°C	-8	-	mA
			2, 3	+125°C, -55°C	-8	-	mA
Power Supply Rejection Ratio	+PSRR	$\Delta V_{SUP} = 10V$ V ₊ = 10V, V ₋ = -15V V ₊ = 20V, V ₋ = -15V	1	+25°C	75	-	dB
			2, 3	+125°C, -55°C	75	-	dB
	-PSRR	$\Delta V_{SUP} = 10V$ V ₊ = 15V, V ₋ = -10V V ₊ = 15V, V ₋ = -20V	1	+25°C	75	-	dB
			2, 3	+125°C, -55°C	75	-	dB

TABLE 2. AC ELECTRICAL PERFORMANCE CHARACTERISTICS

Table 2 Intentionally Left Blank. See A.C. Specifications in Table 3

Specifications HA-2850/883

TABLE 3. ELECTRICAL PERFORMANCE CHARACTERISTICS

Device Characterized at: $V_{\text{SUPPLY}} = \pm 15\text{V}$, $R_{\text{SOURCE}} = 50\Omega$, $R_{\text{LOAD}} = 1\text{k}\Omega$, $C_L \leq 10\text{pF}$, $A_V = +10\text{V/V}$, Unless Otherwise Specified.

PARAMETERS	SYMBOL	CONDITIONS	NOTES	TEMPERATURE	LIMITS		UNITS
					MIN	MAX	
Gain Bandwidth Product	GBWP	$V_O = 200\text{mV}$, $f_O = 5\text{MHz}$	1	+25°C	400	-	MHz
		$V_O = 200\text{mV}$, $f_O = 45\text{MHz}$	1	+25°C	350	-	MHz
Slew Rate	+SR	$V_O = -5\text{V}$ to +5V	1, 4	+25°C	300	-	V/ μs
	-SR	$V_O = +5\text{V}$ to -5V	1, 4	+25°C	300	-	V/ μs
Full Power Bandwidth	FPBW	$V_{\text{PEAK}} = 10\text{V}$	1, 2	+25°C	4.8	-	MHz
Rise and Fall Time	T_R	$V_O = 0\text{V}$ to +200mV	1, 4	+25°C	-	15	ns
	T_F	$V_O = 0\text{V}$ to -200mV	1, 4	+25°C	-	15	ns
Minimum Closed Loop Stable Gain	CLSG	$R_L = 1\text{k}\Omega$, $C_L \leq 10\text{pF}$	1	-55°C to +125°C	10	-	V/V
Overshoot	+OS	$V_O = 0\text{V}$ to +200mV	1	+25°C	-	30	%
	-OS	$V_O = 0\text{V}$ to -200mV	1	+25°C	-	30	%
Open Loop Output Resistance	R_{OUT}	$V_{\text{OUT}} = 0\text{V}$	1	+25°C	-	60	Ω
Quiescent Power Consumption	PC	$V_{\text{OUT}} = 0\text{V}$, $I_{\text{OUT}} = 0\text{mA}$	1, 3	-55°C to +125°C	-	240	mW

NOTES:

- Parameters listed in Table 3 are controlled via design or process parameters and are not directly tested at final production. These parameters are lab characterized upon initial design release, or upon design changes. These parameters are guaranteed by characterization based upon data from multiple production runs which reflect lot to lot and within lot variation.
- Full Power Bandwidth guarantee based on Slew Rate measurement using $\text{FPBW} = \text{Slew Rate} / (2\pi V_{\text{PEAK}})$.
- Quiescent Power Consumption based upon Quiescent Supply Current test maximum. (No load on outputs.)
- Measured between 10% and 90% points.

TABLE 4. ELECTRICAL TEST REQUIREMENTS

MIL-STD-883 TEST REQUIREMENTS	SUBGROUPS (SEE TABLE 1)
Interim Electrical Parameters (Pre Burn-In)	1
Final Electrical Test Parameters	1 (Note 1), 2, 3, 4, 5, 6
Group A Test Requirements	1, 2, 3, 4, 5, 6
Groups C and D Endpoints	1

NOTE:

- PDA applies to Subgroup 1 only.

Die Characteristics

DIE DIMENSIONS:

65 x 52 x 19 mils $\pm$ 1 mils

1650 x 1310 x 483 μ m $\pm$ 25.4 μ m

METALLIZATION:

Type: Al, 1% Cu

Thickness: 16k $\text{\AA}$ $\pm$ 2k $\text{\AA}$

GLASSIVATION:

Type: Nitride over Silox

Silox Thickness: 12k $\text{\AA}$ $\pm$ 2k $\text{\AA}$

Nitride Thickness: 3.5k $\text{\AA}$ $\pm$ 1.5k $\text{\AA}$

WORST CASE CURRENT DENSITY:

0.7 x 10⁵ A/cm² at 1.8mA

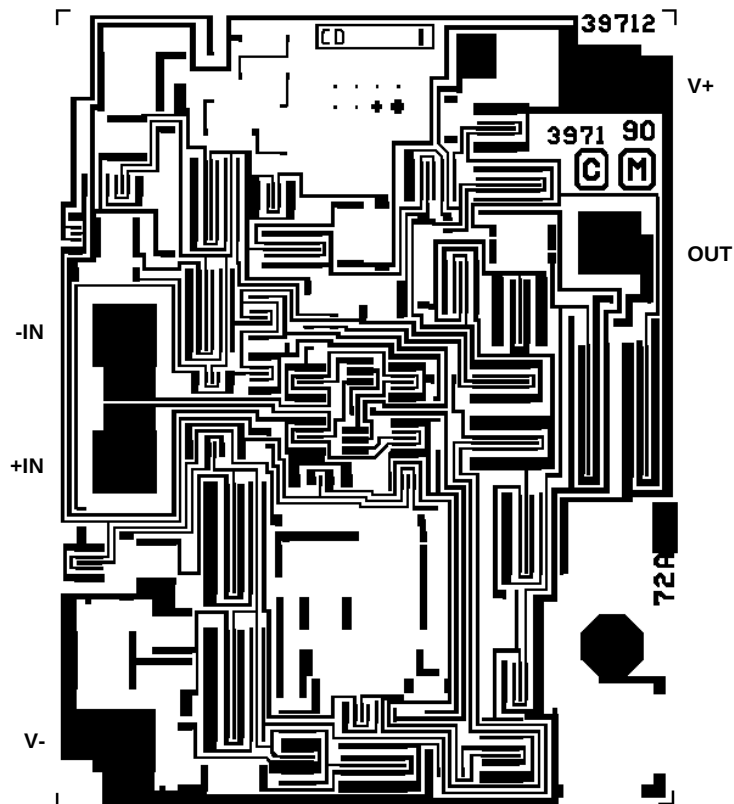
SUBSTRATE POTENTIAL (Powered Up): V-

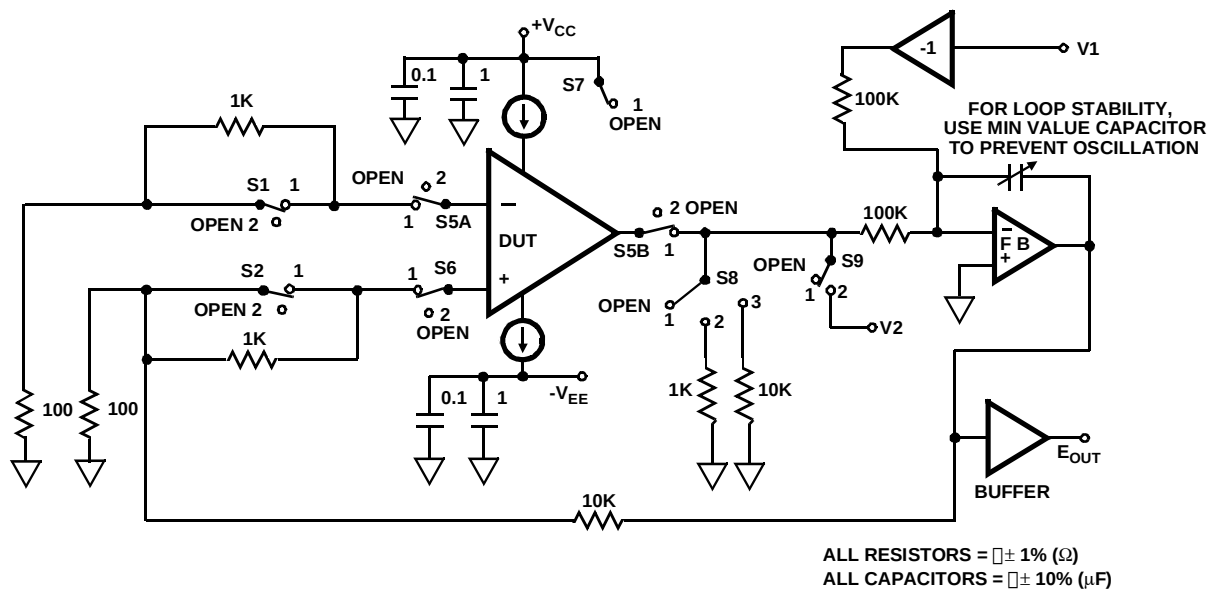
TRANSISTOR COUNT: 34

PROCESS: Bipolar Dielectric Isolation

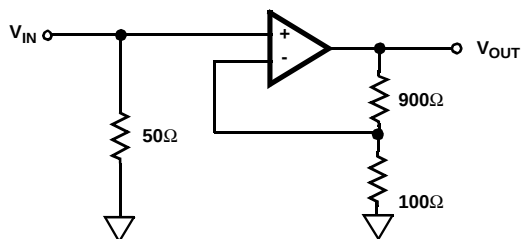
Metallization Mask Layout

HA-2850/883



Test Circuit (Applies to Table 1)**Test Waveforms**

SIMPLIFIED TEST CIRCUIT FOR LARGE AND SMALL SIGNAL RESPONSE (APPLIES TO TABLE 3)

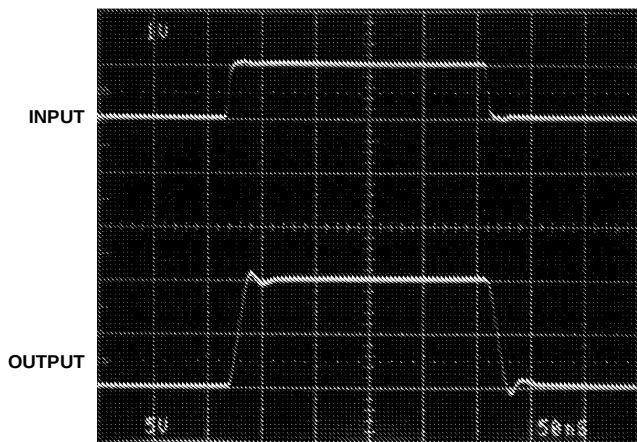


NOTES:

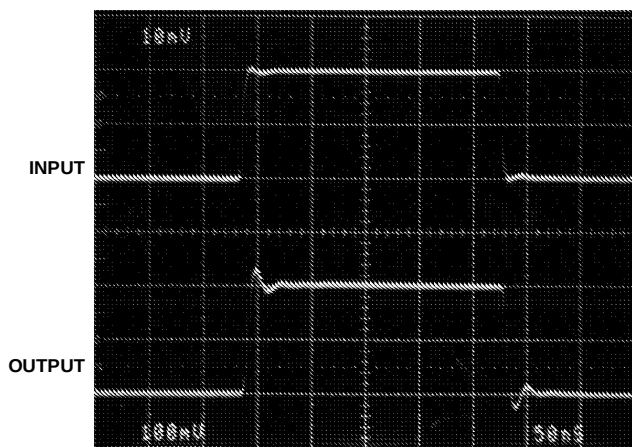
1. $V_S = \pm 15\text{V}$
2. $A_V = +10$
3. $C_L \leq 10\text{pF}$

MEASURED LARGE SIGNAL RESPONSE

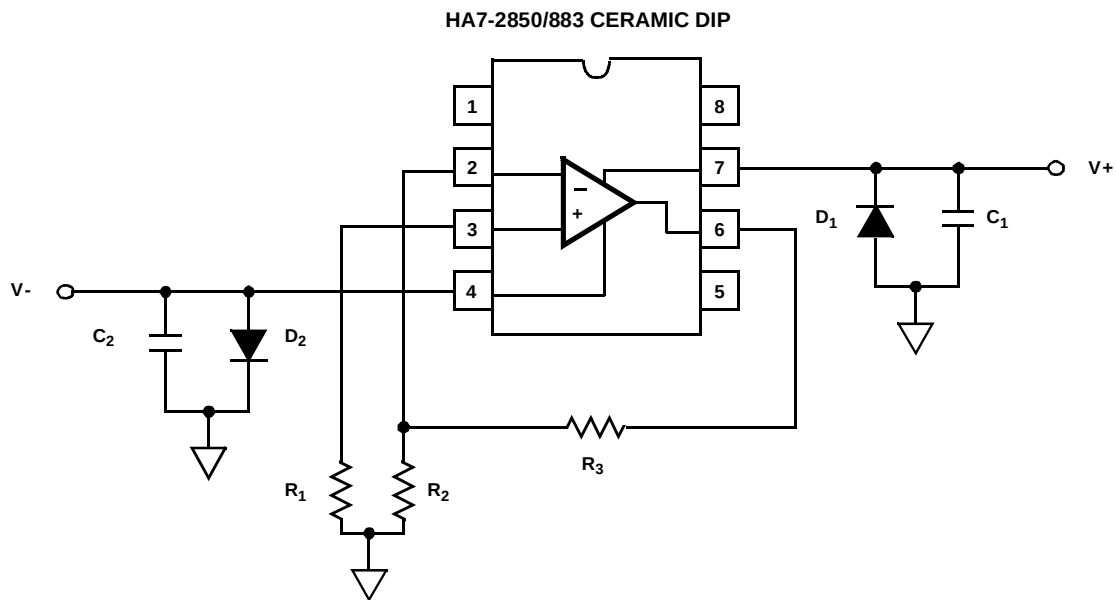
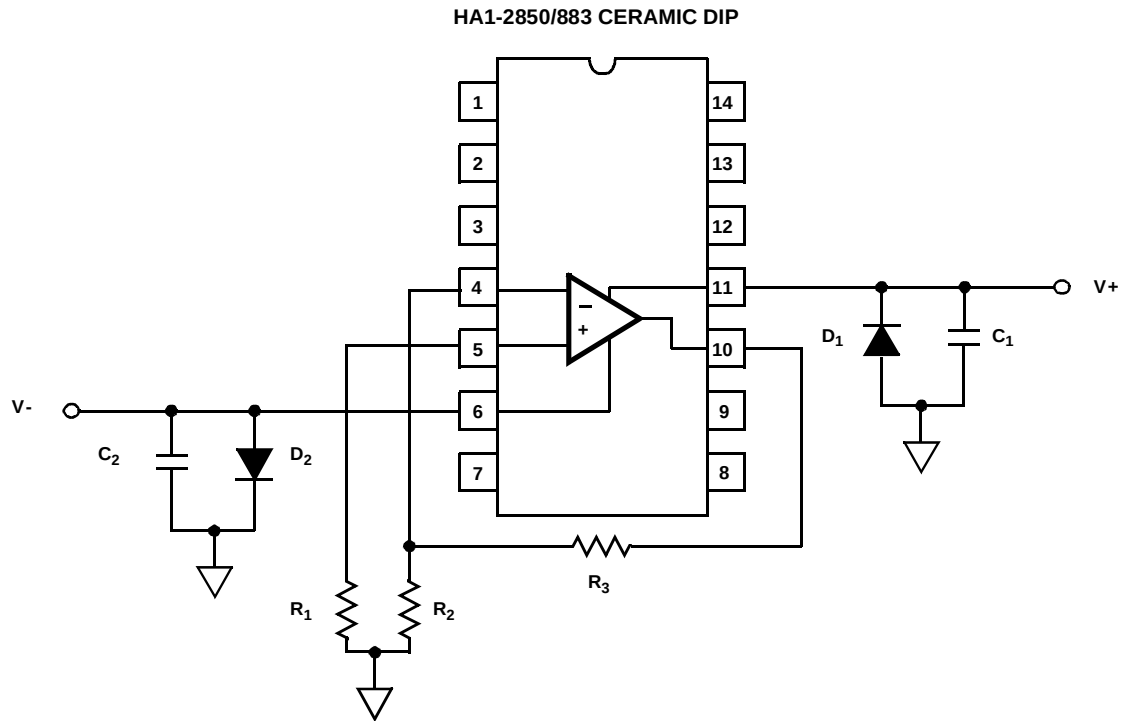
Vertical Scale: Input = 1V/Div., Output = 5V/Div.
Horizontal Scale: 50ns/Div.

**MEASURED SMALL SIGNAL RESPONSE**

Vertical Scale: Input = 10mV/Div., Output=100mV/Div.
Horizontal Scale: 50ns/Div.

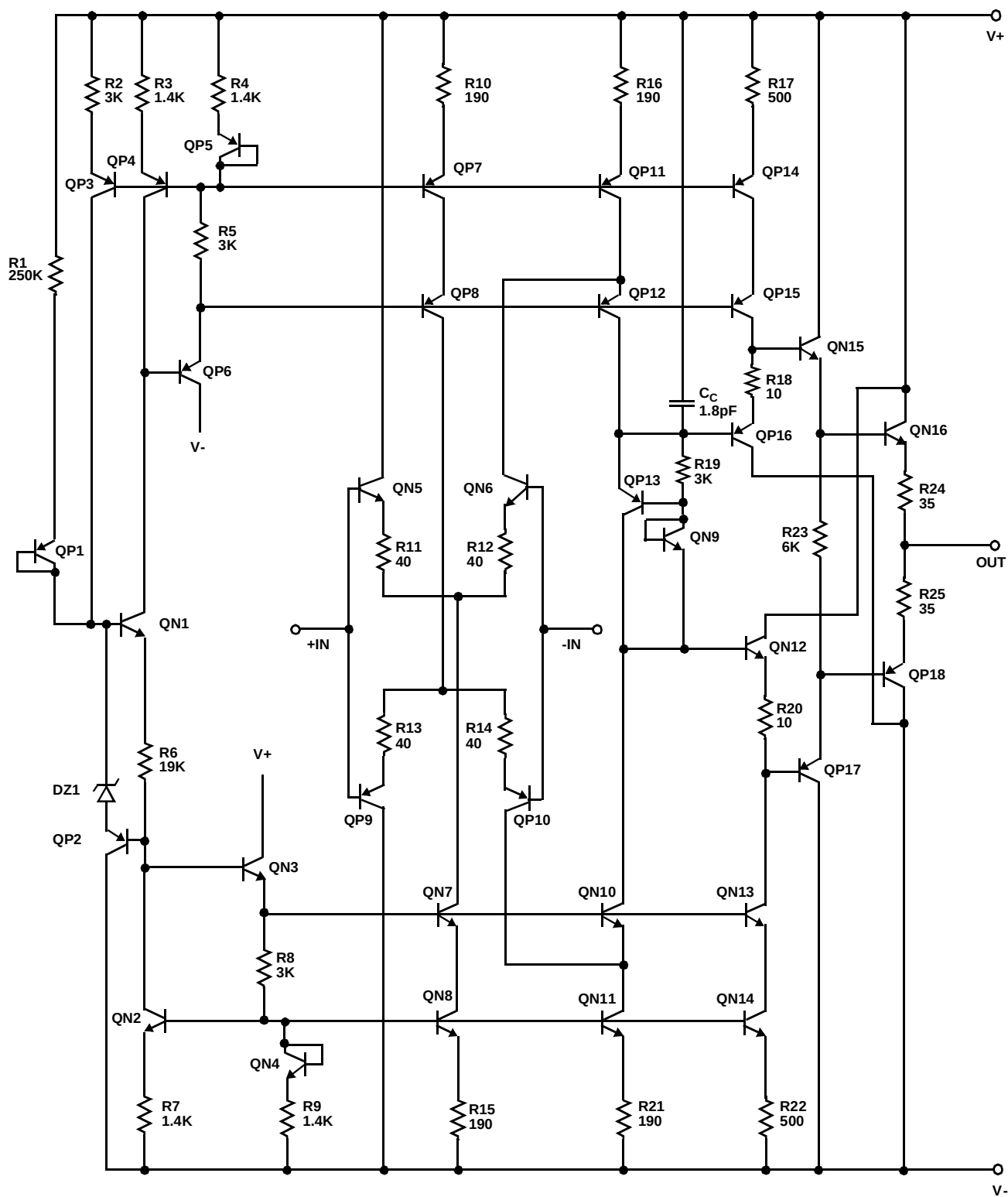


Burn-In Circuits



NOTES:

1. $R_1 = 1k\Omega, \pm 5\%, 1/4W$ (Min)
2. $R_2 = 1k\Omega, \pm 5\%, 1/4W$ (Min)
3. $R_3 = 10k\Omega, \pm 5\%, 1/4W$ (Min)
4. $C_1 = C_2 = 0.01\mu F/\text{Socket}$ (Min) or $0.1\mu F/\text{Row}$, (Min)
5. $D_1 = D_2 = 1N4002$ or Equivalent/Board
6. $| (V+) - (V-) | = 31V \pm 1V$

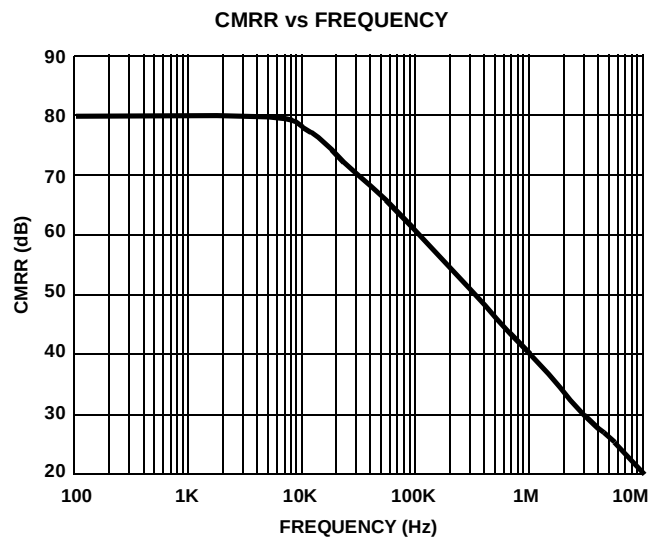
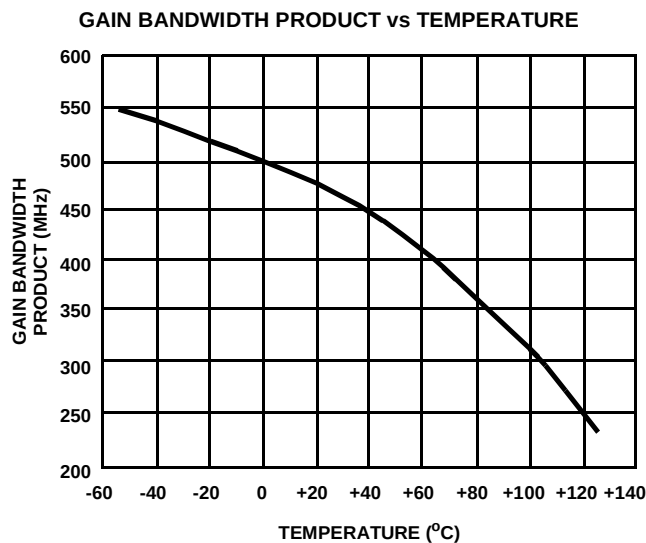
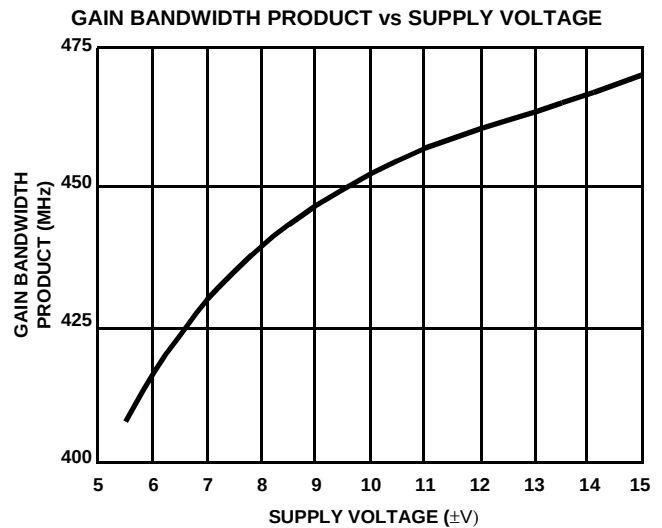
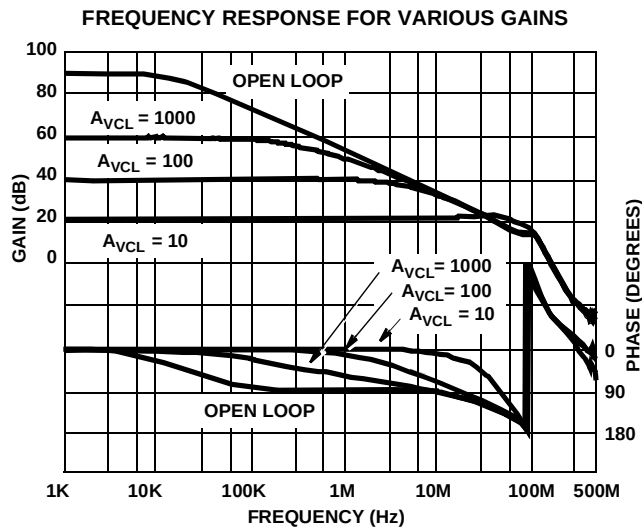
Spec Number **511095-883**

DESIGN INFORMATION

Low Power, High Slew Rate, Wideband Operational Amplifier

The information contained in this section has been developed through characterization by Intersil Corporation and is for use as application and design information only. No guarantee is implied.

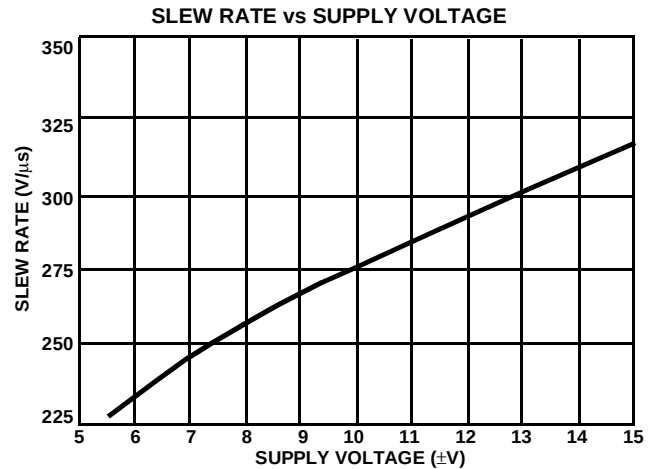
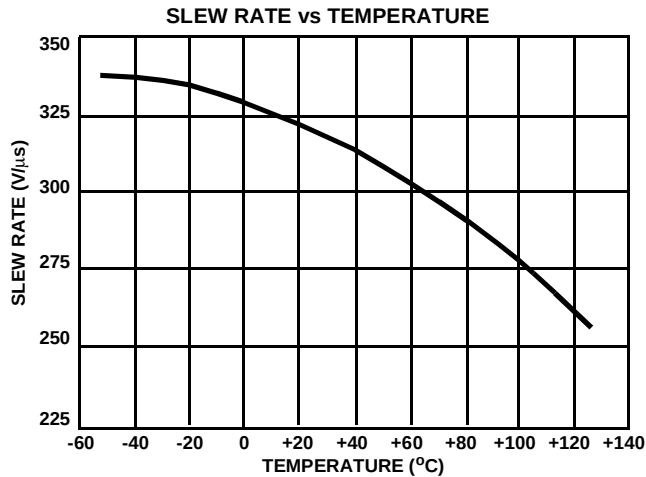
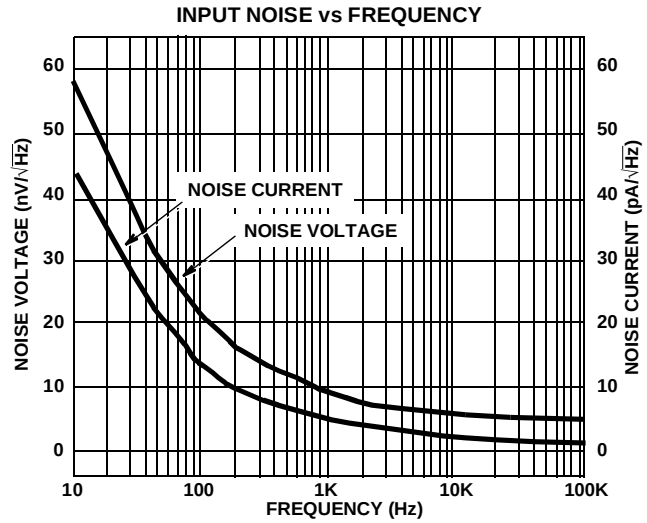
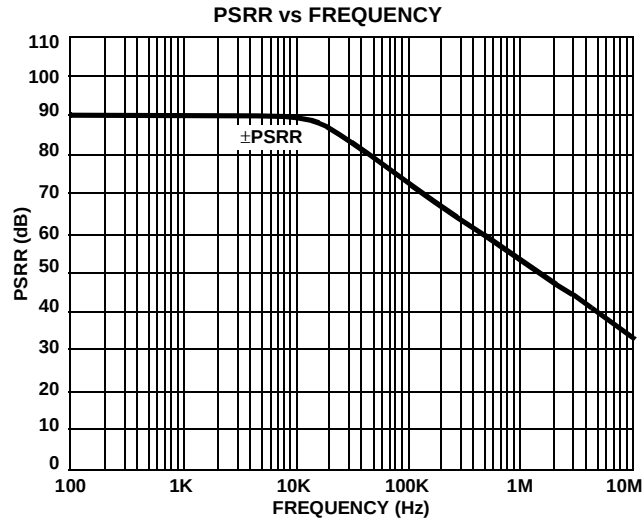
Typical Performance Curves $V_{\text{SUPPLY}} = \pm 15\text{V}$, $A_V = +10$, $R_L = 1\text{k}\Omega$, $C_L \leq 10\text{pF}$, $T_A = +25^\circ\text{C}$, Unless Otherwise Specified



DESIGN INFORMATION (Continued)

The information contained in this section has been developed through characterization by Intersil Corporation and is for use as application and design information only. No guarantee is implied.

Typical Performance Curves $V_{\text{SUPPLY}} = \pm 15\text{V}$, $A_V = +10$, $R_L = 1\text{k}\Omega$, $C_L \leq 10\text{pF}$, $T_A = +25^\circ\text{C}$, Unless Otherwise Specified
(Continued)

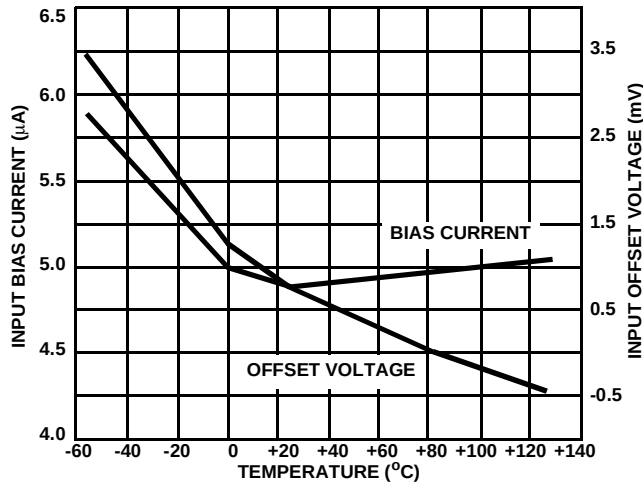


DESIGN INFORMATION (Continued)

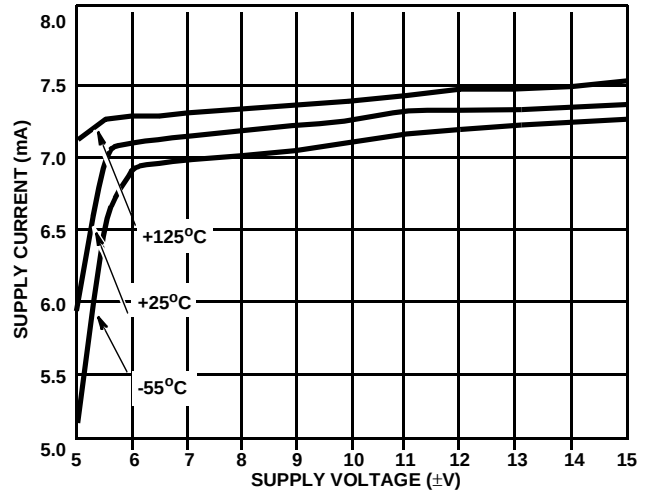
The information contained in this section has been developed through characterization by Intersil Corporation and is for use as application and design information only. No guarantee is implied.

Typical Performance Curves $V_{\text{SUPPLY}} = \pm 15\text{V}$, $A_V = +10$, $R_L = 1\text{k}\Omega$, $C_L \leq 10\text{pF}$, $T_A = +25^\circ\text{C}$, Unless Otherwise Specified
(Continued)

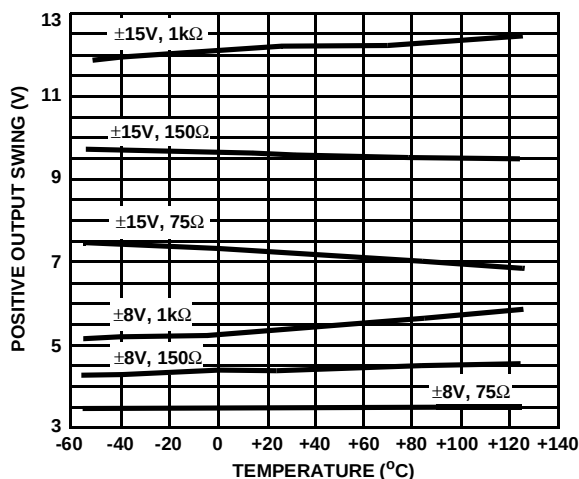
INPUT OFFSET VOLTAGE AND INPUT BIAS CURRENT vs TEMPERATURE



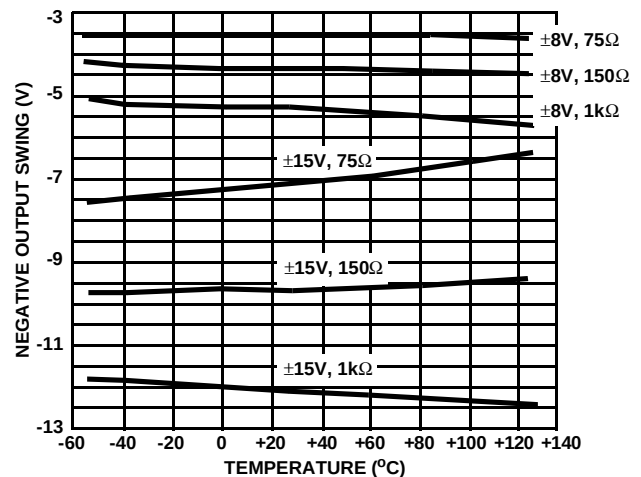
SUPPLY CURRENT vs SUPPLY VOLTAGE



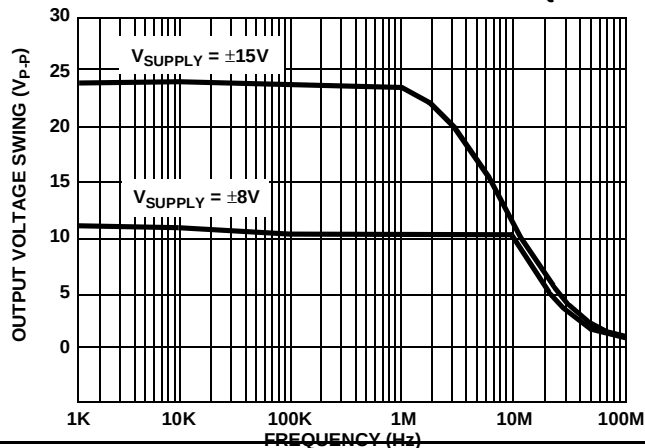
POSITIVE OUTPUT SWING vs TEMPERATURE



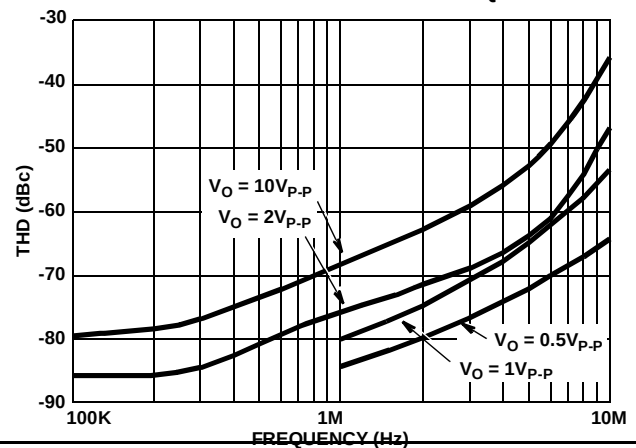
NEGATIVE OUTPUT SWING vs TEMPERATURE



MAXIMUM UNDISTORTED OUTPUT SWING vs FREQUENCY



TOTAL HARMONIC DISTORTION vs FREQUENCY



TYPICAL PERFORMANCE CHARACTERISTICS

Device Characterized at: $V_{SUPPLY} = \pm 15V$, $R_L = 1k\Omega$, $C_L \leq 10pF$, Unless Otherwise Specified

PARAMETERS	CONDITIONS	TEMPERATURE	TYPICAL	UNITS
Input Offset Voltage	$V_{CM} = 0V$	+25°C	0.6	mV
		Full	2.0	mV
Average Offset Voltage Drift	Versus Temperature	Full	20	$\mu V/^{\circ}C$
Input Bias Current	$V_{CM} = 0V$	+25°C	5.0	μA
		Full	8.0	μA
Input Offset Current	$V_{CM} = 0V$	+25°C	1.0	μA
Differential Input Resistance		+25°C	10	k Ω
Input Noise Voltage Density	$f_O = 1000Hz$	+25°C	11	$nV/\sqrt{Hz}$
Input Noise Current Density	$f_O = 1000Hz$	+25°C	6.0	$pA/\sqrt{Hz}$
Large Signal Voltage Gain	$V_{OUT} = \pm 10V$	+25°C	25	kV/V
		Full	20	kV/V
CMRR	$V_{CM} = \pm 10V$	Full	80	dB
Gain Bandwidth Product		+25°C	470	MHz
Output Voltage Swing	$R_L = 1k\Omega$	Full	± 11	V
Output Current	$V_{OUT} \geq 10V$	Full	± 20	mA
Output Resistance	Open Loop	+25°C	30	Ω
Full Power Bandwidth	$FPBW = SR/2\pi V_P$, $V_P = 10V$	+25°C	5.4	MHz
Slew Rate	$V_{OUT} = \pm 10V$, $A_V = +10$	+25°C	340	V/ μs
Rise and Fall Time	$V_{OUT} = \pm 100mV$, $A_V = +10$	+25°C	5	ns
Overshoot	$V_{OUT} = \pm 100mV$, $A_V = +10$	+25°C	25	%
PSRR	Delta $V_S = \pm 10V$ to $\pm 20V$	Full	90	dB
Supply Current	No Load	Full	7.5	mA
Differential Gain	$A_V = +10$, NTSC	+25°C	0.04	%
Differential Phase	$A_V = +10$, NTSC	+25°C	0.04	Degrees
Harmonic Distortion	$A_V = +10$, $f = 1MHz$, $V_{OUT} = 2V_{P-P}$	+25°C	-74	dBc

All Intersil U.S. products are manufactured, assembled and tested utilizing ISO9000 quality systems.

Intersil Corporation's quality certifications can be viewed at www.intersil.com/design/quality

Intersil products are sold by description only. Intersil Corporation reserves the right to make changes in circuit design, software and/or specifications at any time without notice. Accordingly, the reader is cautioned to verify that data sheets are current before placing orders. Information furnished by Intersil is believed to be accurate and reliable. However, no responsibility is assumed by Intersil or its subsidiaries for its use; nor for any infringements of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Intersil or its subsidiaries.

For information regarding Intersil Corporation and its products, see www.intersil.com