



SamHop Microelectronics Corp.

STM7820

Apr.21, 2005

N-Channel Enhancement Mode Field Effect Transistor

PRODUCT SUMMARY		
V _{DSS}	I _D	R _{DS(ON)} (mW) Max
25V	8A	15 @ V _{GS} = 10V
		30 @ V _{GS} = 4.5V

FEATURES

- Super high dense cell design for low R_{DS(ON)}.
- Rugged and reliable.
- Surface Mount Package.



ABSOLUTE MAXIMUM RATINGS (T_A=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage Rating	V _{spike} ^d	30	V
Drain-Source Voltage	V _{DS}	25	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous ^a @ T _c =25°C -Pulsed ^b	I _D	8	A
	I _{DM}	32	A
Drain-Source Diode Forward Current ^a	I _S	1.7	A
Maximum Power Dissipation ^a	P _D	2.5	W
Operating Junction and Storage Temperature Range	T _J , T _{STG}	-55 to 150	°C

THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Ambient ^a	R _{θJA}	50	°C/W
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ELECTRICAL CHARACTERISTICS ($T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ ^c	Max	Unit
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{V}_{\text{GS}} = 0\text{V}, \text{I}_D = 250\text{\mu A}$	25			V
Zero Gate Voltage Drain Current	I_{DSS}	$\text{V}_{\text{DS}} = 20\text{V}, \text{V}_{\text{GS}} = 0\text{V}$		1		\mu A
Gate-Body Leakage	I_{GSS}	$\text{V}_{\text{GS}} = \pm 20\text{V}, \text{V}_{\text{DS}} = 0\text{V}$		± 100		nA
ON CHARACTERISTICS ^b						
Gate Threshold Voltage	$\text{V}_{\text{GS(th)}}$	$\text{V}_{\text{DS}} = \text{V}_{\text{GS}}, \text{I}_D = 250\text{\mu A}$	1	1.8	3	V
Drain-Source On-State Resistance	$\text{R}_{\text{DS(ON)}}$	$\text{V}_{\text{GS}} = 10\text{V}, \text{I}_D = 8\text{A}$		11.5	15	m ohm
		$\text{V}_{\text{GS}} = 4.5\text{V}, \text{I}_D = 5\text{A}$		20	30	m ohm
On-State Drain Current	$\text{I}_{\text{D(ON)}}$	$\text{V}_{\text{DS}} = 10\text{V}, \text{V}_{\text{GS}} = 10\text{V}$	30			A
Forward Transconductance	g_{F}	$\text{V}_{\text{DS}} = 10\text{V}, \text{I}_D = 8\text{A}$		12		S
DYNAMIC CHARACTERISTICS ^c						
Input Capacitance	C_{iss}	$\text{V}_{\text{DS}} = 15\text{V}, \text{V}_{\text{GS}} = 0\text{V}$ $f = 1.0\text{MHz}$		960		pF
Output Capacitance	C_{oss}			240		pF
Reverse Transfer Capacitance	C_{rss}			140		pF
SWITCHING CHARACTERISTICS ^c						
Turn-On Delay Time	$\text{t}_{\text{D(ON)}}$	$\text{V}_{\text{DD}} = 15\text{V},$ $\text{I}_D = 1\text{A},$ $\text{V}_{\text{GS}} = 10\text{V},$ $\text{R}_{\text{GEN}} = 6 \text{ ohm}$		15.3		ns
Rise Time	t_r			32.6		ns
Turn-Off Delay Time	$\text{t}_{\text{D(OFF)}}$			17.8		ns
Fall Time	t_f			6.9		ns
Total Gate Charge	Q_g	$\text{V}_{\text{DS}} = 15\text{V}, \text{I}_D = 8\text{A}, \text{V}_{\text{GS}} = 10\text{V}$		18.1		nC
		$\text{V}_{\text{DS}} = 15\text{V}, \text{I}_D = 8\text{A}, \text{V}_{\text{GS}} = 4.5\text{V}$		9.2		nC
Gate-Source Charge	Q_{gs}	$\text{V}_{\text{DS}} = 15\text{V}, \text{I}_D = 8\text{A},$ $\text{V}_{\text{GS}} = 10\text{V}$		2.5		nC
Gate-Drain Charge	Q_{gd}			4.9		nC

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ELECTRICAL CHARACTERISTICS ($T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ ^c	Max	Unit
DRAIN-SOURCE DIODE CHARACTERISTICS ^b						
Diode Forward Voltage	V_{SD}	$V_{GS} = 0V, I_S = 1.7A$		0.78	1.2	V

Notes

- a. Surface Mounted on FR4 Board, $t \leq 10\text{sec}$.
- b. Pulse Test Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.
- c. Guaranteed by design, not subject to production testing.
- d. Guaranteed when external $R_g=6\text{ ohm}$ and $t_f < t_{f\max}$

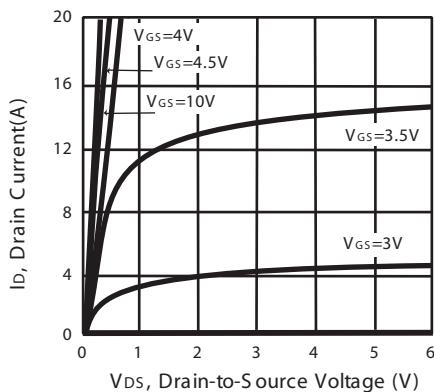


Figure 1. Output Characteristics

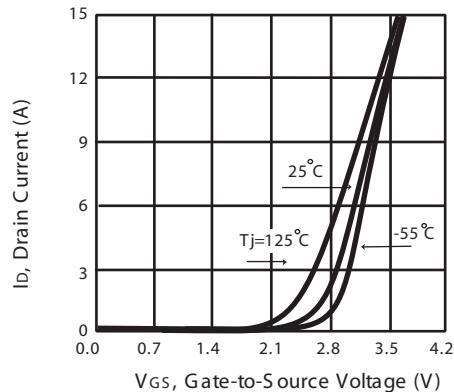


Figure 2. Transfer Characteristics

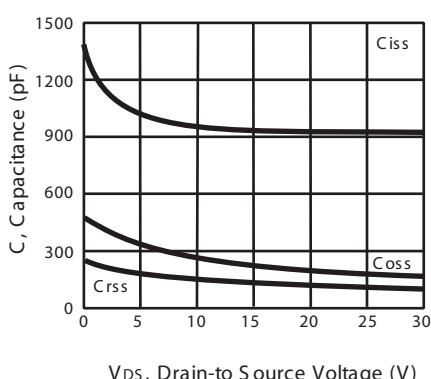


Figure 3. Capacitance

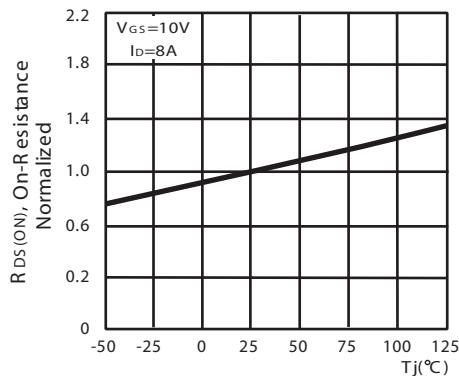
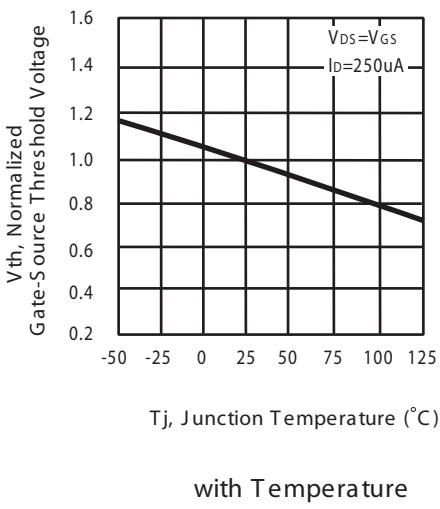


Figure 4. On-Resistance Variation with Temperature

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with Temperature

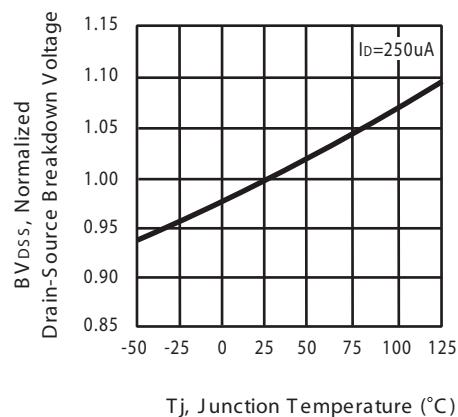
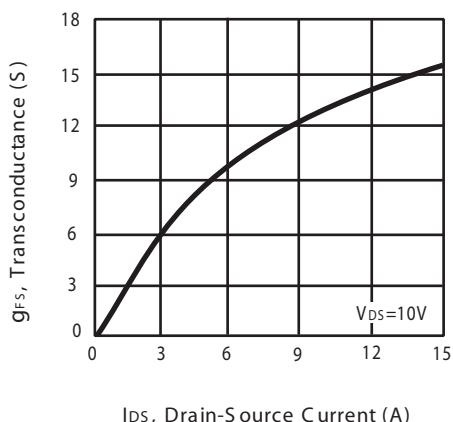
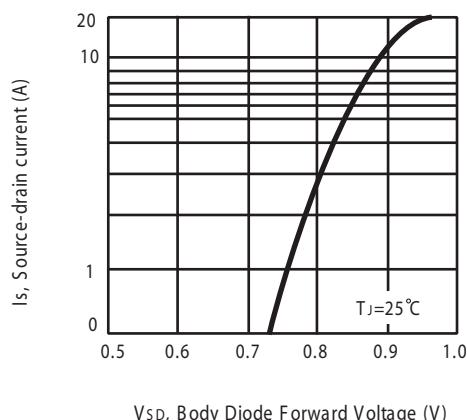


Figure 6. Breakdown Voltage Variation with Temperature



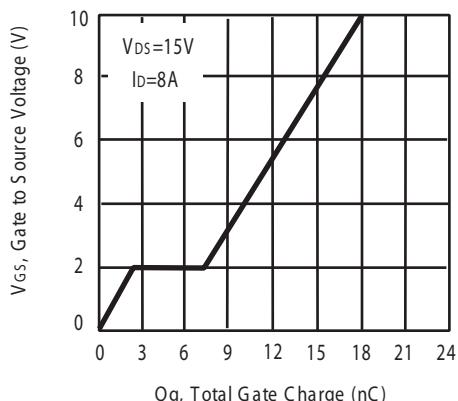
I_{DS} , Drain-Source Current (A)

Figure 7. Transconductance Variation with Drain Current



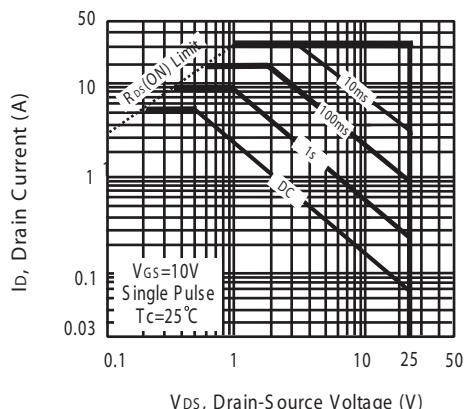
V_{SD} , Body Diode Forward Voltage (V)

Figure 8. Body Diode Forward Voltage Variation with Source Current



Q_g , Total Gate Charge (nC)

Figure 9. Gate Charge



V_{DS} , Drain-Source Voltage (V)

Figure 10. Maximum Safe Operating Area

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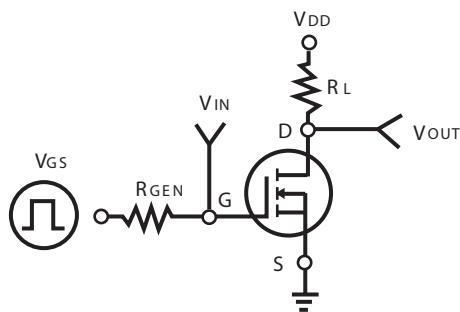


Figure 11. S switching Test Circuit

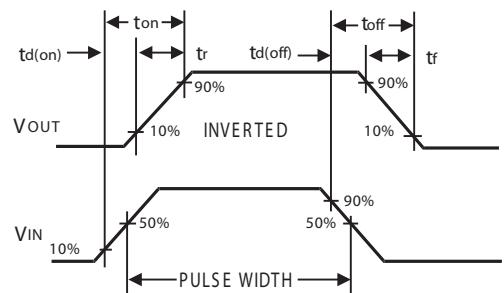


Figure 12. S switching Waveforms

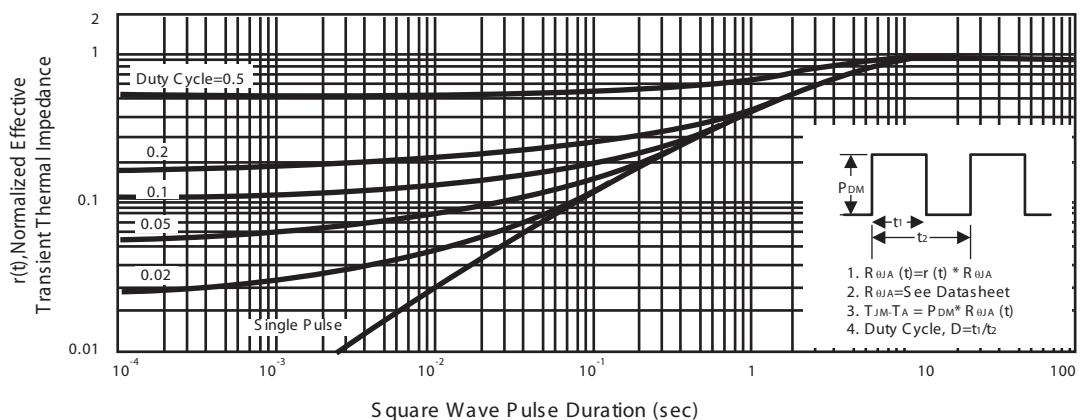
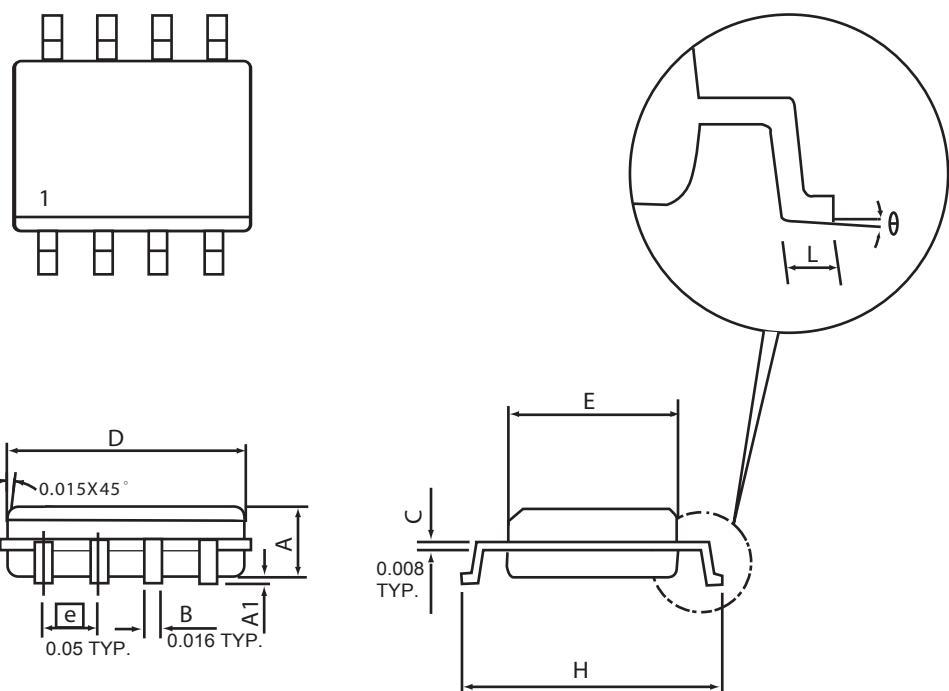


Figure 13. Normalized Thermal Transient Impedance Curve

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PACKAGE OUTLINE DIMENSIONS

SO-8

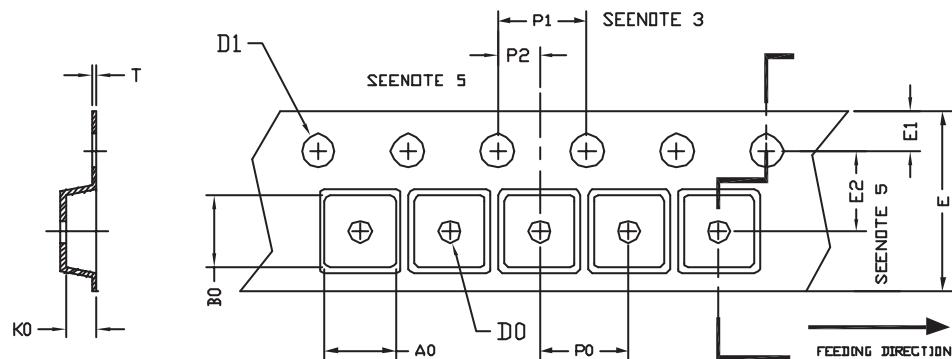


SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	1.35	1.75	0.053	0.069
A1	0.10	0.25	0.004	0.010
D	4.80	4.98	0.189	0.196
E	3.81	3.99	0.150	0.157
H	5.79	6.20	0.228	0.244
L	0.41	1.27	0.016	0.050
θ	0°	8°	0°	8°

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SO-8 Tape and Reel Data

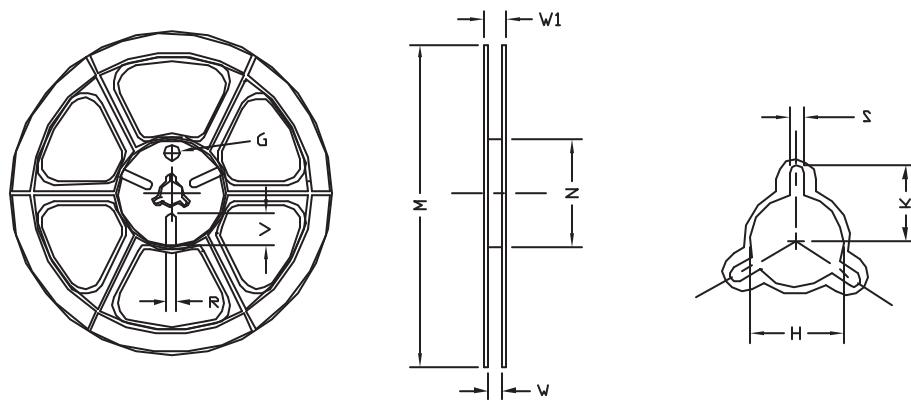
SO-8 Carrier Tape



unit:mm

PACKAGE	A0	B0	K0	D0	D1	E	E1	E2	P0	P1	P2	T
SOP 8N 150mil	6.40	5.20	2.10	$\phi 1.5$ (MIN)	$\phi 1.5$ $+ 0.1$ $- 0.0$	12.0 ± 0.3	1.75	5.5 ± 0.05	8.0	4.0	2.0 ± 0.05	0.3 ± 0.05

SO-8 Reel



UNIT:mm

TAPE SIZE	REEL SIZE	M	N	W	W1	H	K	S	G	R	V
12 mm	$\phi 330$	330 ± 1	62 ± 1.5	12.4 $+ 0.2$	16.8 $- 0.4$	$\phi 12.75$ $+ 0.15$	---	2.0 ± 0.15	---	---	---