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# HAT2052T

Silicon N Channel Power MOS FET  
High Speed Power Switching

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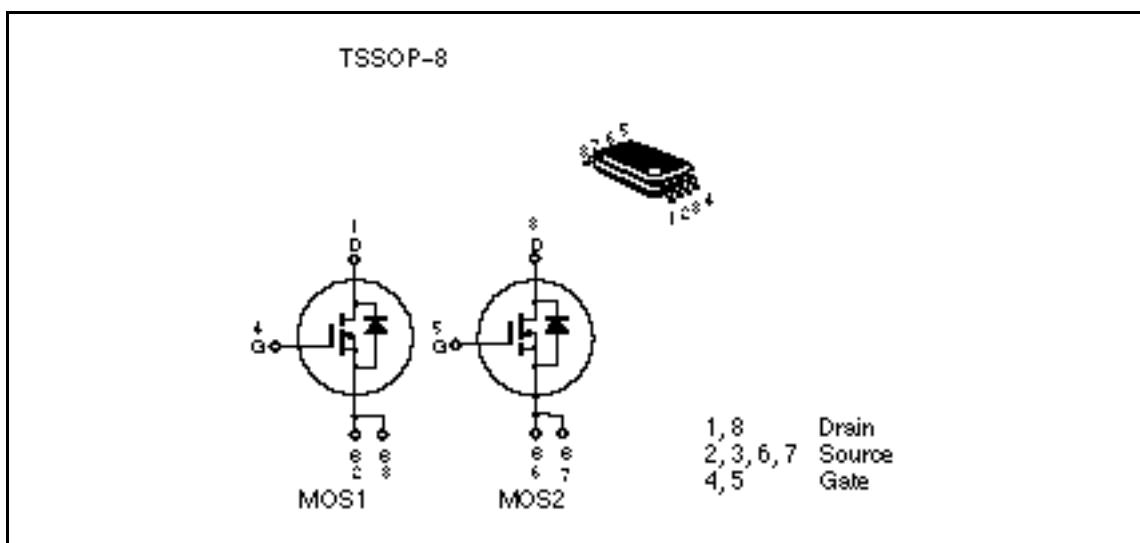
ADE-208-724C (Z)  
4th. Edition  
February 1999

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## Features

- Low on-resistance
- Capable of 2.5 V gate drive
- Low drive current
- High density mounting

## Outline



## HAT2052T

### Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	28	V
Gate to source voltage	V <sub>GSS</sub>	±12	V
Drain current	I <sub>D</sub>	5.0	A
Drain peak current	I <sub>D(pulse)</sub> <sup>Note1</sup>	40	A
Body-drain diode reverse drain current	I <sub>DR</sub>	5.0	A
Channel dissipation	Pch <sup>Note2</sup>	1.0	W
Channel dissipation	Pch <sup>Note3</sup>	1.5	W
Channel temperature	T <sub>ch</sub>	150	°C
Storage temperature	T <sub>tstg</sub>	-55 to +150	°C

Note: 1. PW 10μs, duty cycle 1 %

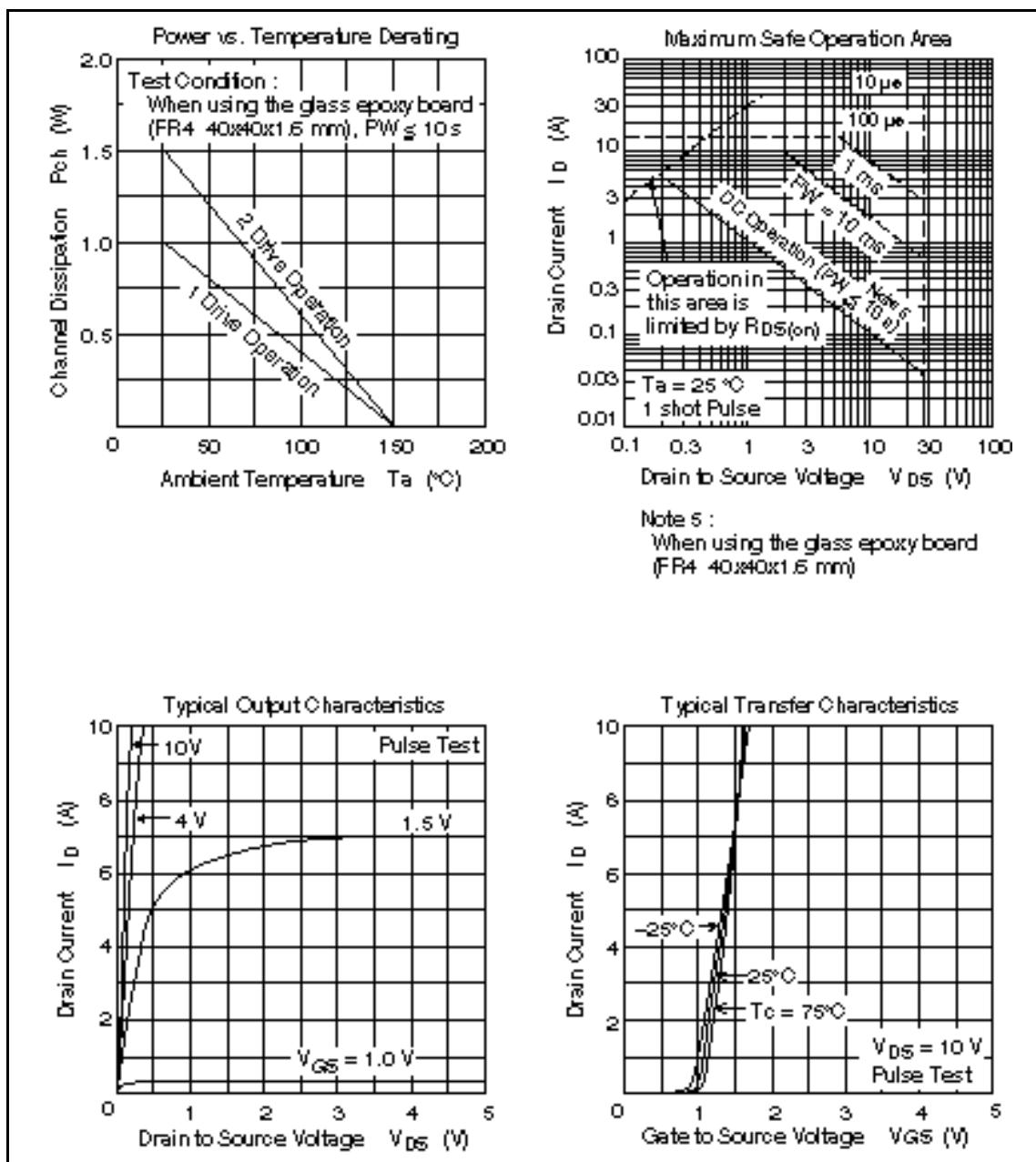
2. 1 Drive operation ; When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW 10s

3. 2 Drive operation ; When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW 10s

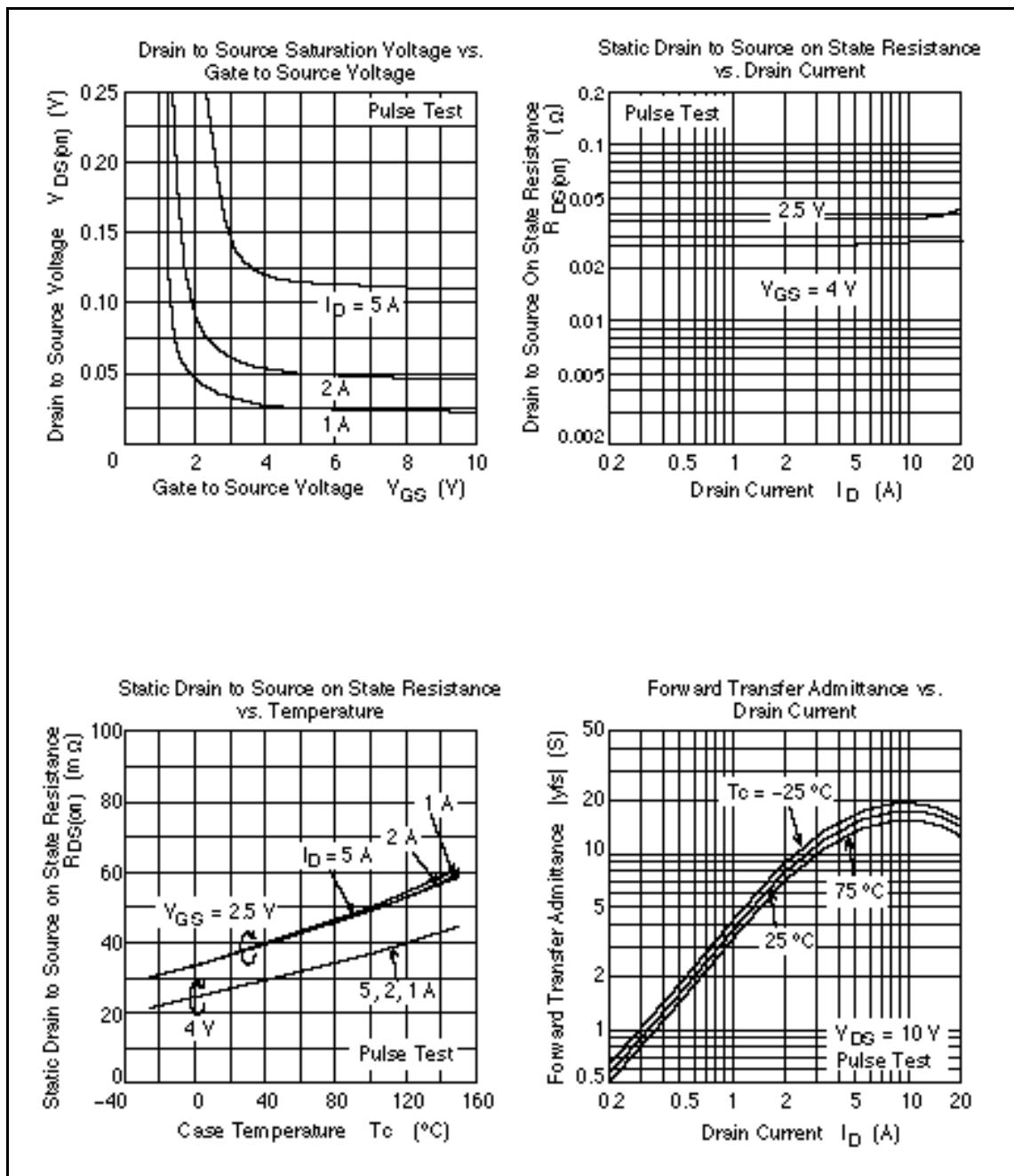
### Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	28	—	—	V	I <sub>D</sub> = 10mA, V <sub>GS</sub> = 0
Gate to source leak current	I <sub>GSS</sub>	—	—	±0.1	μA	V <sub>GS</sub> = ±12V, V <sub>DS</sub> = 0
Zero gate voltage drain current	I <sub>DSS</sub>	—	—	1	μA	V <sub>DS</sub> = 28 V, V <sub>GS</sub> = 0
Gate to source cutoff voltage	V <sub>GS(off)</sub>	0.4	—	1.4	V	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1mA
Static drain to source on state resistance	R <sub>DS(on)</sub>	—	0.027	0.034		I <sub>D</sub> = 3A, V <sub>GS</sub> = 4V <sup>Note4</sup>
Forward transfer admittance	Y <sub>fs</sub>	7	11	—	S	I <sub>D</sub> = 3A, V <sub>DS</sub> = 10V <sup>Note4</sup>
Input capacitance	C <sub>iss</sub>	—	510	—	pF	V <sub>DS</sub> = 10V
Output capacitance	C <sub>oss</sub>	—	190	—	pF	V <sub>GS</sub> = 0
Reverse transfer capacitance	C <sub>rss</sub>	—	140	—	pF	f = 1MHz
Total gate charge	Q <sub>g</sub>	—	8.5	—	nc	V <sub>DD</sub> = 10V
Gate to source charge	Q <sub>gs</sub>	—	4.5	—	nc	V <sub>GS</sub> = 4V
Gate to drain charge	Q <sub>gd</sub>	—	4	—	nc	I <sub>D</sub> = 5A
Turn-on delay time	t <sub>d(on)</sub>	—	14	—	ns	V <sub>GS</sub> = 4V, I <sub>D</sub> = 3A
Rise time	t <sub>r</sub>	—	120	—	ns	V <sub>DD</sub> = 10V
Turn-off delay time	t <sub>d(off)</sub>	—	85	—	ns	
Fall time	t <sub>f</sub>	—	120	—	ns	
Body-drain diode forward voltage	V <sub>DF</sub>	—	0.85	1.1	V	IF = 5.0A, V <sub>GS</sub> = 0 <sup>Note4</sup>
Body-drain diode reverse recovery time	t <sub>rr</sub>	—	40	—	ns	IF = 5.0A, V <sub>GS</sub> = 0 dI/dt = 20A/μs

Note: 4. Pulse test

**Main Characteristics**

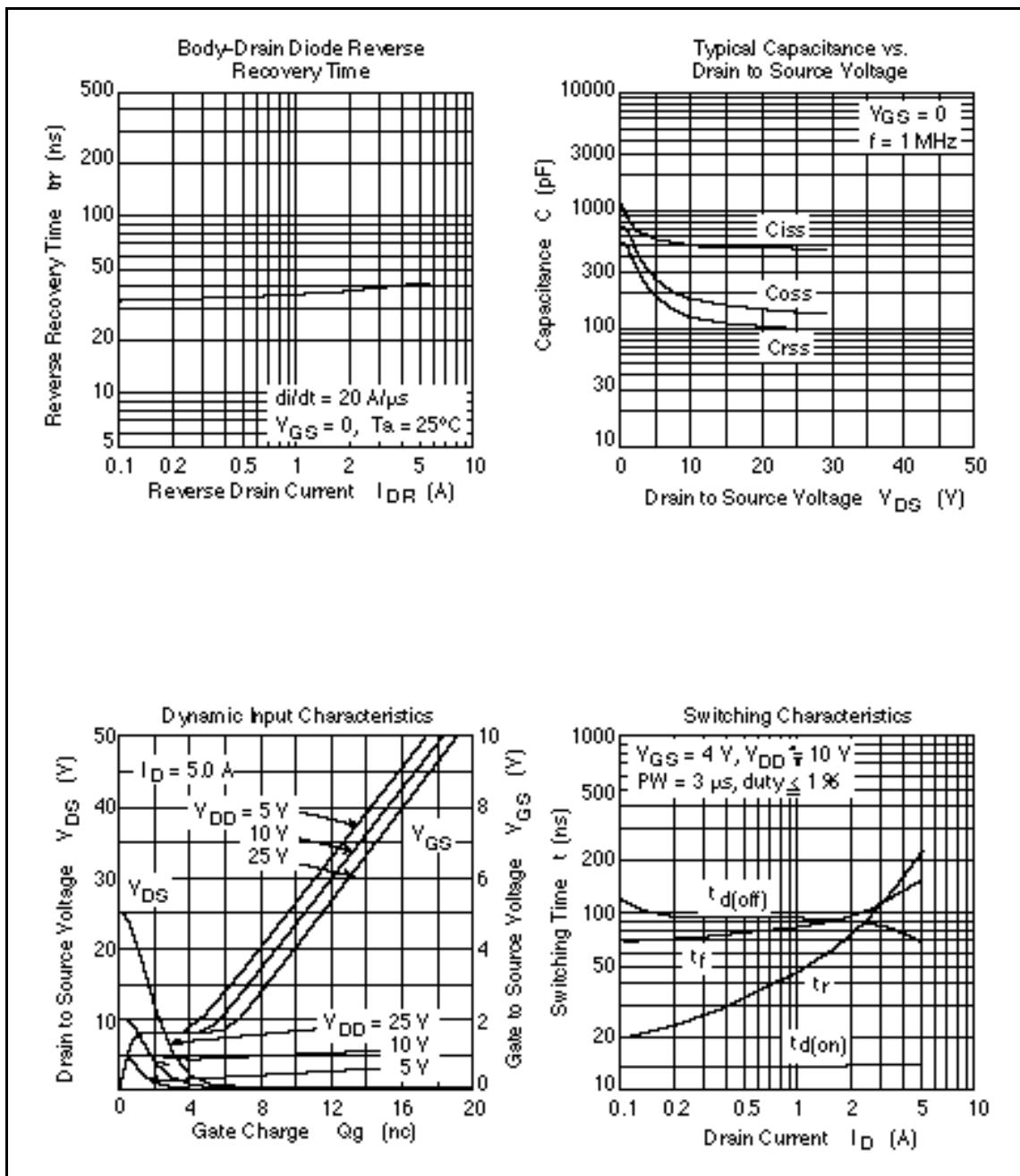
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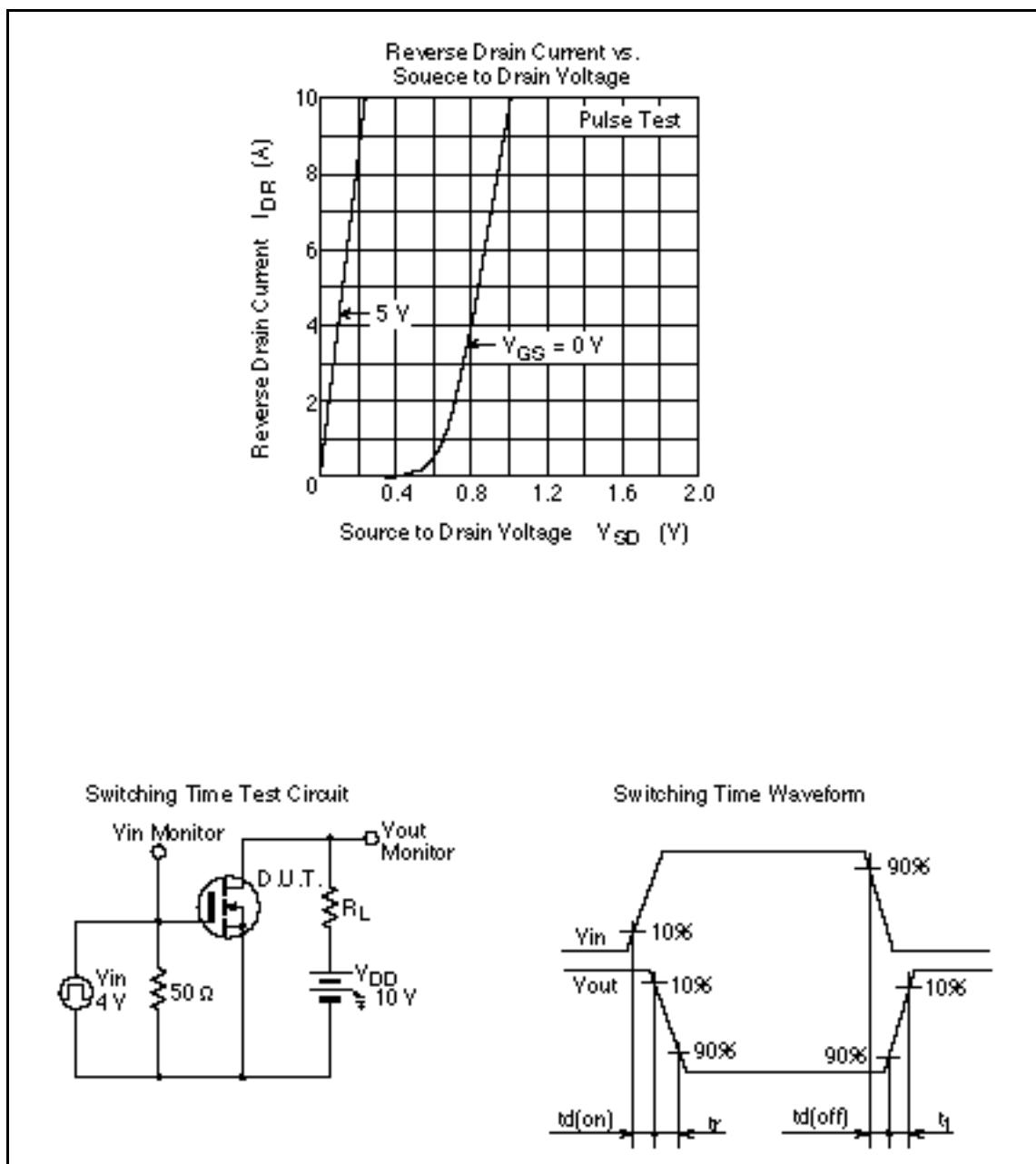
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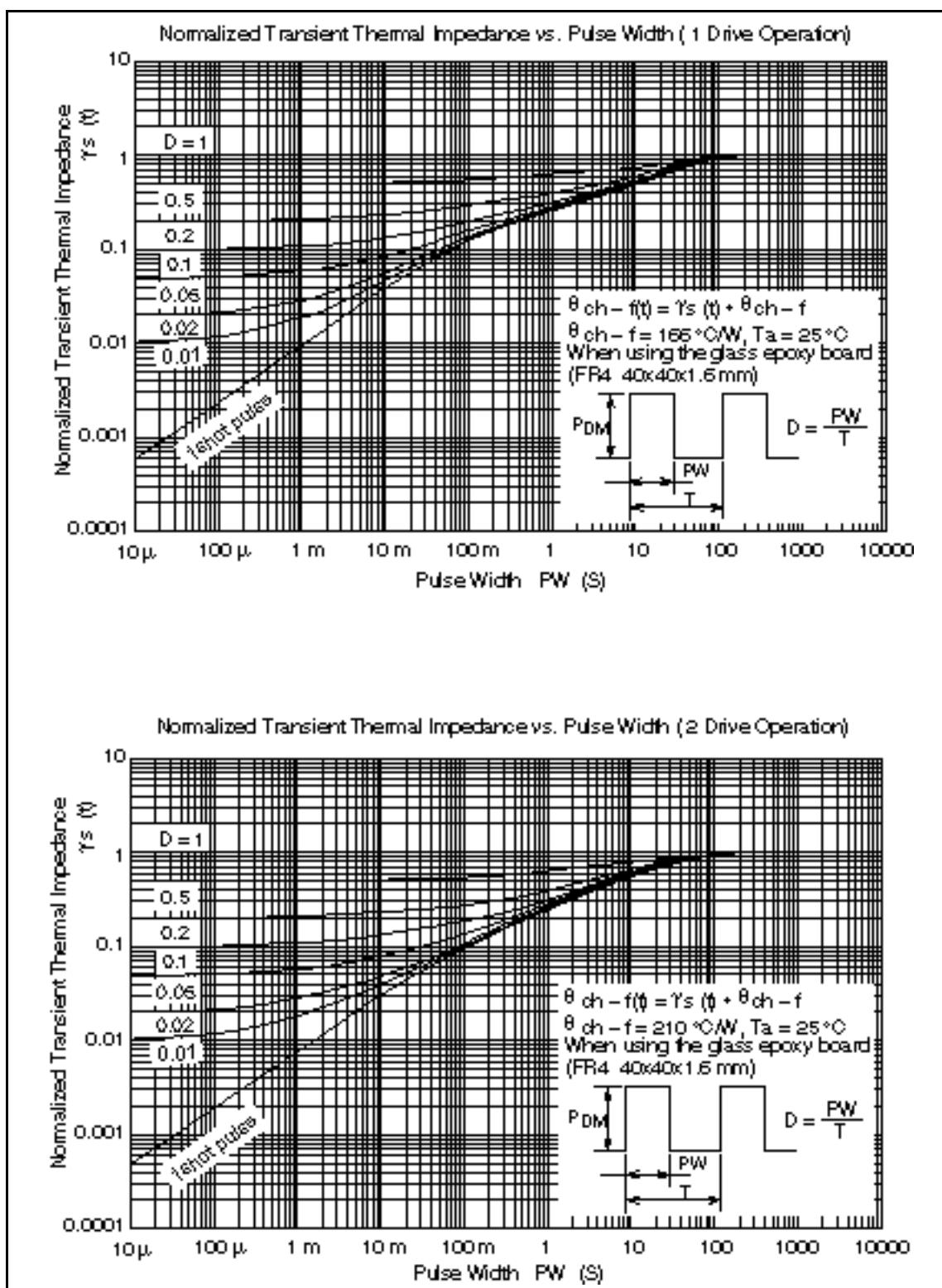
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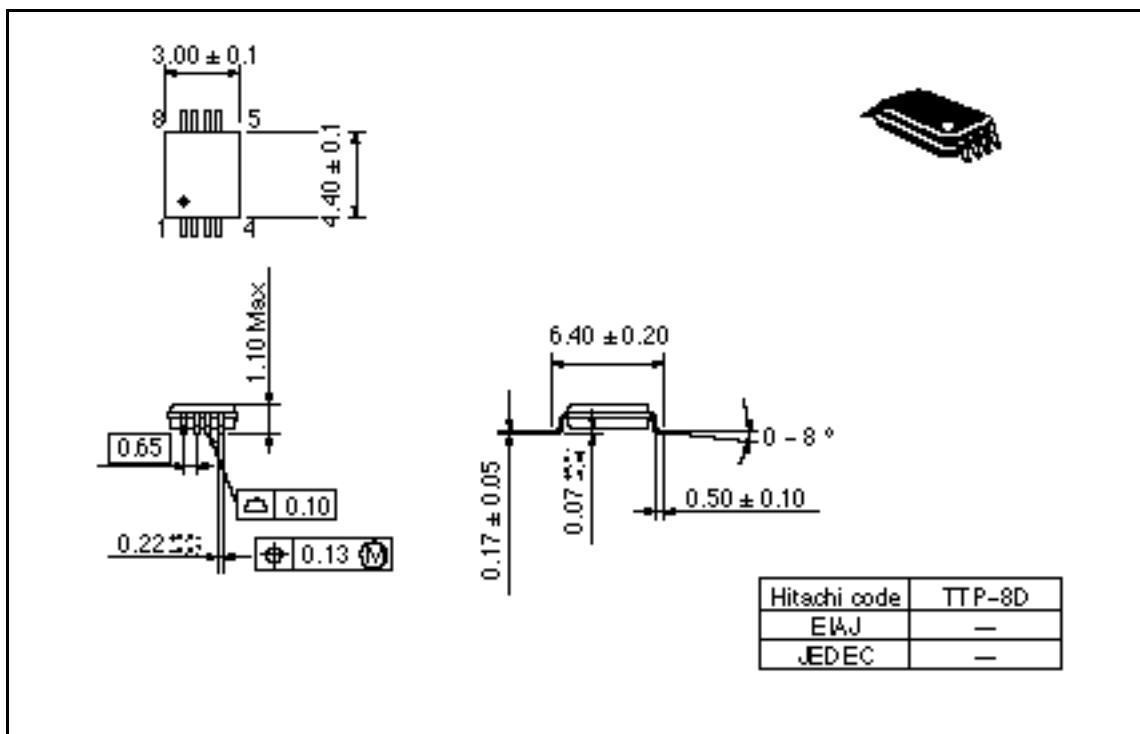




## HAT2052T

### Package Dimensions

Unit: mm



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