AN5534N

Vertical deflection output IC

Overview

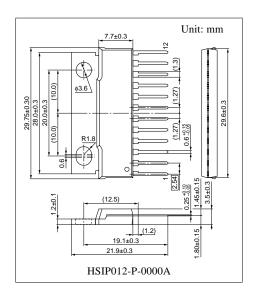
The AN5534N is a vertical deflection output IC for television and CRT monitor. Incorporating a sawtooth wave generator, this IC enables you to form an AC/DC feedback-loop by itself only.

Features

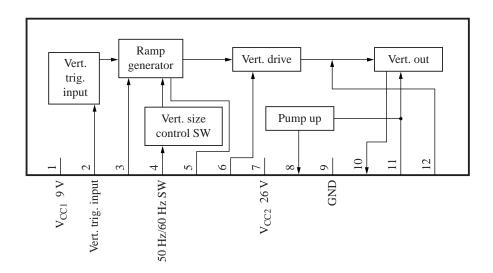
- Built-in stable sawtooth wave generator independent of input pulse width variation
- Built-in 50 Hz/60 Hz changeover circuit
- Minimum fly-back time of saw-tooth wave signal: 100 µs

Applications

• Televisions and CRT displays



Block Diagram



Pin Descriptions

Pin No.	Description	Pin No.	Description
1	Power supply 1	7	Power supply 2
2	Vertical pulse input	8	Pulse amplification
3	Vertical amplitude control	9	GND
4	50 Hz/60 Hz changeover	10	Vertical output
5	Saw-tooth wave generation	11	Power supply for vertical output
6	AC/DC feedback input	12	Prevention from oscillation

Absolute Maximum Ratings

Parameter	Symbol	Rating	Unit	
Supply voltage	V _{CC1}	15	V	
	V _{CC2}	30		
Pin voltage	V ₂₋₉	0 to 2.7	V	
	V ₄₋₉	0 to V ₁₋₉		
	V ₅₋₉	0 to V ₁₋₉		
	V ₆₋₉	0 to V ₁₋₉		
	V ₁₀₋₉	0 to 61		
	V ₁₁₋₉	0 to 61		
Supply current	I _{CC1}	20	mA	
Pin current	I ₃	- 0.2 to 0	mA	
	I ₈	-1.8 to +1.8	A[0-p]	
	I ₁₀	-2.2 to +2.2		
Power dissipation	P _D	27	W	
Operating ambient temperature *	T _{opr}	-20 to +70	°C	
Storage temperature *	T _{stg}	-55 to +150	°C	

Note) 1. Do not apply external currents or voltages to any pins not specifically mentioned.

2. For circuit currents, '+' denotes current flowing into the IC, and '-' denotes current flowing out of the IC.

3. * : Except for the operating ambient temperature and storage temperature, all ratings are for $T_a = 25^{\circ}C$.

Recommended Operating Range

Parameter	Symbol	Range	Unit	
Supply voltage	V _{CC1}	7 to 15	V	
	V _{CC2}	10 to 30		

Electrical Characteristics at $T_a = 25^{\circ}C$

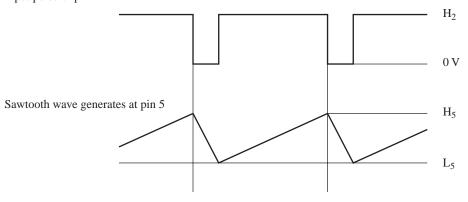
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Load short-circuit	R. short	$V_{CC2} = 26 V$	Free from breakdown			
Deflection current	I _{HP-P}	$V_{CC2} = 26 V, V_5 = 2.2 V[p-p]$ Sine wave 1 kHz	1.8	2.0	2.2	A[p-p]
Vertical amplifier distortion factor	T. H. D _H	$V_{CC2} = 26 V, V_5 = 2.2 V[p-p]$ Sine wave 1 kHz		2.0	5.0	%
Input threshold voltage	V ₂	Refer to the "• Test method"	0.5	0.7	1.0	V
Sawtooth wave generating start voltage	V ₅	Refer to the "• Test method"	3.6	4.5	5.4	V
Middle point voltage	V _{MID}		11.5	12.8	14.1	V
Idling current	I ₁₁		21.0	36.0	51.0	mA
Output saturation voltage (lower)	V ₁₁₋₁₀	$V_7 = GND$		3.0	4.0	V
Output saturation voltage (upper)	V ₁₀₋₉	$V_7 = GND$		1.5	2.5	V
Pump up charging saturation voltage	V ₈₋₉	$V_7 = GND$	_	0.2	0.5	V
Pump up discharging saturation voltage	V ₇₋₈	$V_7 = GND$		3.0	4.0	V

Test method

1. Input threshold voltage (V_2)

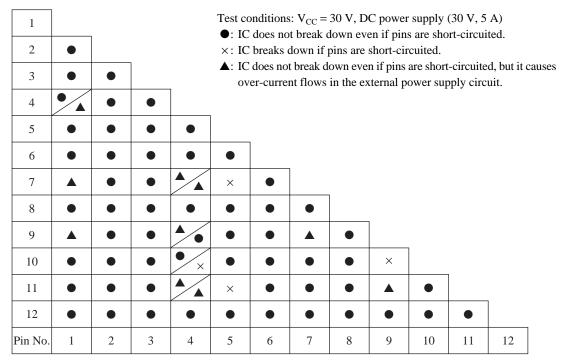
 $\rm H_2$ voltage at which a saw-tooth wave shown below is generated at pin 5 when $\rm H_2$ voltage is gradually increased from 0 V

 Saw-tooth wave generating start voltage (V₅)
 Lower level voltage of a sawtooth wave which generates at pin 5 Input pulse of pin 2



Usage Notes

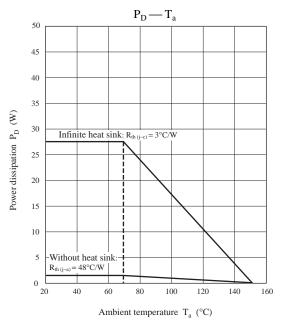
• Inter-pin short-circuit test result



Pin 4 (50 Hz/60 Hz changeover) = V_{CC1} Pin 4 (50 Hz/60 Hz changeover) = GND

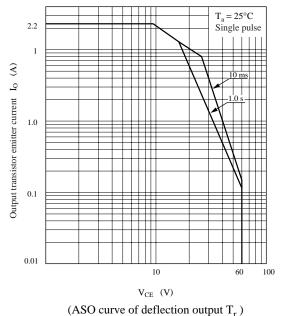
Technical Information

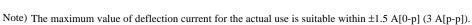
1. P_D — T_a curves of HSIP012-P-0000A

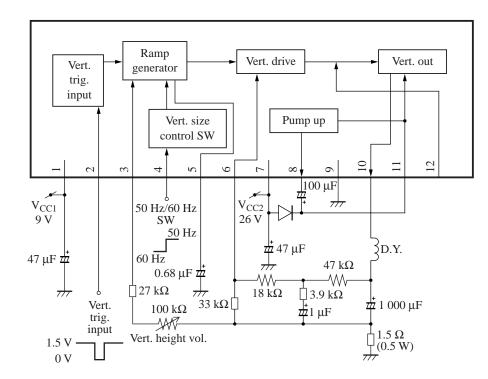


Technical Information (continued)

2. Area of safe operation (ASO) of output transistor forward-biased







■ Application Circuit Example

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