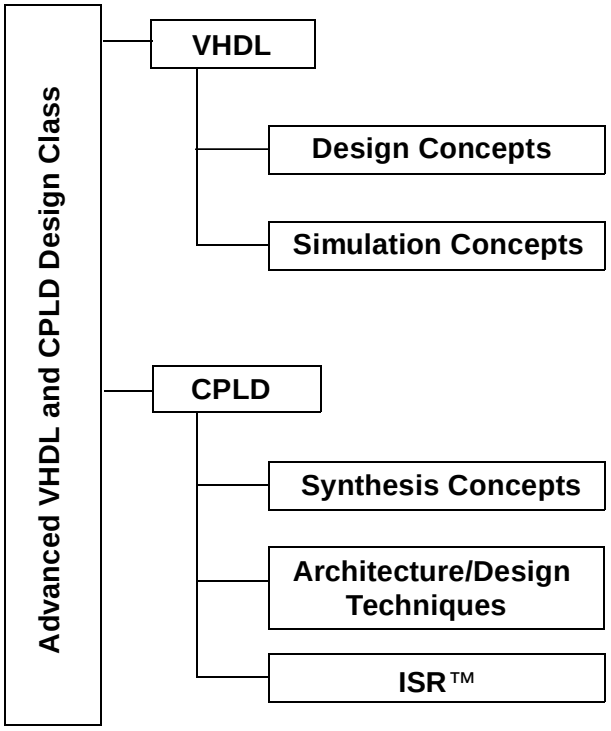
CY3120AVHDL

Advanced VHDL and CPLD Design Class Kit

Features

- **Advanced VHDL design and simulation concepts**
- **Advanced Programmable Logic synthesis concepts**
- **CPLD design techniques**
- **In-System Reprogramming (ISR™) Design Issues**



Introduction

Using VHDL and CPLDs increases efficiency and productivity. This one day workshop teaches methods for minimizing errors, simplifying and optimizing designs, and utilizing Cypress CPLDs to their maximum capabilities.

Objectives

- Learn advanced VHDL concepts such as **aggregates, generics, and subprograms** to improve design efficiency.
- Learn how to create and use **test benches** to streamline your verification process.
- Become familiar with **advanced PLD synthesis** concepts to direct and improve how your PLD designs are realized in silicon.
- Learn how **CPLD architecture features** effect design performance and device utilization, and how to use this information and specialized design techniques to get the most from your designs.
- Learn how to implement CPLD designs that may be programmed and reprogrammed **in system** to improve your manufacturing efficiency and facilitate field upgrades.

Prerequisites

- Completion of the Cypress Introductory VHDL Class

  or

- Prior VHDL knowledge and familiarity with a Cypress *Warp*™ product

Ordering Information

1. **Register for the class** by contacting your local Cypress or Representative Sales office.
2. **When you register, order the CY3120AVHDL** Advanced VHDL&CPLD Design Class kit which includes:

   A letter outlining the Advanced VHDL&CPLD Design Class

   FLASH370i™ brochure

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