TOSHIBA TA4001F

TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

# TA4001F

# VHF UHF WIDE BAND AMPLIFIER

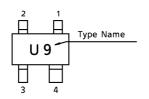
#### **FEATURES**

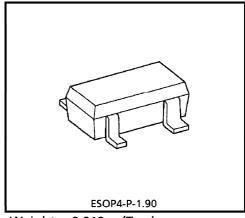
- Band Width 2.4 GHz (Typ.) (3 dB down)
- High Gain:  $|S_{21}|^2 = 12.5 \, dB \, (Typ.) \, (f = 500 \, MHz)$
- 50  $\Omega$  Input and Output Impedance
- Small Package

## PIN ASSIGNMENT (TOP VIEW)



## **MARKING**





Weight: 0.013 g (Typ.)

### **MAXIMUM RATINGS** (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	V <sub>CC</sub>	6	V
Total Power Dissipation	P <sub>D</sub> (*)	300	mW
Operating Temperature	T <sub>opr</sub>	<b>- 40∼85</b>	°C
Storage Temperature	T <sub>stg</sub>	- 55~125	°C

(\*): When mounted glass epoxy of  $2.5 \text{ cm}^2 \times 1.6 \text{ t}$ 

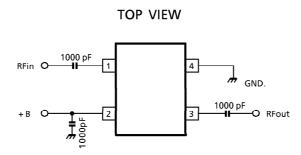
## **ELECTRICAL CHARACTERISTICS** (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Circuit Current	lcc	_	V <sub>CC</sub> = 5 V, Non carrier	14	18	24	mA
Insertion Gain	S <sub>21</sub>   <sup>2</sup>	1	V <sub>CC</sub> = 5 V, f = 500 MHz	9	12.5	16	dB
Band Width	BW	1	V <sub>CC</sub> = 5 V (Note 1)	1.9	2.4	_	GHz
Noise Figure	NF	1	V <sub>CC</sub> = 5 V, f = 500 MHz	_	5.2	7	dB
Input Return Loss	S <sub>11</sub>   <sup>2</sup>	1	V <sub>CC</sub> = 5 V, f = 500 MHz	_	- 18	_	dB
Output Return Loss	S <sub>22</sub>   <sup>2</sup>	1	V <sub>CC</sub> = 5 V, f = 500 MHz	_	- 21	_	dB
Isolation	S <sub>12</sub>   <sup>2</sup>	1	V <sub>CC</sub> = 5 V, f = 500 MHz	_	- 22	_	dB
Maximum Output Level	Po	1	$V_{CC} = 5 \text{ V, f} = 500 \text{ MHz,}$ Pin = 0 dBmW	_	2	_	dBmW

(Note 1): BW is frequency of 3 dB down from  $|S_{21}|^2$  at 500 MHz.

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#### **TEST CIRCUIT 1**

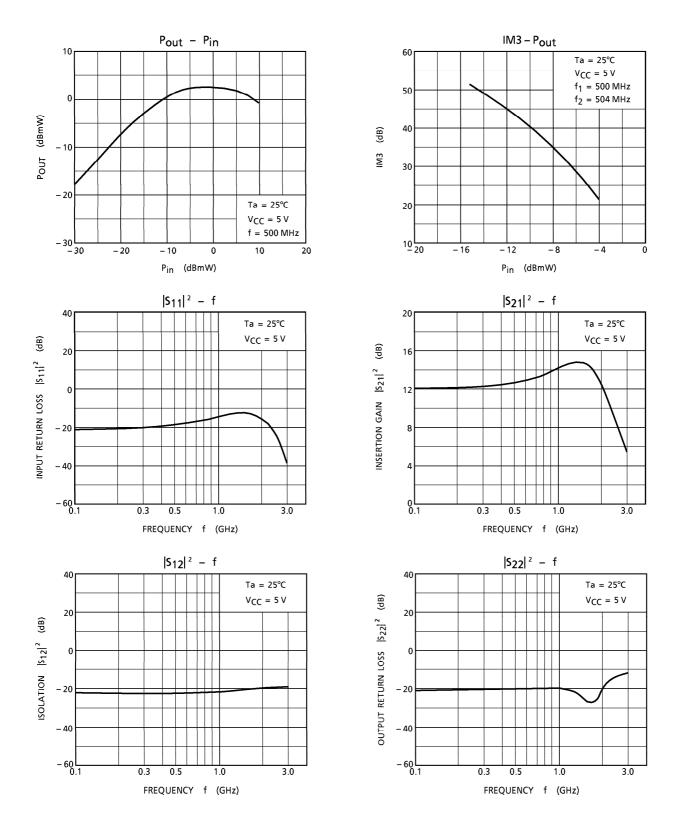


#### **NOTICE**

The circuits and measurements contained in this document are given only in the context of as examples of applications for these products.

Moreover, these example application circuits are not intended for mass production, since the high-frequency characteristics (the AC characteristics) of these devices will be affected by the external components which the customer uses, by the design of the circuit and by various other conditions. It is the responsibility of the customer to design external circuits which correctly implement the intended application, and to check the characteristics of the design.

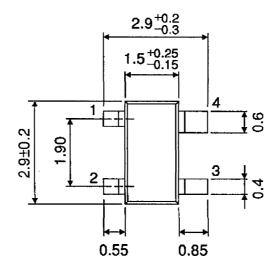
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# **PACKAGE DIMENSIONS**

ESOP4-P-1.90

Unit: mm



0.16 +0.1

Weight: 0.013 g (Typ.)