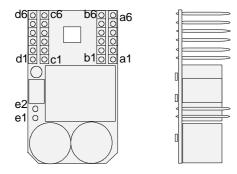
TECHNICAL DATA

BUS INTERFACE MODUL BIM M111, BIM M115



Ordering Information

BIM M111 5WG1 111 8AA01 BIM M115 5WG1 115 8AA01

Features

- EIB Bus Interface Module for piggyback-use on PCBs
- MC68HC05B6 with mask programmed ROM, contains the EIB System Software
- PEI *) and Port A, PLM A and PLM B for applications available
- Smallest instabus EIB Bus Access Unit
- Operating Temperature Range:

BIM M111: - 5 to + 45 °C BIM M115: - 25 to + 70 °C

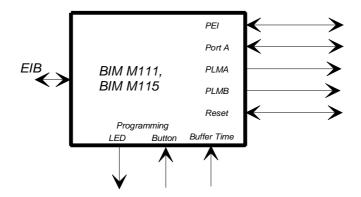
- EIBA certified part
- *) = Peripheral External Interface

DESCRIPTION

The BIM M111 or the BIM M115 are members of Siemens <u>instabus</u> EIB BCU - family. They are especially designed as small modules for piggyback use and can be fitted directly into PCBs. The modules contain all functional parts of a EIB - BCU with the exception of shift register for the serial synchronous interface. Additionally access to such microcontroller ports as Reset, PLMA, or Port A is possible. These ports are directly accessible by the user. Additional pins for the connection of an external programming button and a LED are available. For applications with the need of longer 'Bus Buffer Time' the BIM allows to connect an additional capacitance at bus voltage level.



Schematic Block Diagram



Absolute Maximum Electrical Ratings

Rating	Symbol	Value	Units
Bus Voltage	V Bus	± 35	V
Microcontroller Input Voltage PEI,	V	GND - 0,5 to Vcc + 0,5	V
PLMA, PLMB, Reset, LED, Button			
Microcontroller input or output current	I	see Motorola Technical	
per Pin		Data MC68HC05B6	

Features of the Controller

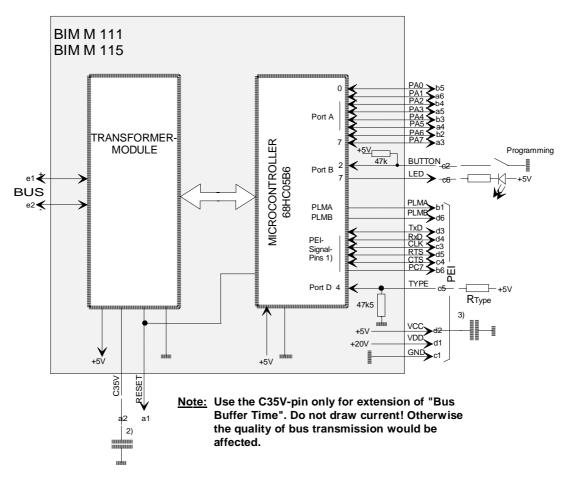
CPU MC68HC05B6

Operating Frequency
On-Chip RAM
On-Chip EEPROM
On-Chip EEPROM
2,0 MHz (crystal frequency of 4,0 MHz)
176 Bytes (18 Bytes available for user)
256 Bytes (230 Bytes available for user)

- 8-Bit A/D-converter (5 channels available for user)
- 8-Bit pulse length modulator (PLM)
- Serial asynchronous communication interface
- Serial synchronous communication interface
- · Watch dog



HARDWARE BLOCK DIAGRAM



¹⁾ For detailed description see "PEI" - table.

Memory Map

Address	s Range	Memory	Bytes
0000	001F	I/O, Register	32
0020	004F	ROM	48
0050	00BF	RAM	176
00C0	00FF	Stack	64
0100	0100	OPTR	1
0101	011F	EEPROM non protected	31
0120	01FF	EEPROM protected	224
0200	1FF1	ROM and unused areas	
1FF2	1FFF	Special functions	

 $^{^{2)}}$ Additional capacitance (Vc=35V) for extended "Bus Buffer Time", e.g.100 $\mu F.$

 $^{^{3)}}$ Additional capacitance (Vc=5V) for extended "Bus Buffer Time"., e.g. $470\mu F.$



PEI (Physical External Interface)

PEI - Pin - Assignment

	Pin	Power	Serial	I - Ports	Digital I /O	Remarks
	Mnemonics	Supply	Interface	or A / D –	- Ports	
			Async./sync.	Ports		
a1	RESET				_RESET	In-/Output
a2	C35V					Buffer Capacitor
a3	PA7				PA7	Digital Input / Output
a4	PA5				PA5	Digital Input / Output
a5	PA3				PA3	Digital Input / Output
a6	PA1				PA1	Digital Input / Output
b1	PLMA				PLMB	Pulse-Length-Mod.
b2	PA6				PA6	Digital Input / Output
b3	PA4				PA4	Digital Input / Output
b4	PA2				PA2	Digital Input / Output
b5	PA0				PA0	Digital Input / Output
b6	PC7				PC7	Digital Output (Input /
						Output when PEI-Type17)
c1	GND	Ground				
c2	BUTTON				PB2	Digital Input
сЗ	CLK		SCLK	PD6	PC4	Clock •→
c4	CTS			PD3	PC6	Clear to Send •←
с5	TYPE			PD4		PEI - Type
с6	LED				PB7	Digital Output
d1	VDD	Vdd				2mA Current Limit.
		(20V)				
d2	VCC	Vcc (5V)				
d3	TxD		TDO	PD5	PC3	Data •→
d4	RxD		RDI	PD7	PC2	Data •←
d5	RTS			PD2	PC5	Request to Send •→
d6	PLMB				PLMB	Pulse-Length-Mod.
e1	+ Bus					Bus Line
e2	- Bus					Bus Line



ELECTRICAL SPECIFICATION

See also EIBA Handbook for Development, Release 3.0

Bus Interface Characteristics:

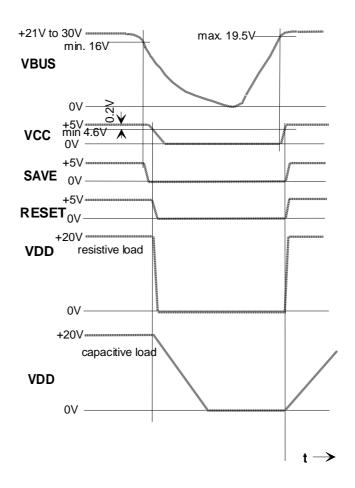
Characteristics	Symbol	Min	Max	Typical	Unit	Remarks
Operating Voltage	V _{Bus}	21	30		V	
Current consumption	I _{Bus}			4	mA	
Reset conditions	Vcc	< 4,6			V	Reset generated by transceiver
BCU-Buffer-Time		TBD			ms	PEI-Load:50mW
Transmission Rate				9600	bit/s	

PEI DC-Characteristics:

Characteristics	Symbol	Min	Max	Typical	Unit	Remarks
Supply Output	VCC	4.7	5.3		V	Max. 10mA
Voltage +5V						
Supply Output	VDD	18	20.5		V	load = 2mA
Voltage +20V						VBUS - VDD
						> 1,5V
						VBUS = 21V
						30V
Current Limitation	IDD	2	5		mA	
Data Output Voltage	VOL		0.4		V	Iload=1,6mA
Port A, Port B, Port C	VOH	VCC-0,8				Iload=0,8mA
Data Output Voltage	VOL		0.4		V	Iload=1,6mA
TDO, SCLK, PLMB	VOH	VCC-0,8				Iload=1,6mA
Data Output Voltage	VOL		1,0		V	Iload=1,6mA
Reset						
Data Input Voltage	VIL	0	0.2 VCC		V	
Port A, Port B, Port C	VIH	0.7 VCC	VCC			
Reset, RDI						
Analog Input Voltage	VAIL	0			V	
Range	VAIH		VCC			
I/O Ports Three-State-	IOZ		±10		μΑ	
Leakage						
Input Capacitance	CIN		50		pF	

Timing

The timing of Save, Reset, VCC and VDD which are depending on VBUS are shown in the following depiction:



Environmental Conditions

Rating	Symbol	Value	Unit	Remarks
Operation Temperature Range BIM M111	TA	- 5 to +45	°C	no condensation
Operation Temperature Range BIM M115	TA	- 25 to + 70	°C	no condensation
Storage Temperature Range BIM M111	TSTG	- 25 to + 55	°C	
Storage Temperature Range BIM M115	TSTG	-25 to +70	°C	
Relative Humidity		5 to 93	%	no condensation



SOFTWARE SPECIFICATION

See also EIBA Handbook for Development, Release 3.0

Application Hint

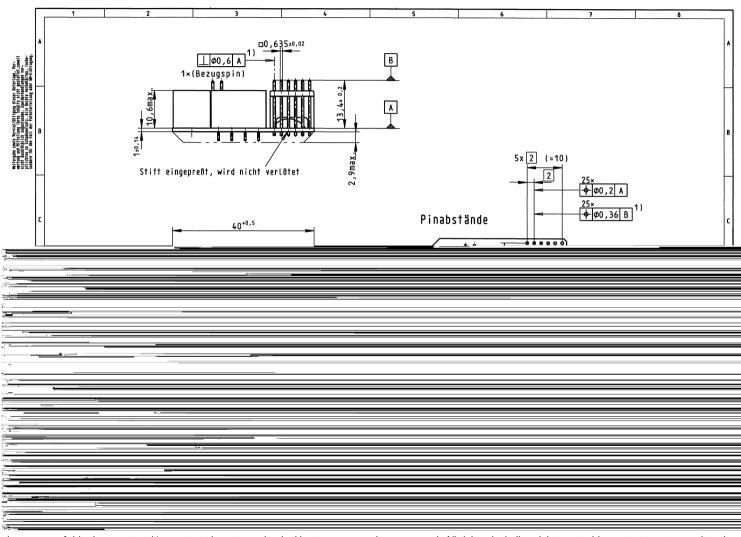
To avoid malfunctioning by EMI, it is recommended to shield the BIM. The connection for the electric screen is shown in the figure below.



Connection for electric screen (= Ground), for information about the exact position see "Maßbild"

The data contained herein are subject to change without notice. Siemens does not warrant for correctness or completeness of the documentation or that the products described are qualified for certification.

MECHANICAL SPECIFICATION (BIM M111)



MECHANICAL SPECIFICATION (BIM M115)

