

PM5348

S/UNI-155 DUAL

SATURN USER NETWORK INTERFACE

DATASHEET ERRATA

ISSUE 2: JUNE 2001

REVISION HISTORY

Issue No.	Issue Date	Details of Change
2	June 2001	This document contains changes to the datasheet revision 7. Specifically, the behavior of TXPRTYI bits in split mode and the removal of a reference to a non-existing application note. .
1	Oct 1999	This document contains changes to the datasheet revision 7.

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1 ISSUE 1 ERRATA

This issue 2 errata notifies that changes have been made to the issue 7 of the PMC-1950919 data sheet. The issue 7 datasheet and issue 2 errata supersede all prior editions and versions.

2 BIT ERROR RATE MONITOR

The following table on Page 146 of the S/UNI-DUAL datasheet has been updated with the latest information.

STS-n	Bit Error Rate	SMODE bit	CMODE bit	Sample Accumulation Period	Declare Threshold	Clear Threshold
1	10 ⁻³	0	0	0x000008	0x0BA	0x0DF
1	10 ⁻⁴	0	0	0x000028	0x0A7	0x0D4
1	10 ⁻⁵	0	0	0x00012C	0x084	0x0AD
1	10 ⁻⁶	0	0	0x000BB8	0x085	0x0AF
1	10 ⁻⁷	0	0	0x007530	0x085	0x0AF
1	10 ⁻⁸	0	0	0x03D090	0x06D	0x093
1	10 ⁻⁹	0	0	0x1E8480	0x055	0x078
3	10 ⁻³	0	0	0x000008	0x245	0x286
3	10 ⁻⁴	0	0	0x00000D	0x0A3	0x0CF
3	10 ⁻⁵	0	0	0x000064	0x084	0x0AD
3	10 ⁻⁶	0	0	0x0003E8	0x085	0x0AF
3	10 ⁻⁷	0	0	0x002710	0x085	0x0AF
3	10 ⁻⁸	0	0	0x014438	0x06D	0x093
3	10 ⁻⁹	0	0	0x0A2D78	0x055	0x078

This new table below lists the recommended content for the BERM registers for different speeds (STS-N) and BERs.

STS	declare BER	Eval Per (s)	SF/SD SMODE	SF/SD CMODE	SF/SD SAP	SF/SD DTH	SF/SD CTH
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1	10-3	0.008	0	1	0x000008	0x0BA	0x14B
1	10-4	0.040	0	1	0x000028	0x0A7	0x0B8
1	10-5	0.300	0	1	0x00012C	0x084	0x08E
1	10-6	3.000	0	1	0x000BB8	0x085	0x08E
1	10-7	30.000	0	1	0x007530	0x085	0x08E
1	10-8	250.000	0	1	0x03D090	0x06D	0x078
1	10-9	2000.000	0	1	0x1E8480	0x055	0x061
3	10-3	0.008	0	0	0x000008	0x245	0x083
3	10-4	0.013	0	1	0x00000D	0x0A3	0x0B4
3	10-5	0.100	0	1	0x000064	0x084	0x08E
3	10-6	1.000	0	1	0x0003E8	0x085	0x08E
3	10-7	10.000	0	1	0x002710	0x085	0x08E
3	10-8	83.000	0	1	0x014438	0x06D	0x077
3	10-9	667.000	0	1	0x0A2D78	0x055	0x061

3 ORDERING AND THERMAL INFORMATION

The following table on page 221 incorrectly lists the S/UNI-DUAL with an RI suffix.

PART NO.	DESCRIPTION		
PM5348-RI	160 Pin Metric Quad Flat Pack (MQFP)		

PART NO.	AMBIENT TEMPERATURE	Theta Ja	Theta Jc
PM5348-RI	0°C to 70°C	27 °C/W	11 °C/W

The correct -RC suffix is listed below.

PART NO.	DESCRIPTION		
PM5348-RC	160 Pin Plastic Quad Flat Pack (PQFP)		

PART NO.	AMBIENT TEMPERATURE	Theta Ja	Theta Jc
PM5348-RC	0°C to 70°C	27 °C/W	11 °C/W

4 BEHAVIOUR OF TXPRTY BITS IN SPLIT BUS MODE

TXPRTYI[1:0]:

The TXPRTYI[1:0] bits indicate if a parity error was detected on the TDAT bus. These bits are cleared when this register is read. Odd or even parity is selected using the TXPTYP bit.

If in 8 bit bus mode or split bit mode, a parity error detected on the inputs TDAT[7:0] and TXPRTY[0] sets TXPRTYI[0] **of register 0x63** to a logic 1. If in split bus mode a parity error detected on TDAT[15:8] and TXPRTY[1] sets ~~TXPRTYI[1]~~ **TXPRTYI[0] of register 0xE3** to a logic 1.

If in 16 bit bus mode, a parity error detected on the inputs TDAT[15:0] and TXPRTY[1] sets TXPRTYI[1] to a logic 1. The TXPRTY[0] input is ignored.

5 REMOVAL OF DOCUMENT REFERENCE

~~Additional analysis of jitter transfer and jitter tolerance issues for the S/UNI-DUAL can be found in PMC's ATM Design Notes "Meeting SONET/SDH WAN Interface Jitter Transfer Requirements with the S/UNI-DUAL", PMC 950139~~

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